

FIG. 1A

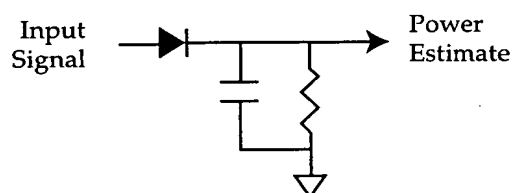


FIG. 1B

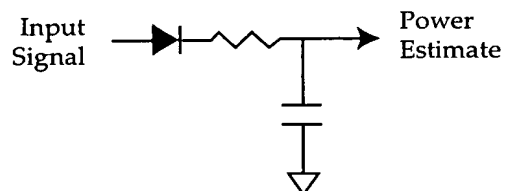


FIG. 1C



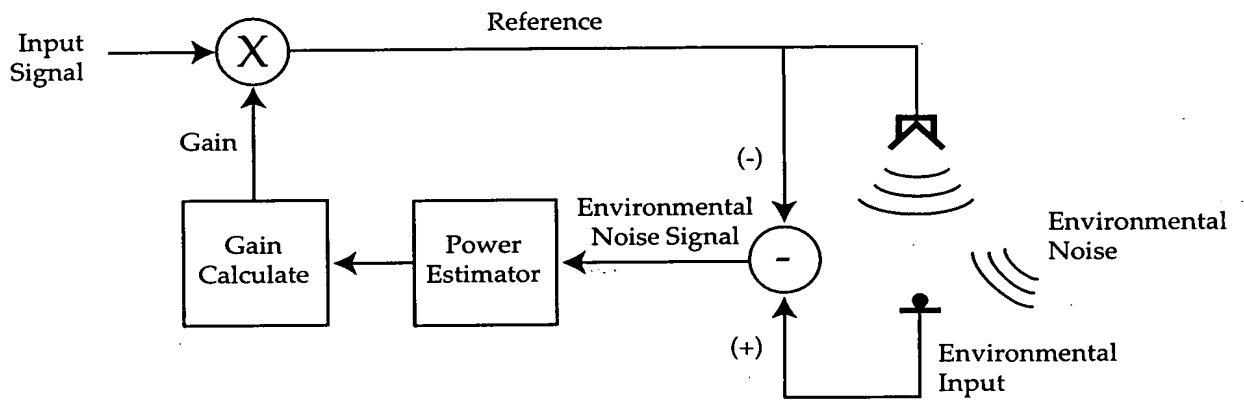


FIG. 2

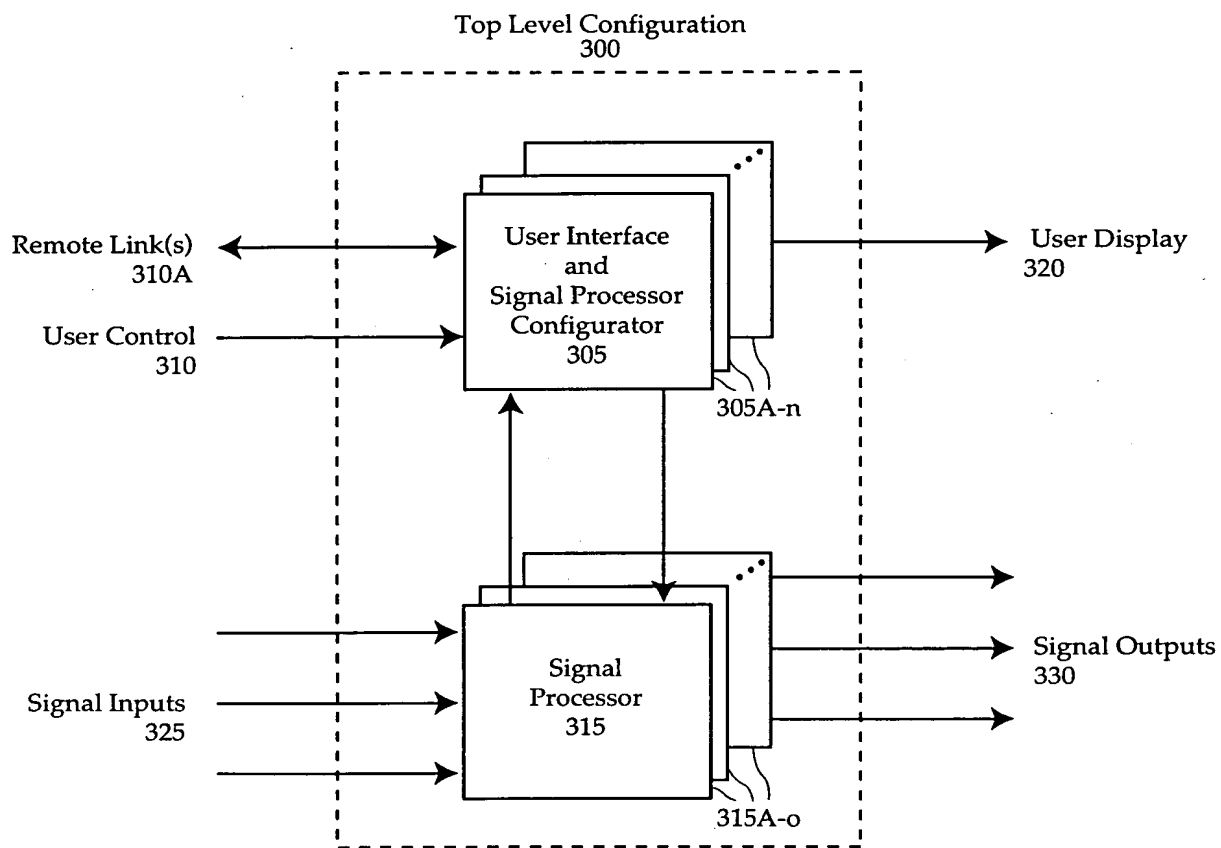


FIG. 3A

00627T-5282/60

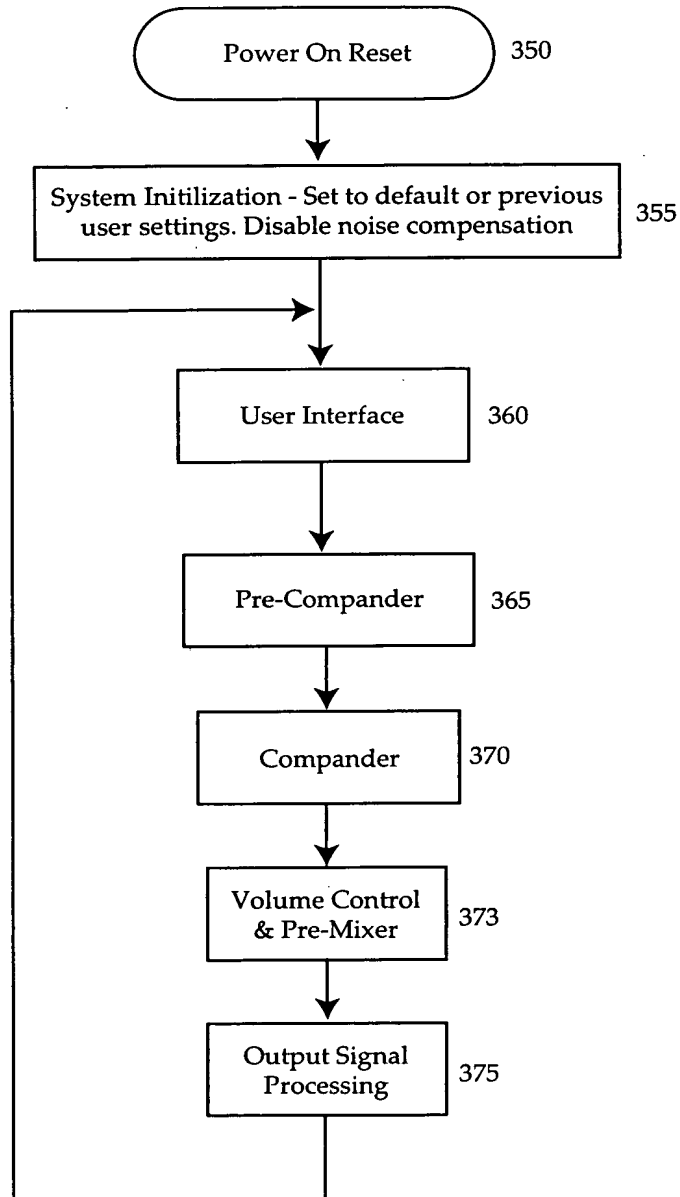


FIG. 3B

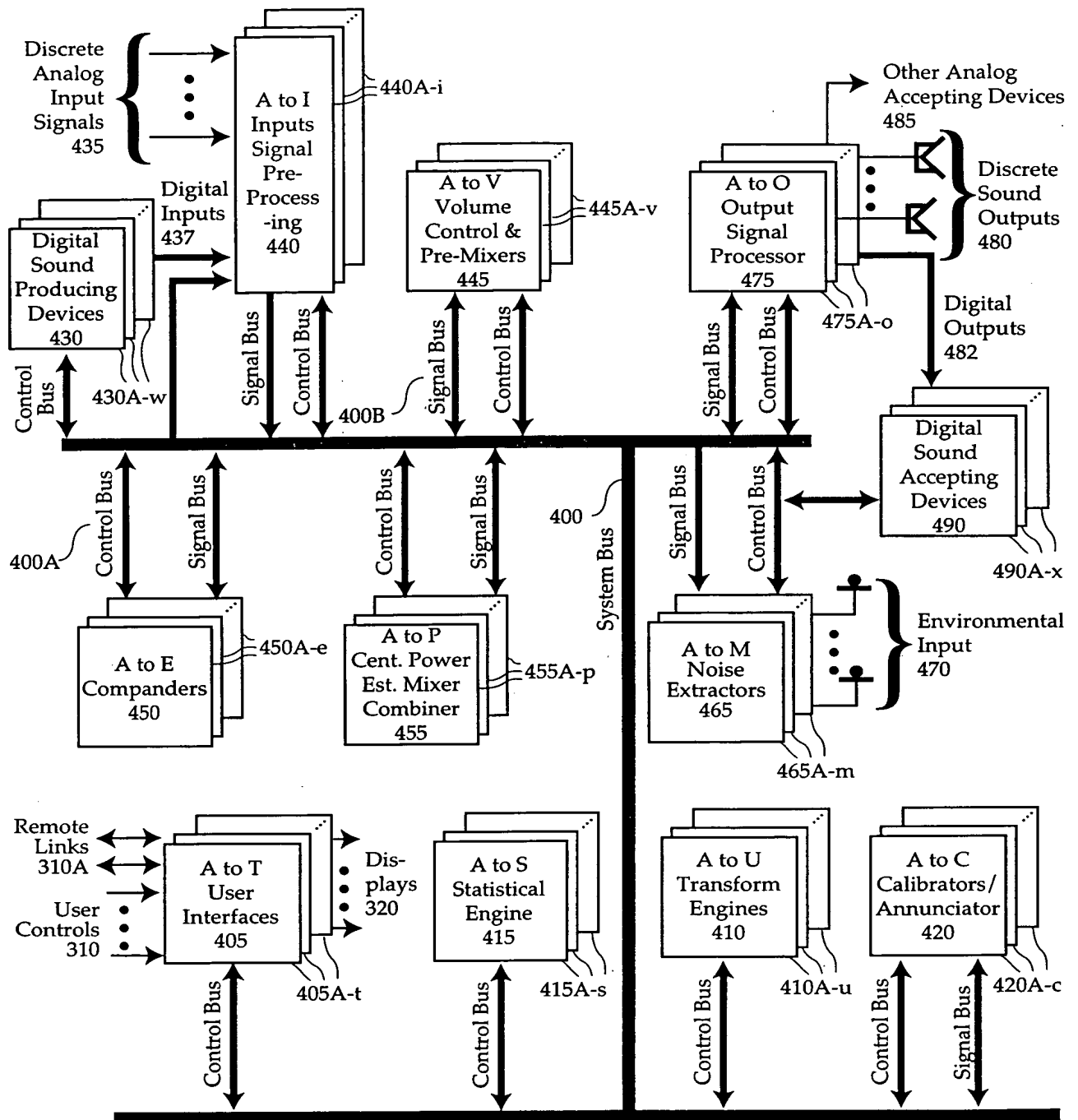


FIG. 4

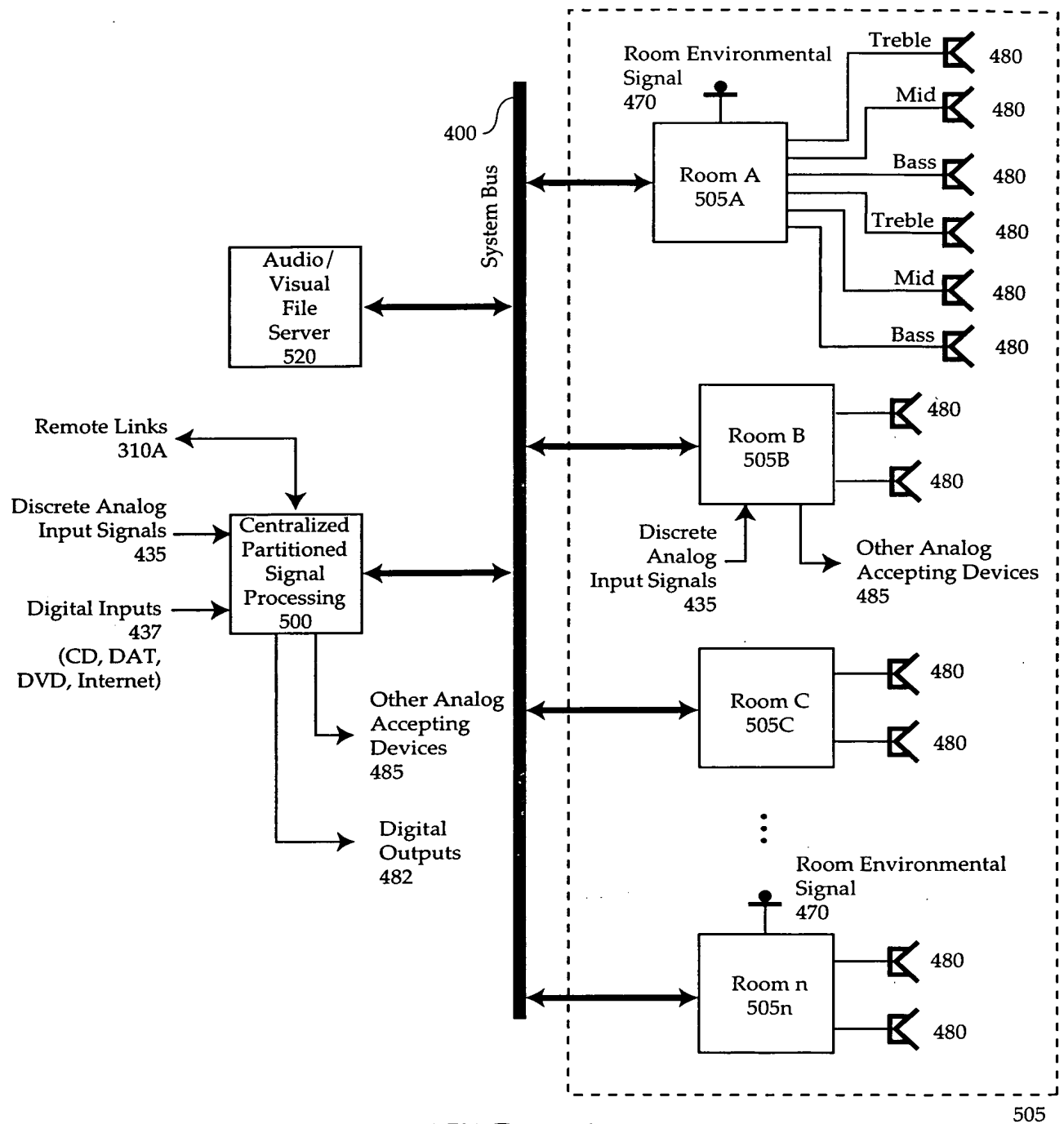


FIG. 5A

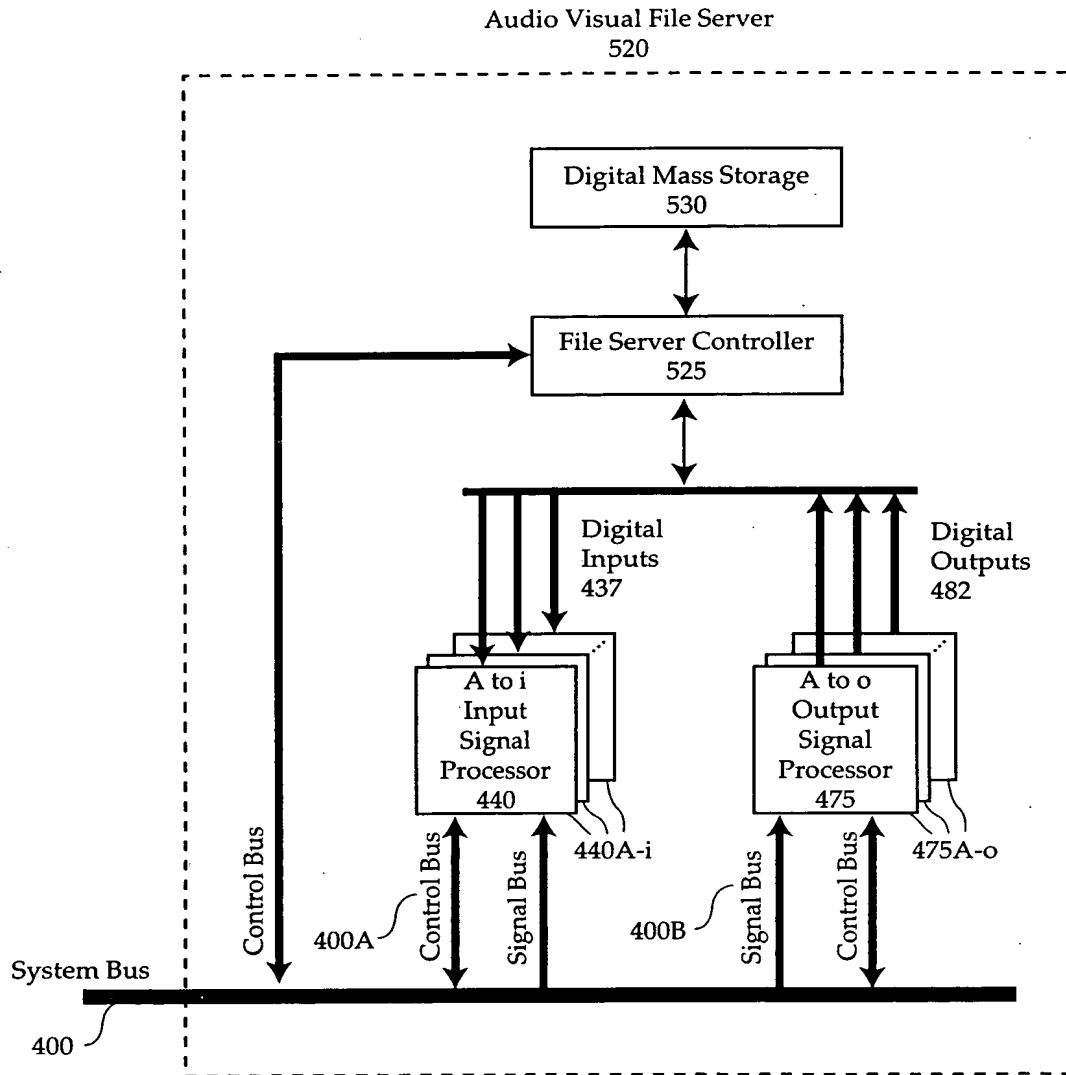


FIG. 5B

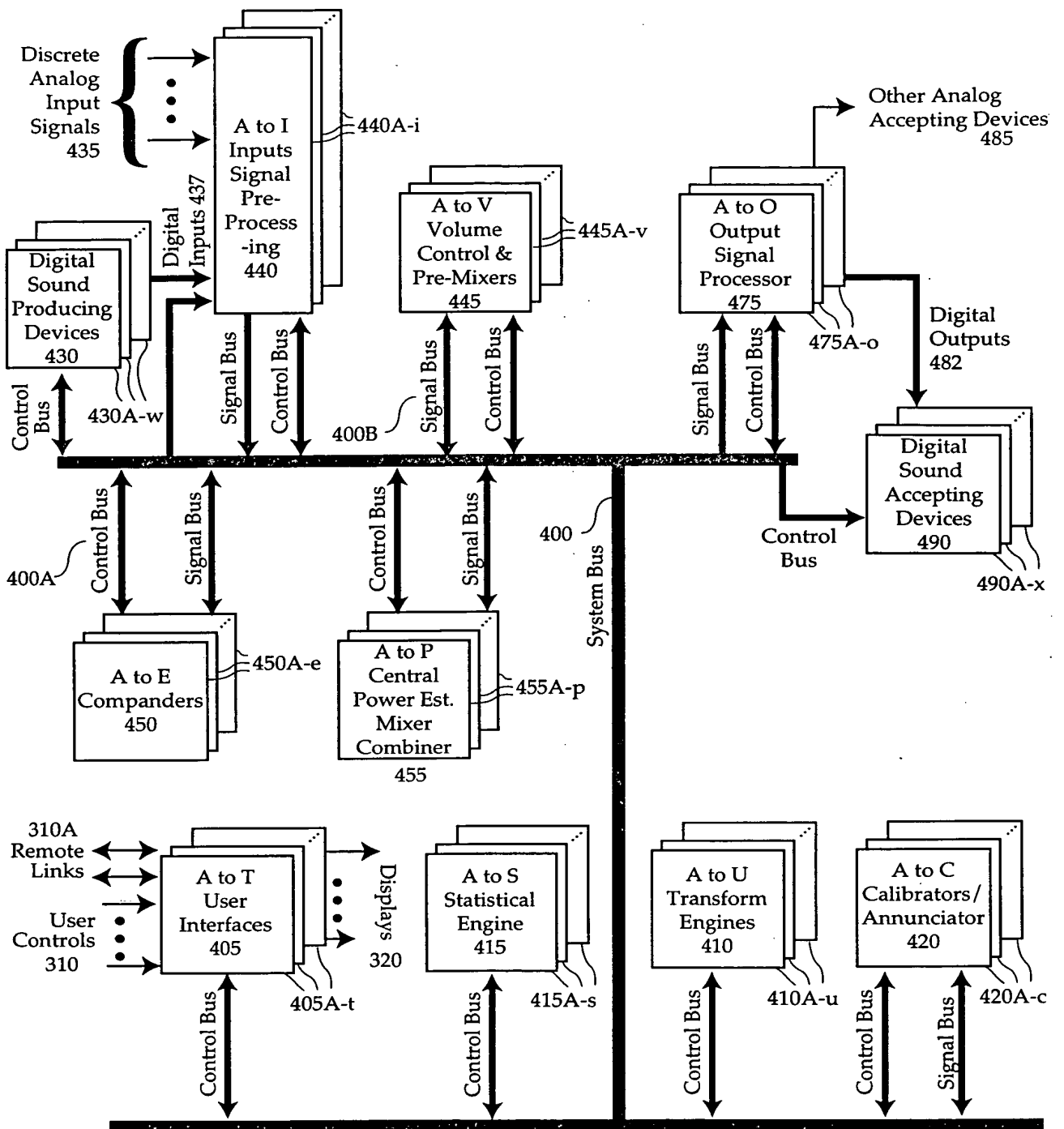


FIG. 5C

0000011-00000000

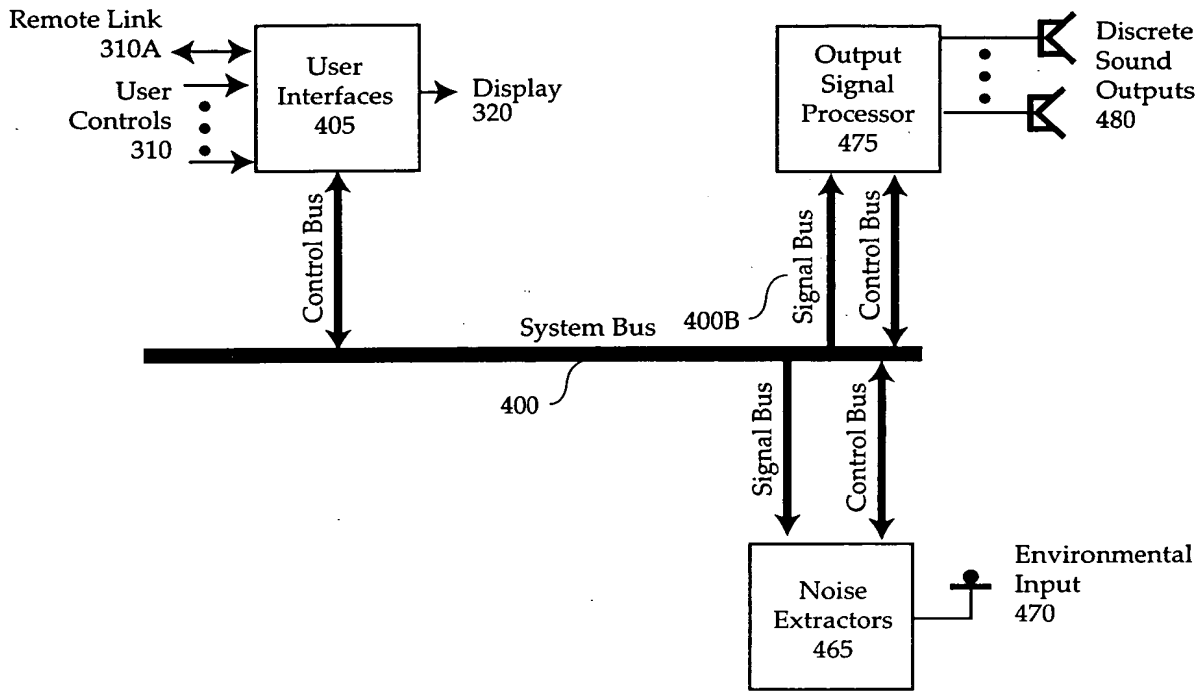


FIG. 5D

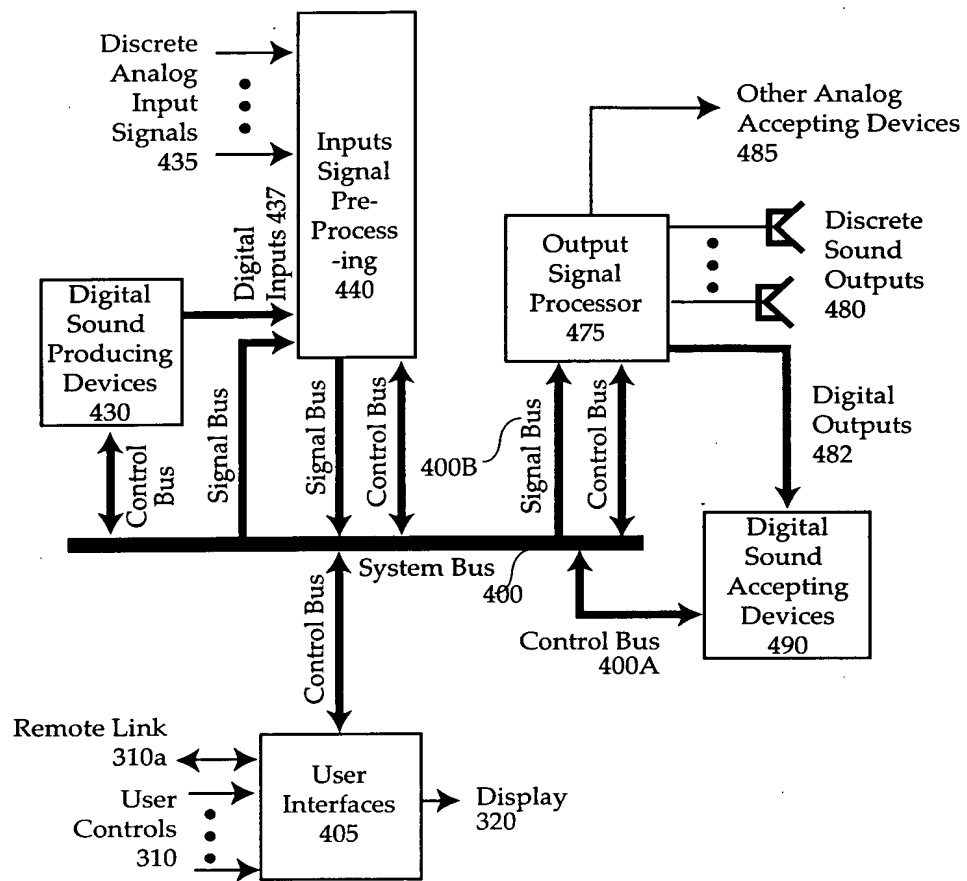


FIG. 5E

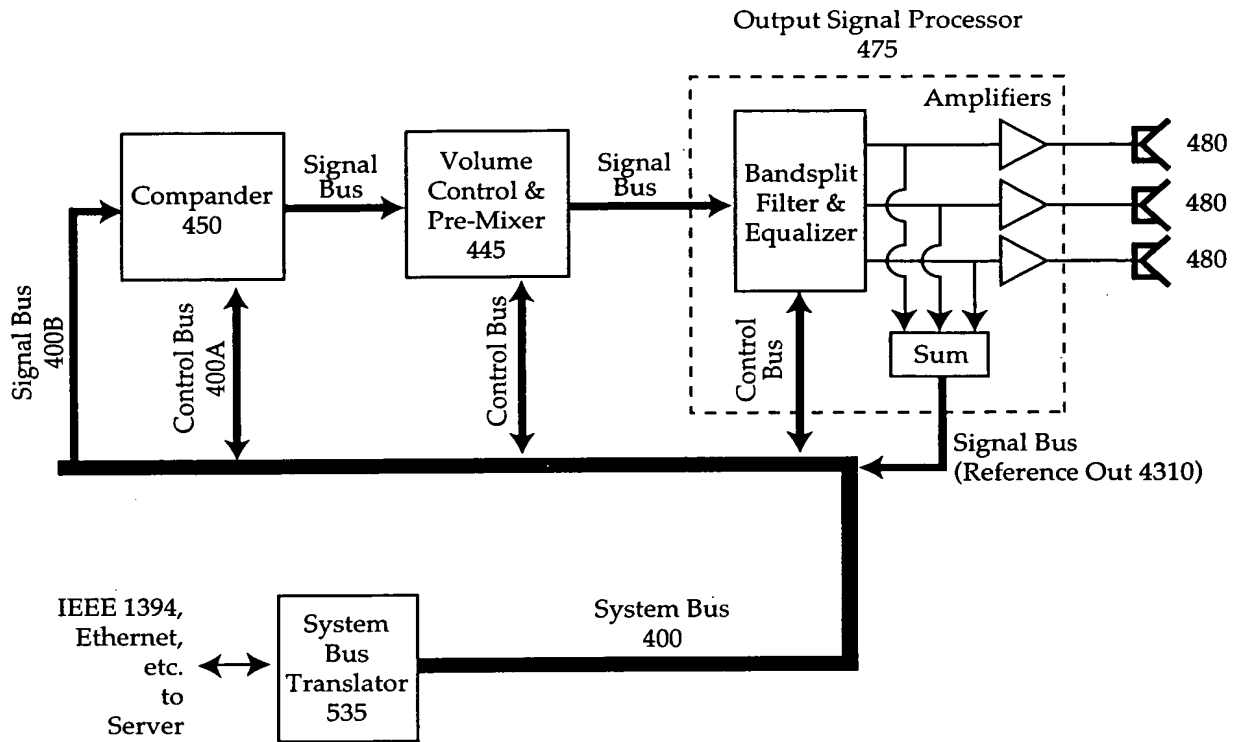


FIG. 5F

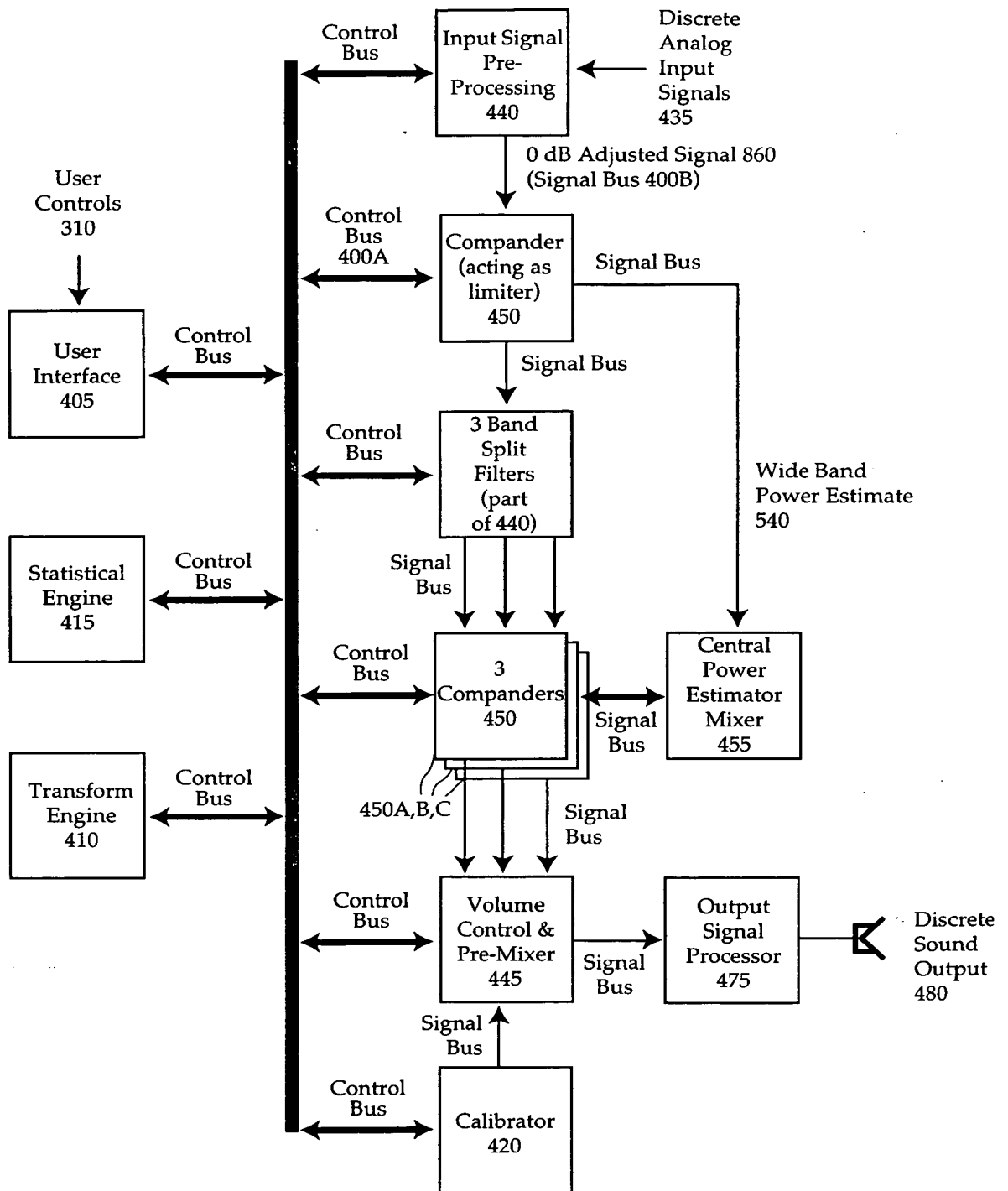


FIG. 5G

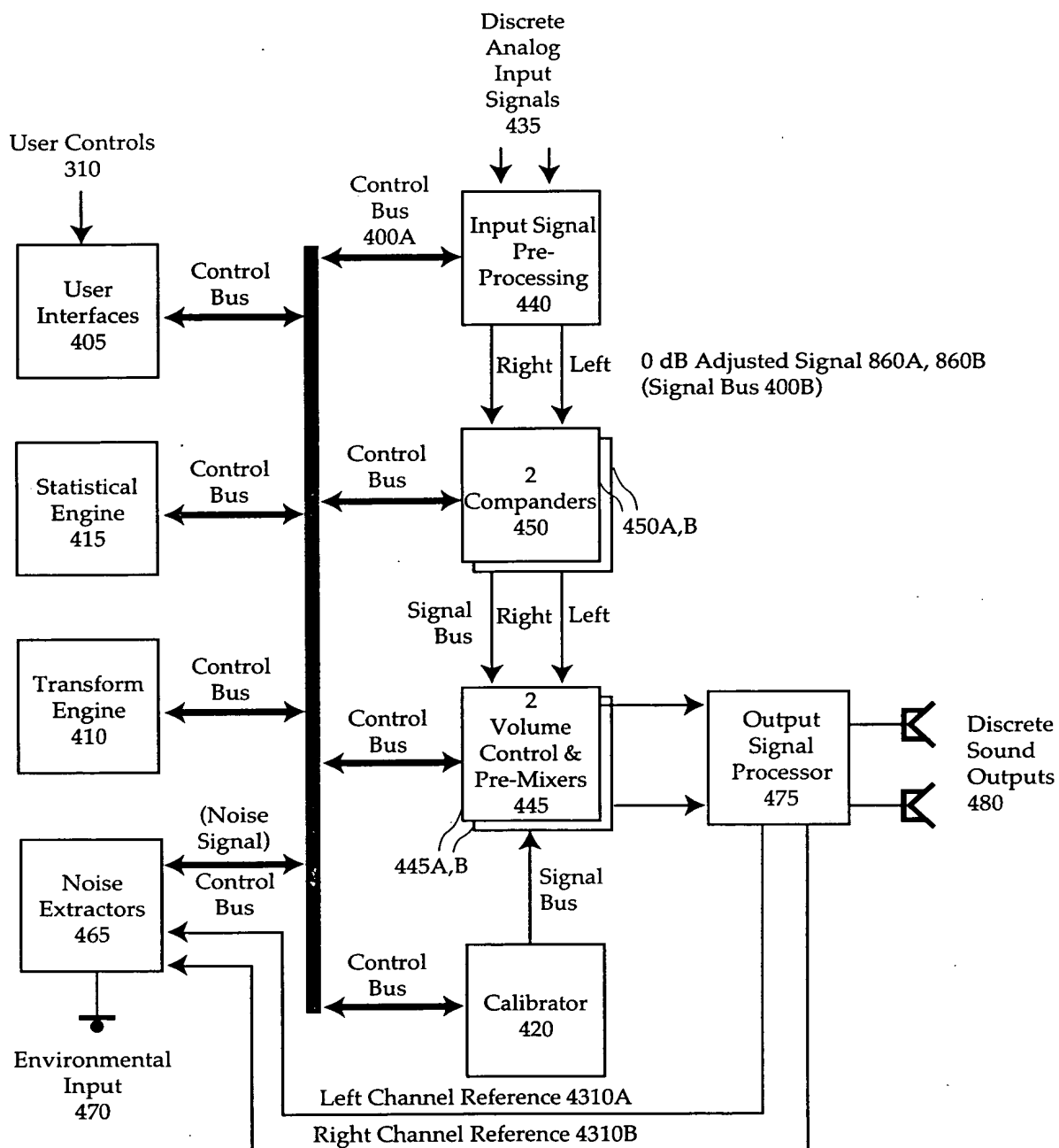


FIG. 5H

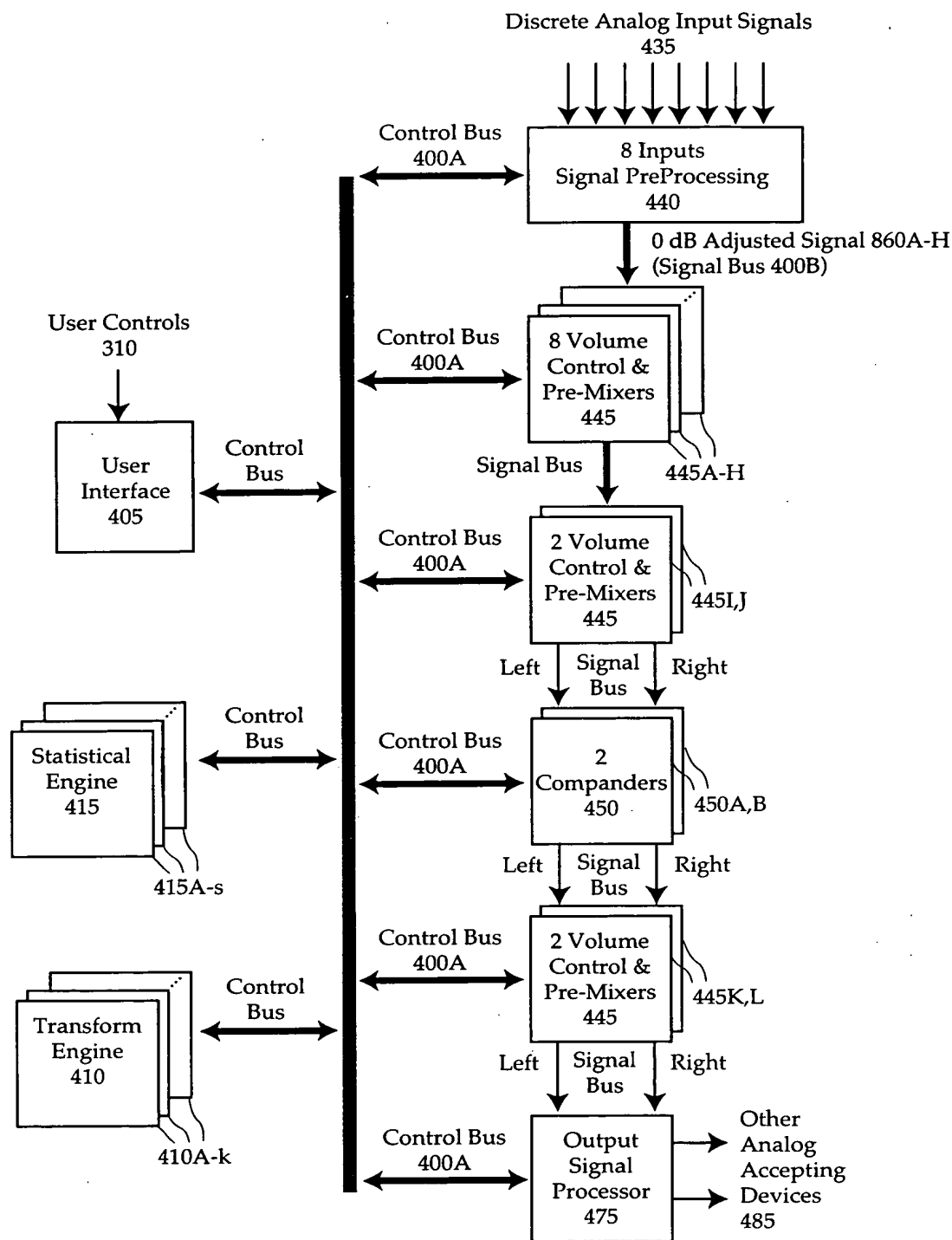


FIG. 5I

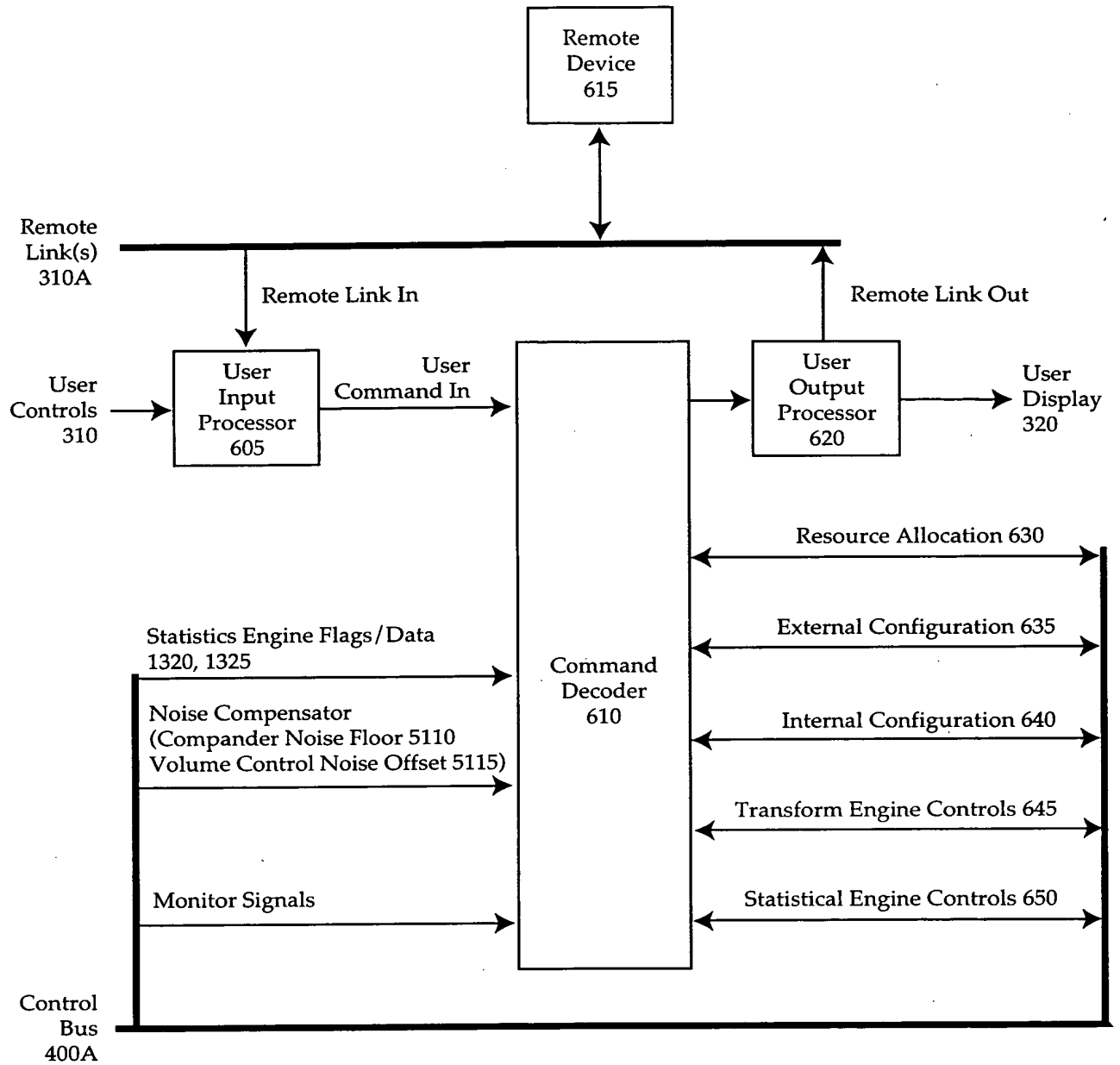


FIG. 6A

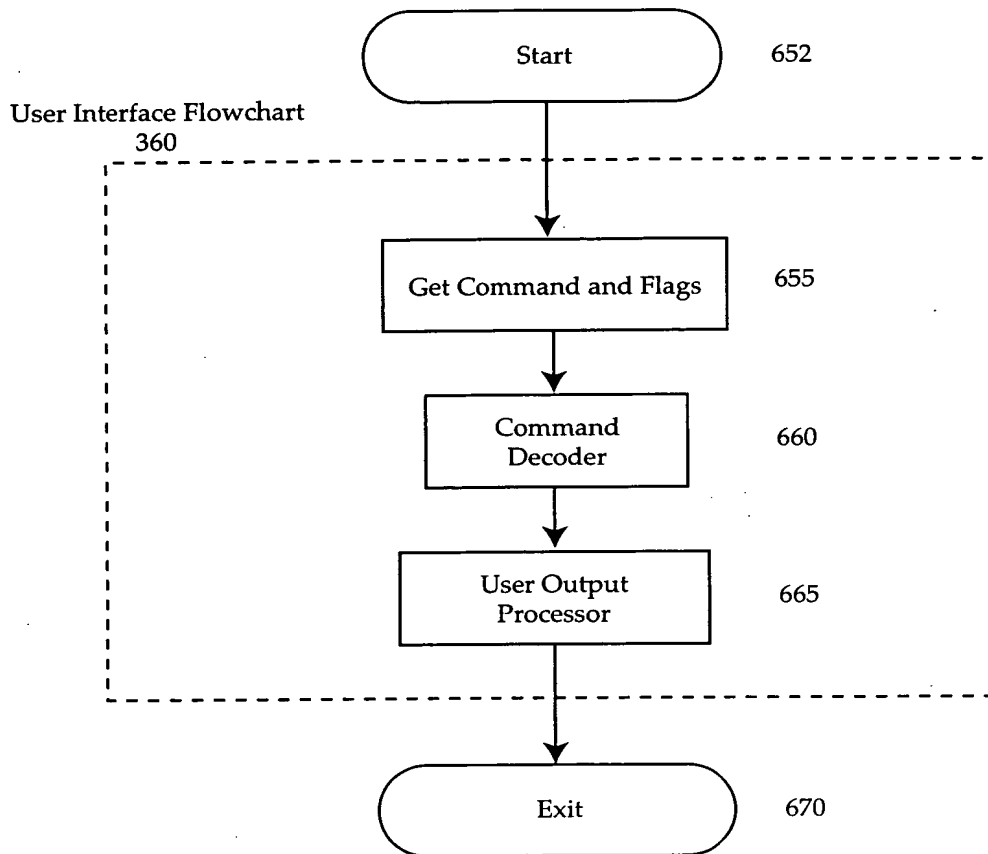


FIG. 6B

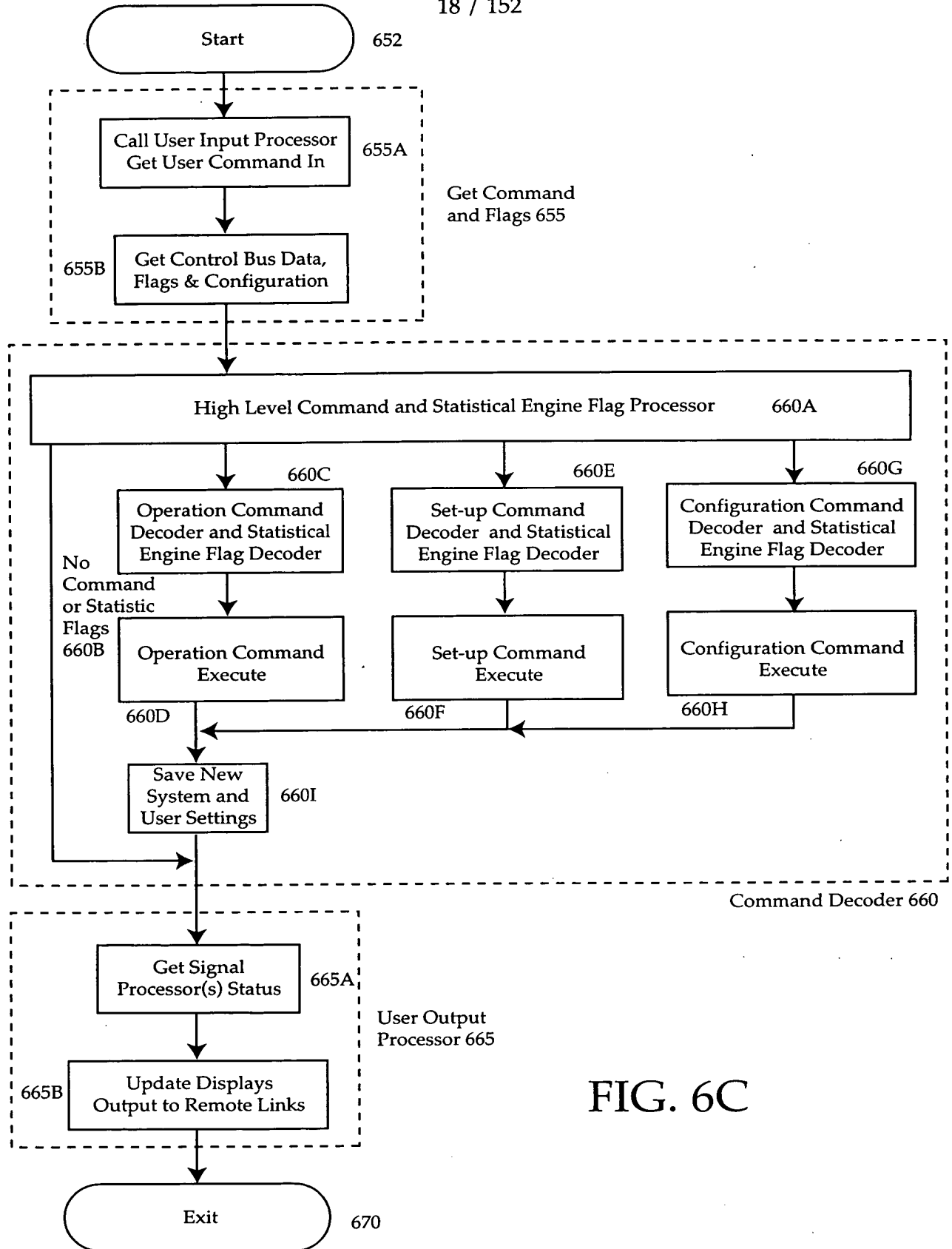


FIG. 6C

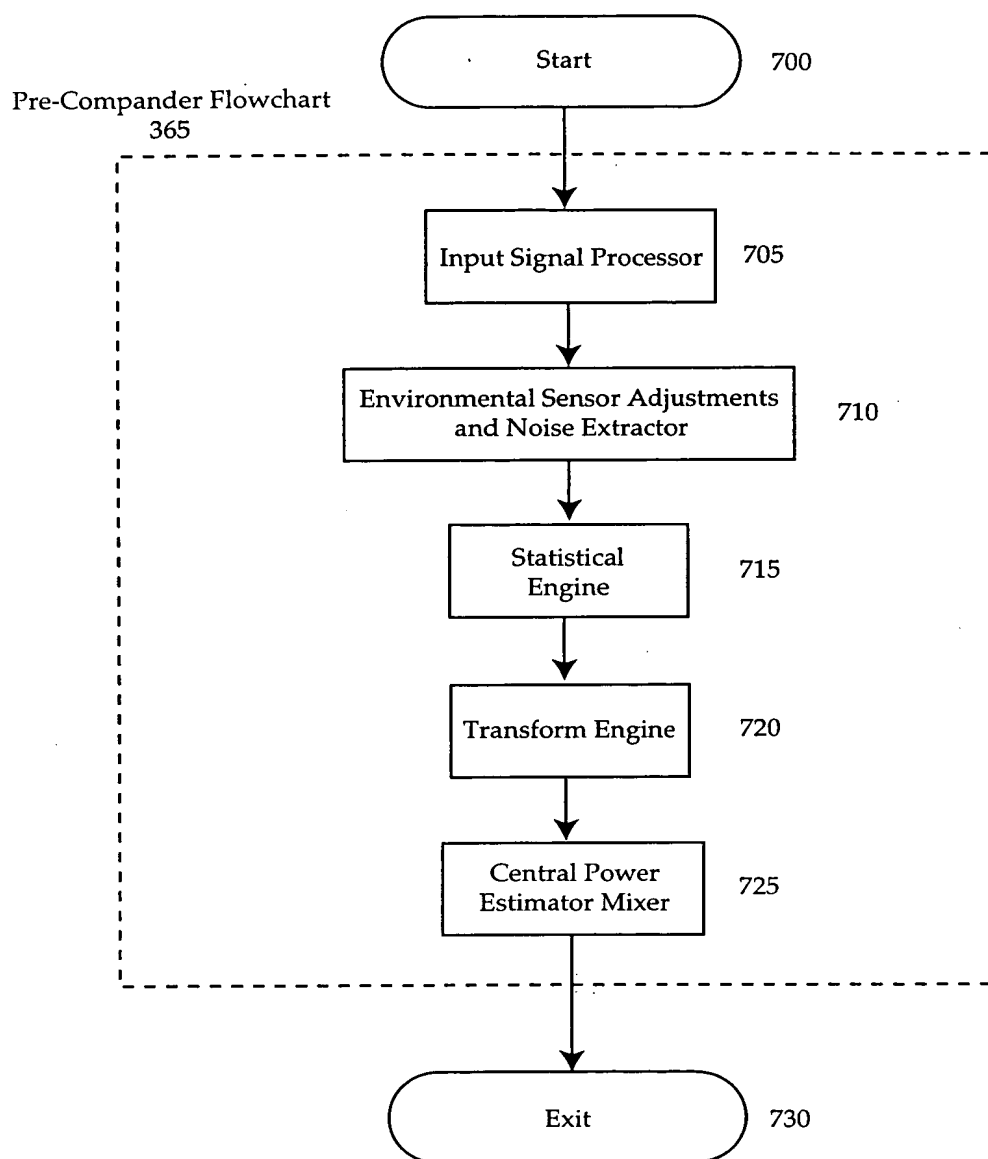
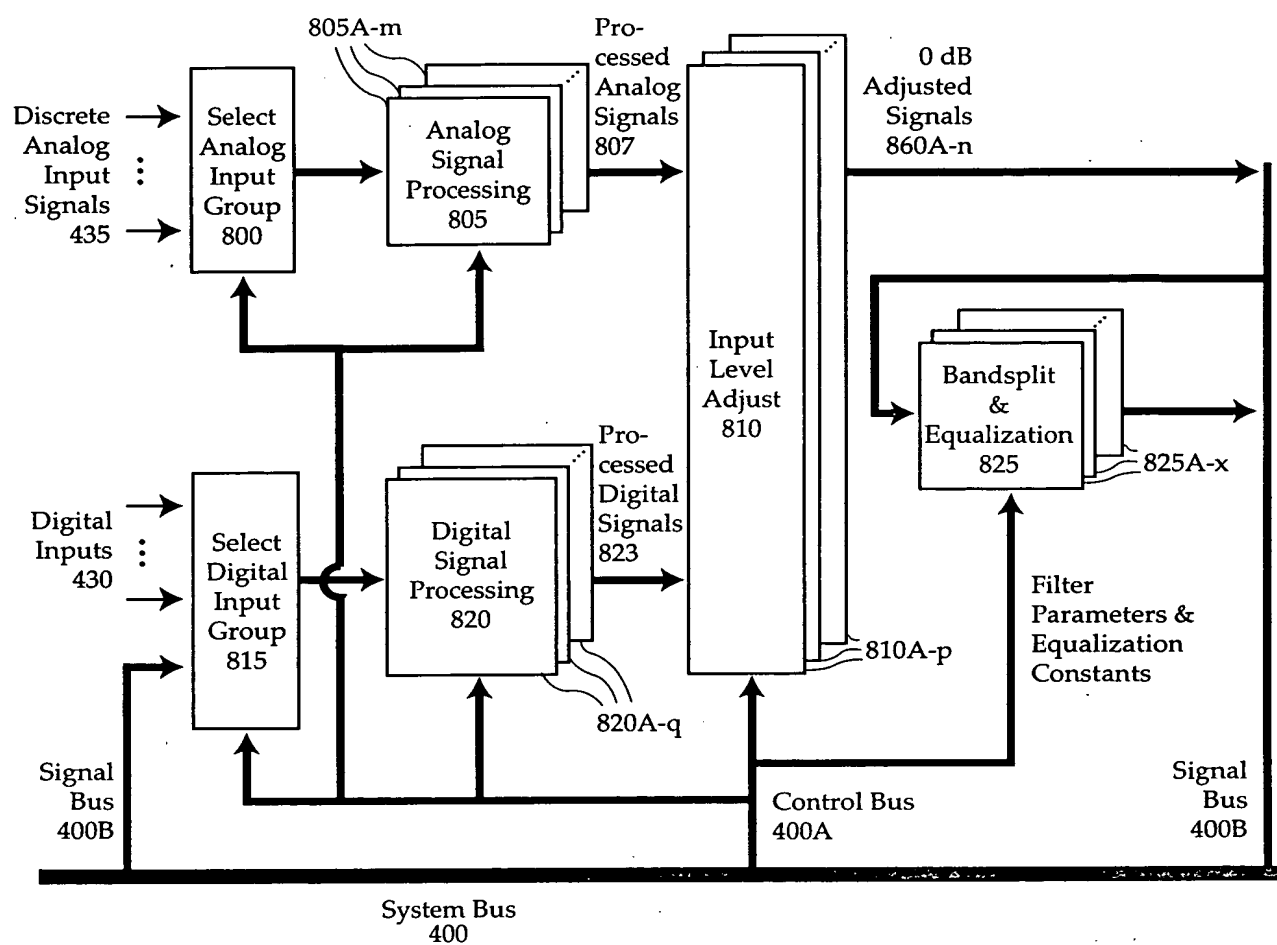


FIG. 7



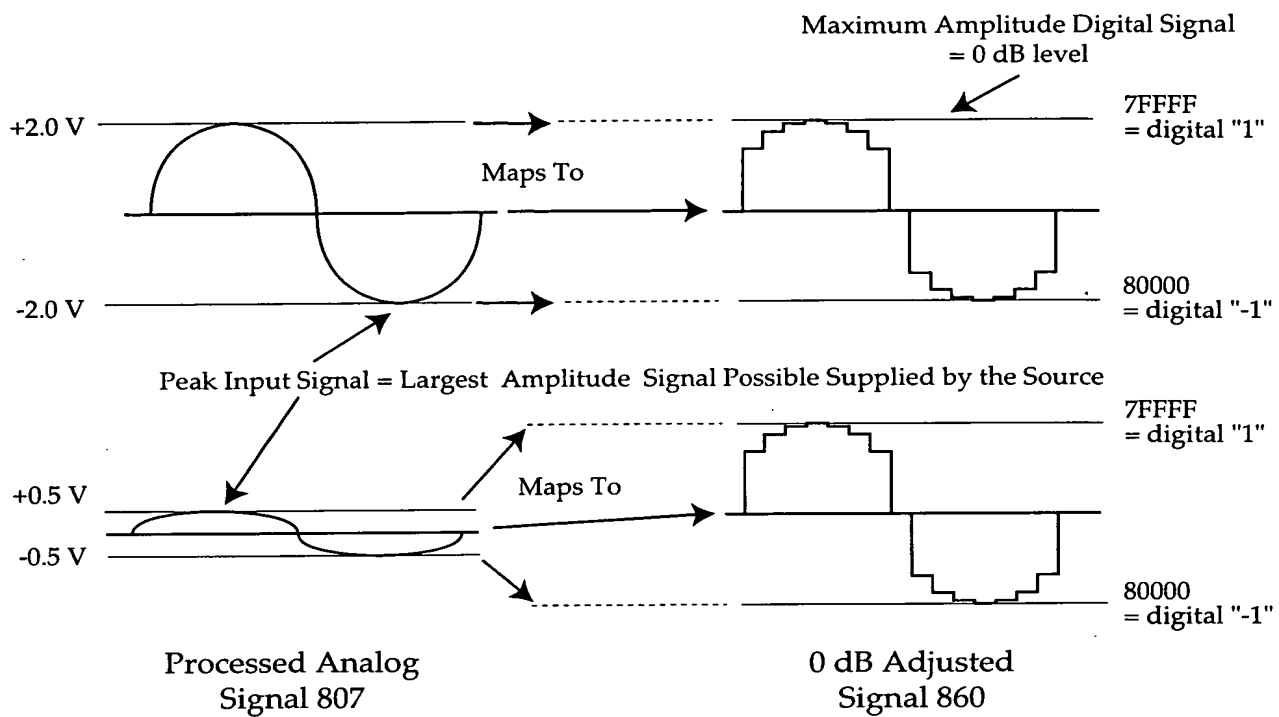


FIG. 8B

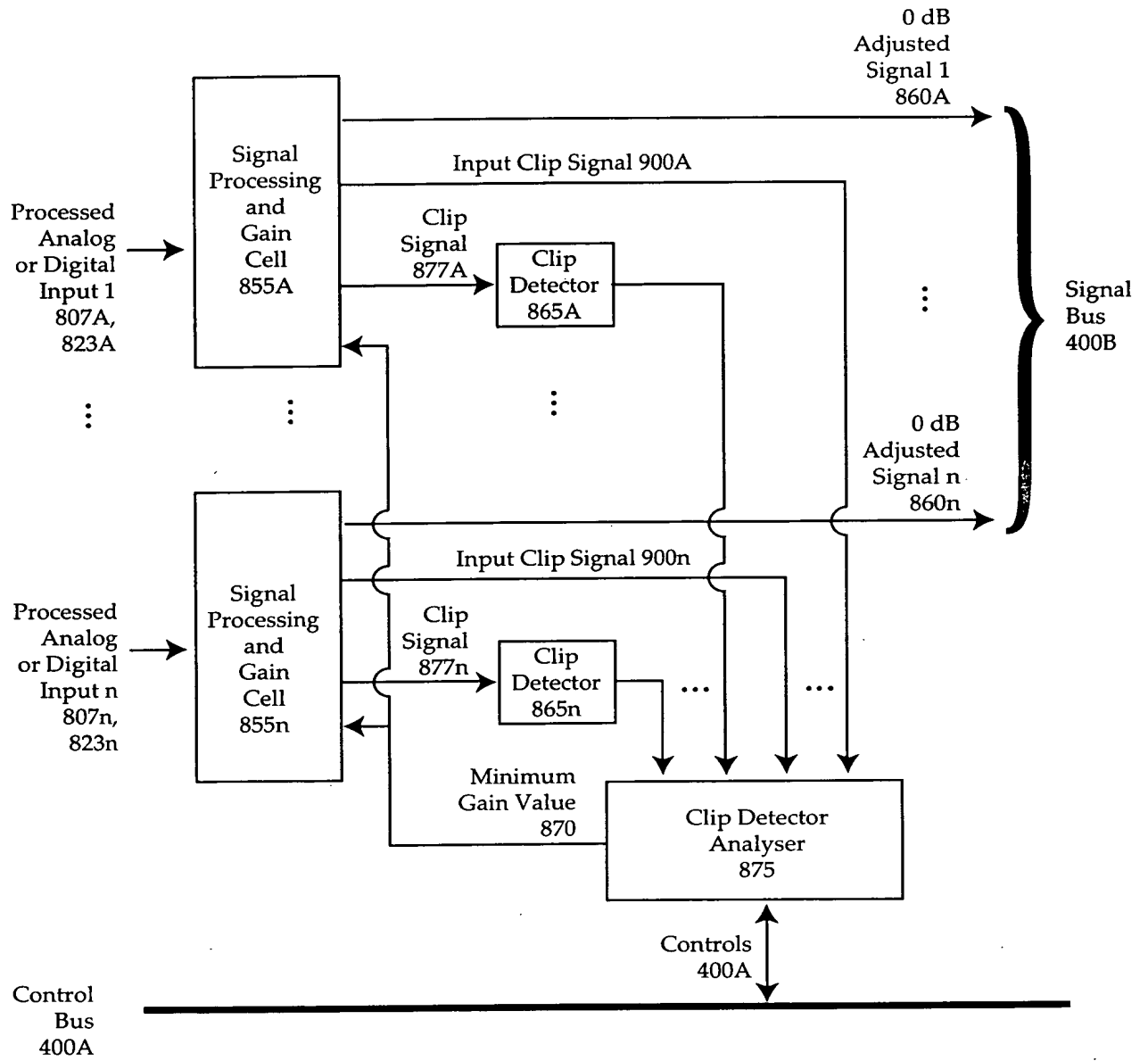


FIG. 8C

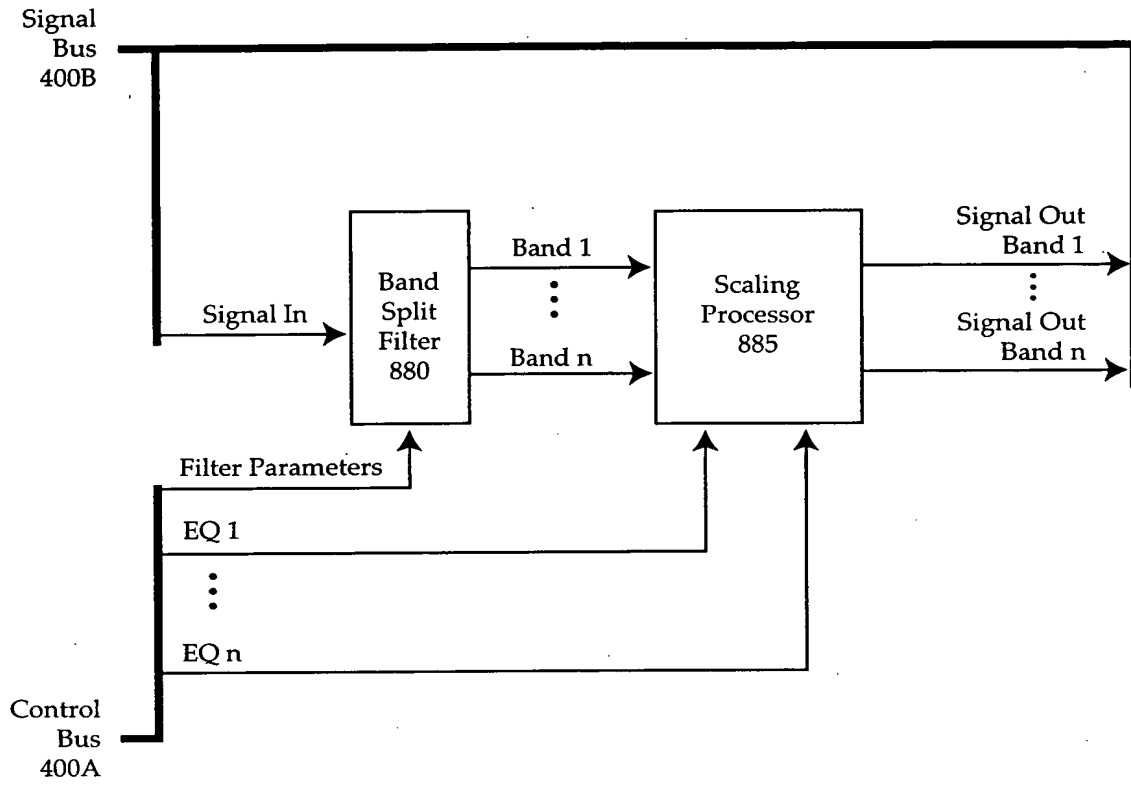


FIG. 8D

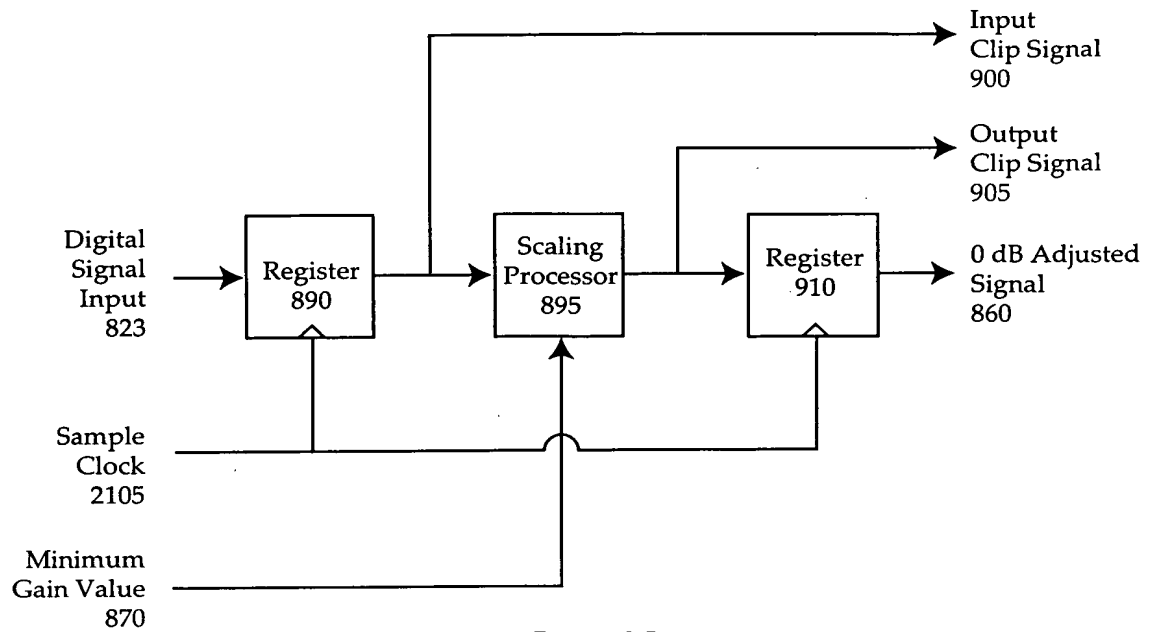


FIG. 8E

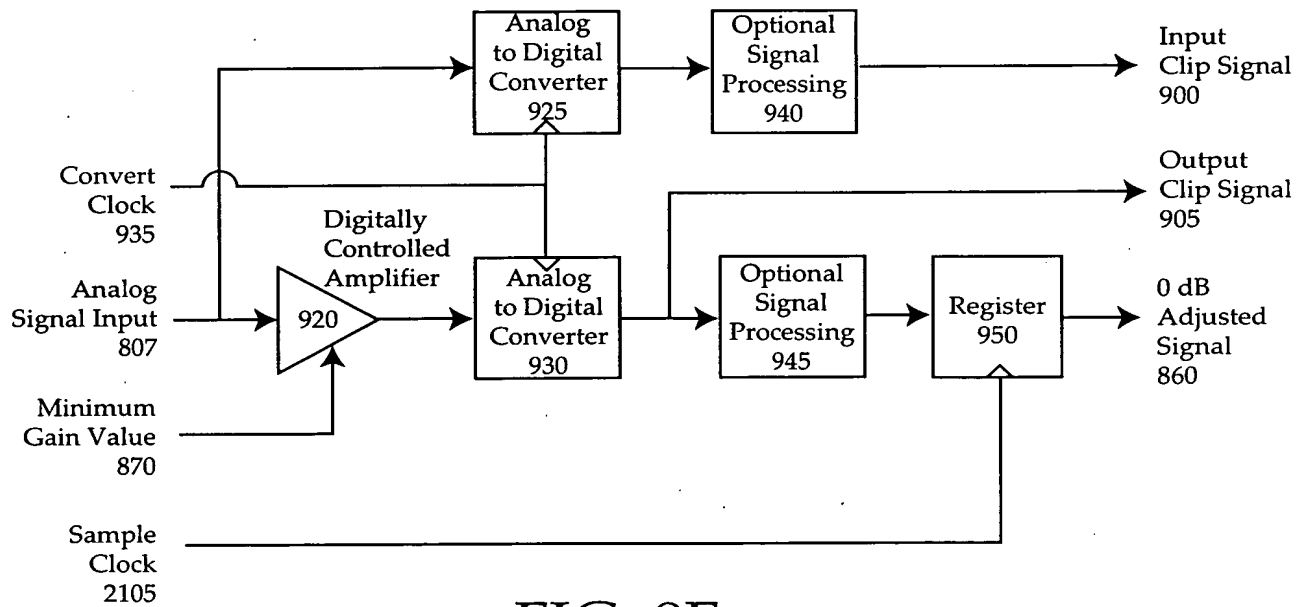


FIG. 8F

FIG. 8G

FIG. 9A

FIG. 9B

[illegible]

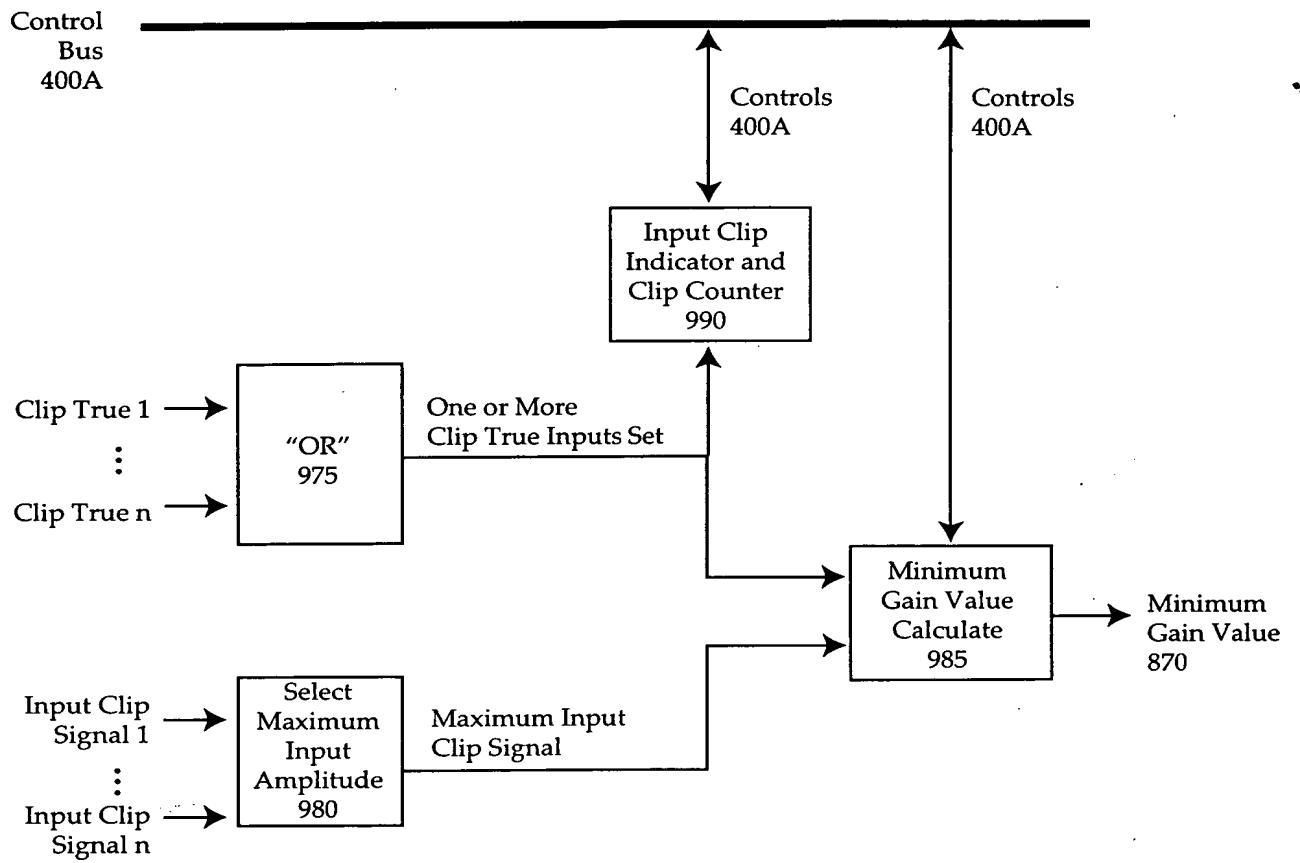
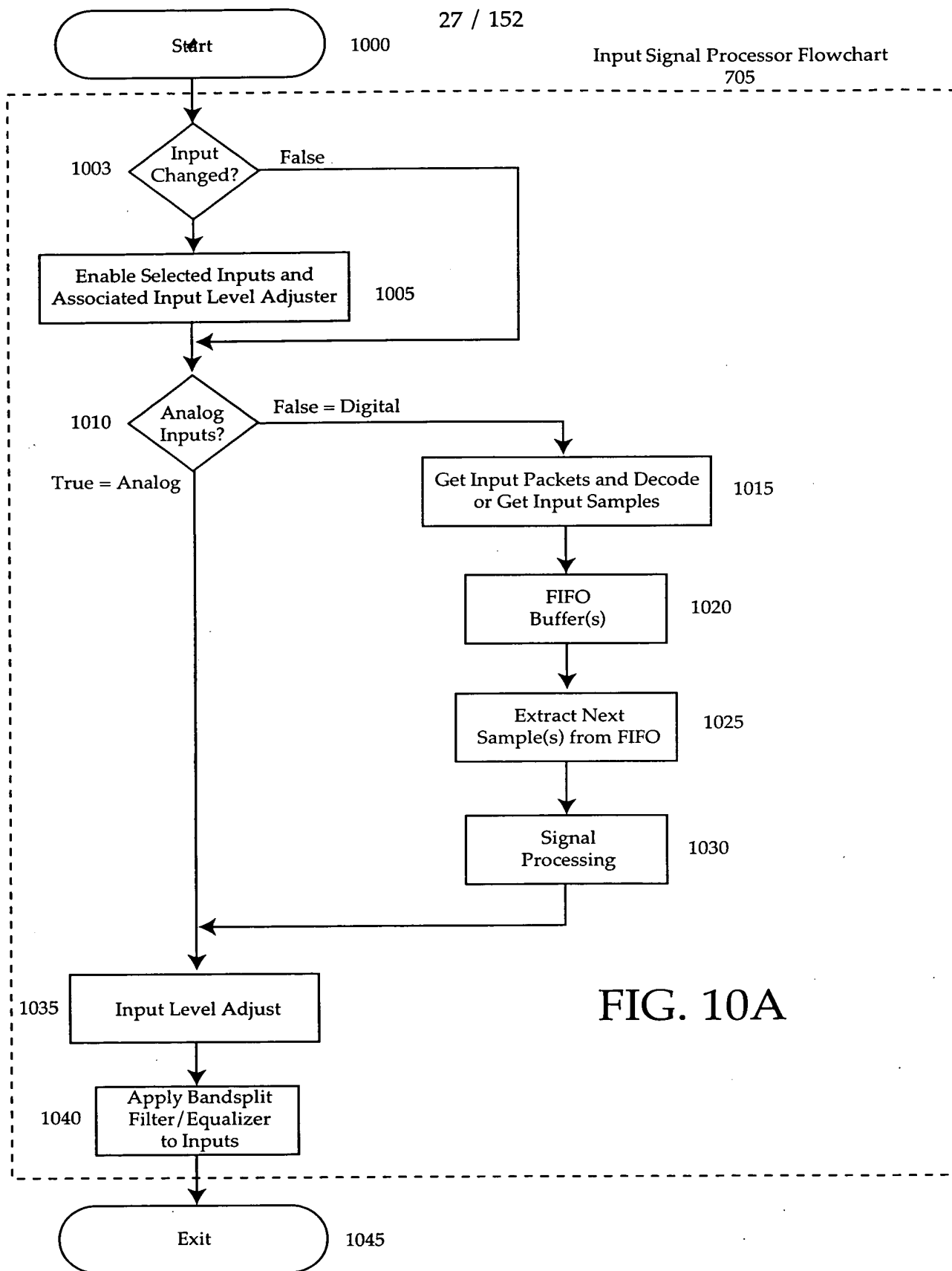
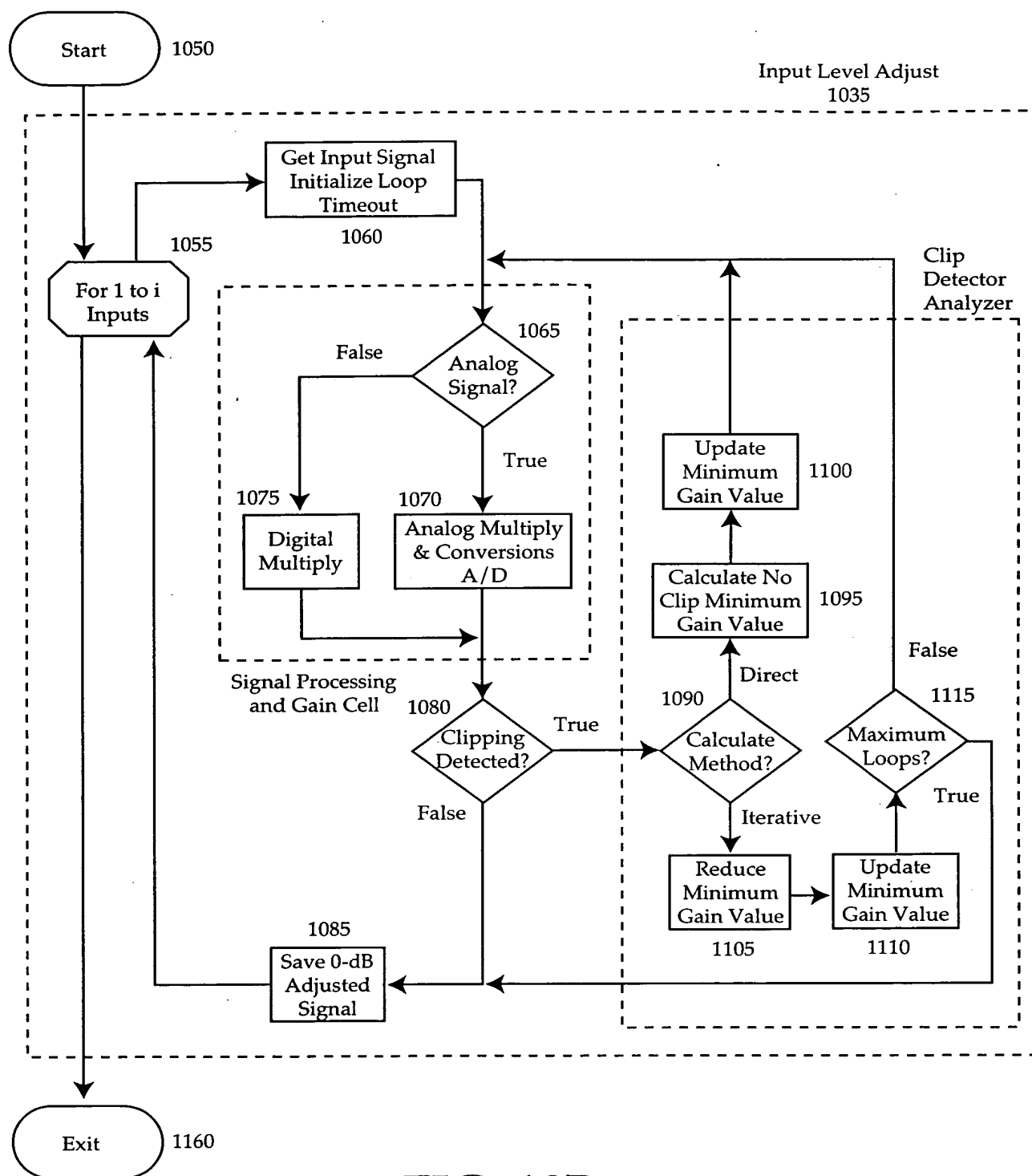


FIG. 9C





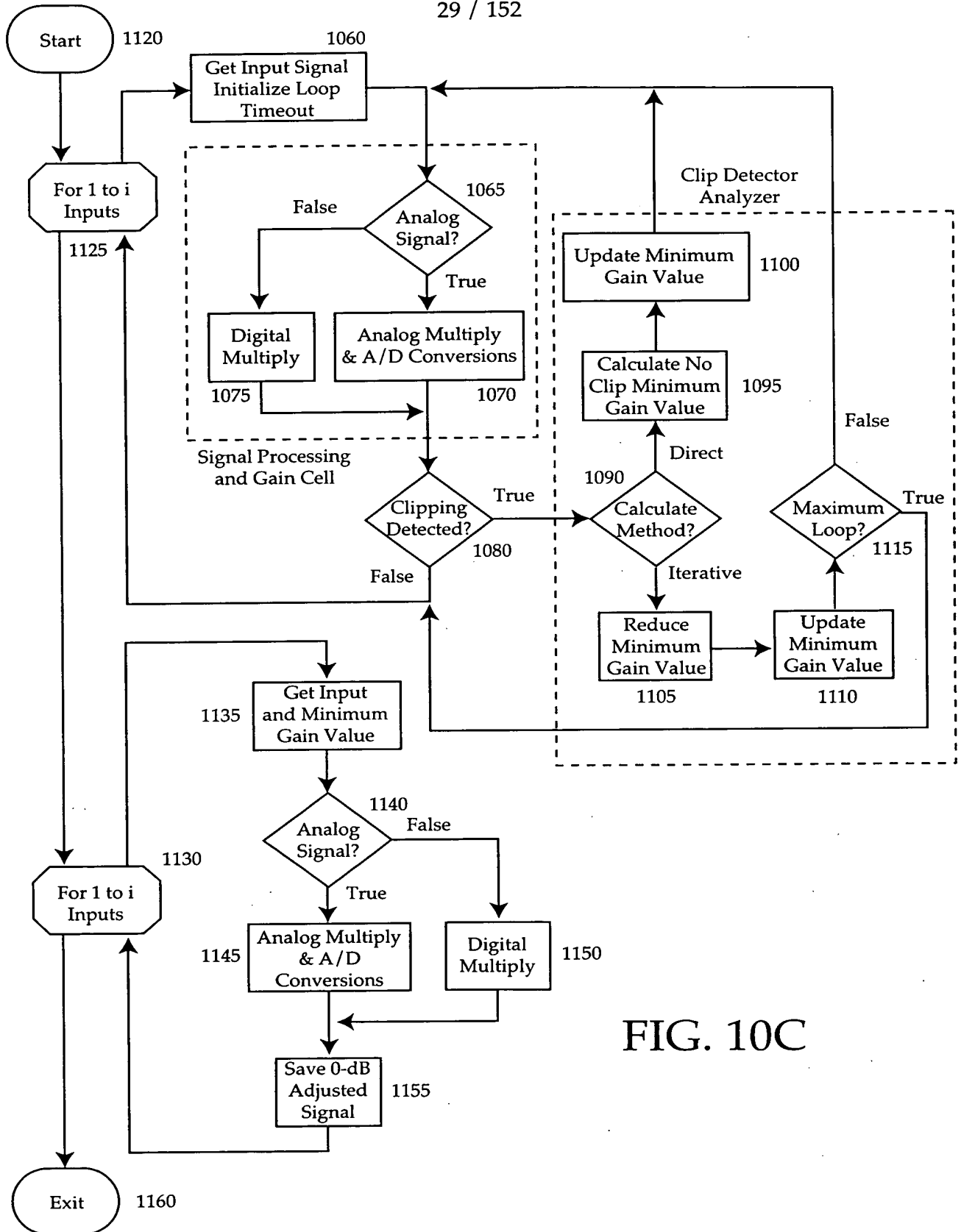
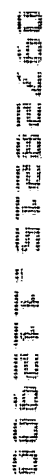


FIG. 10C



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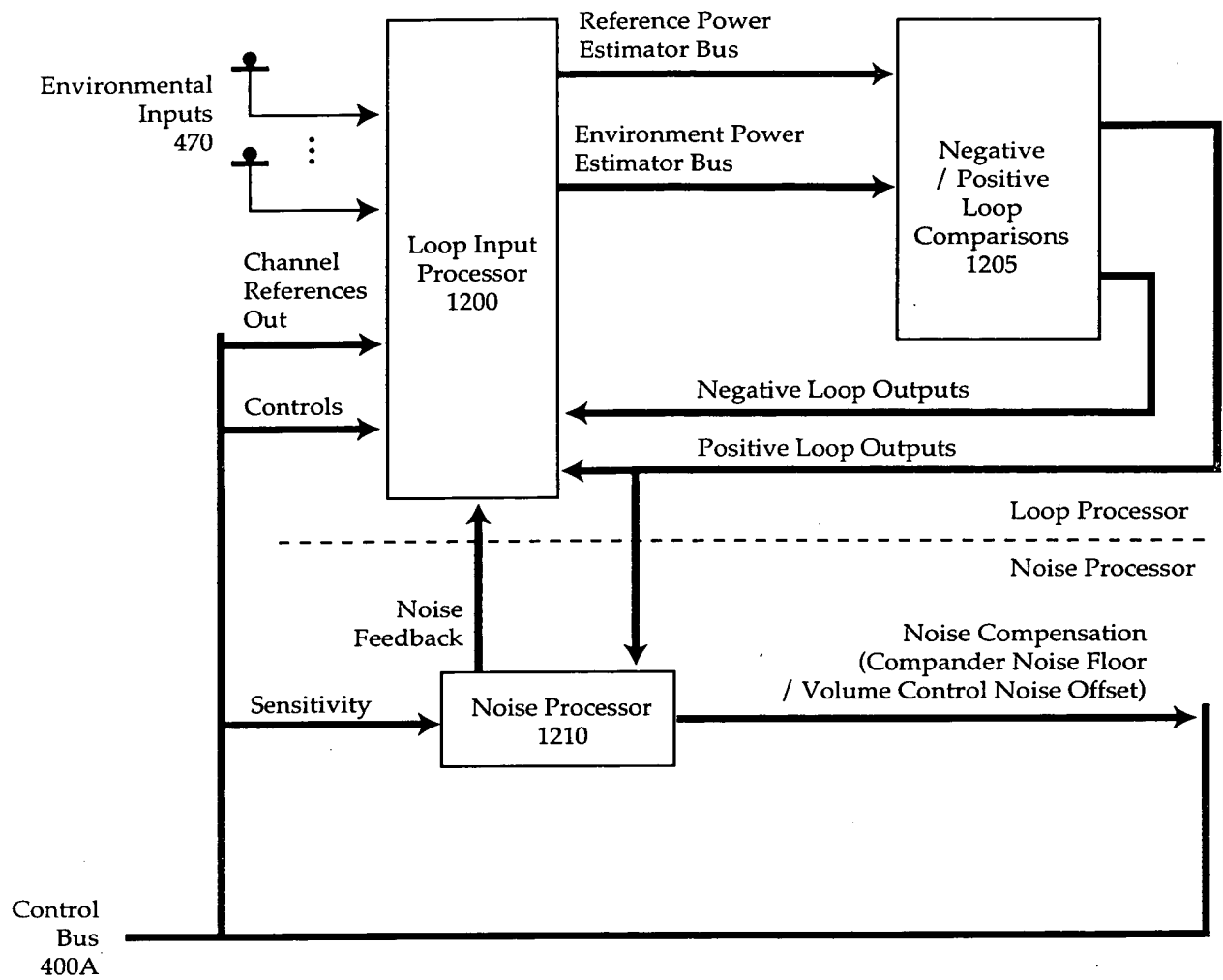


FIG. 11

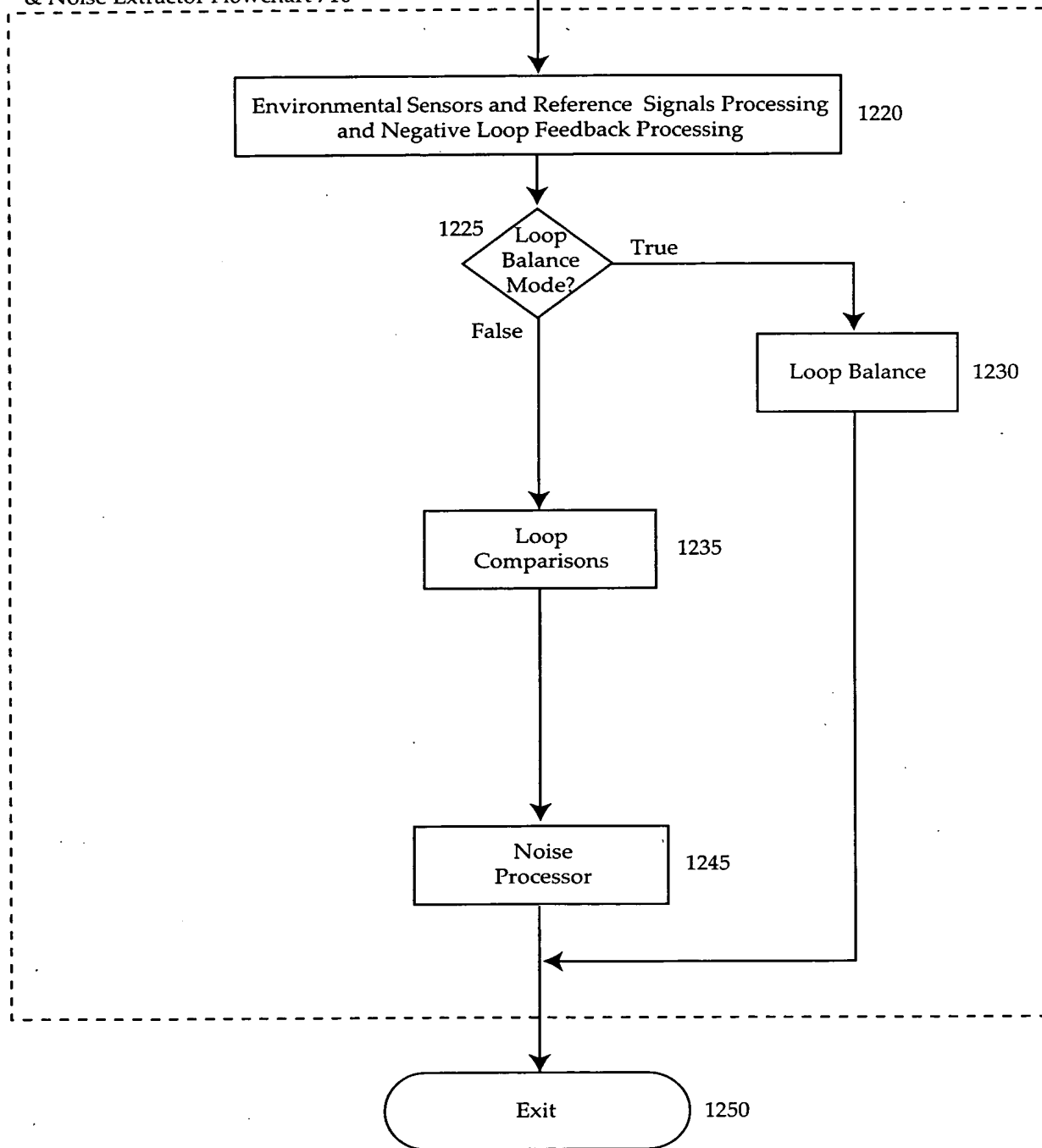
Environmental Sensor Adjustments
& Noise Extractor Flowchart 710

FIG. 12

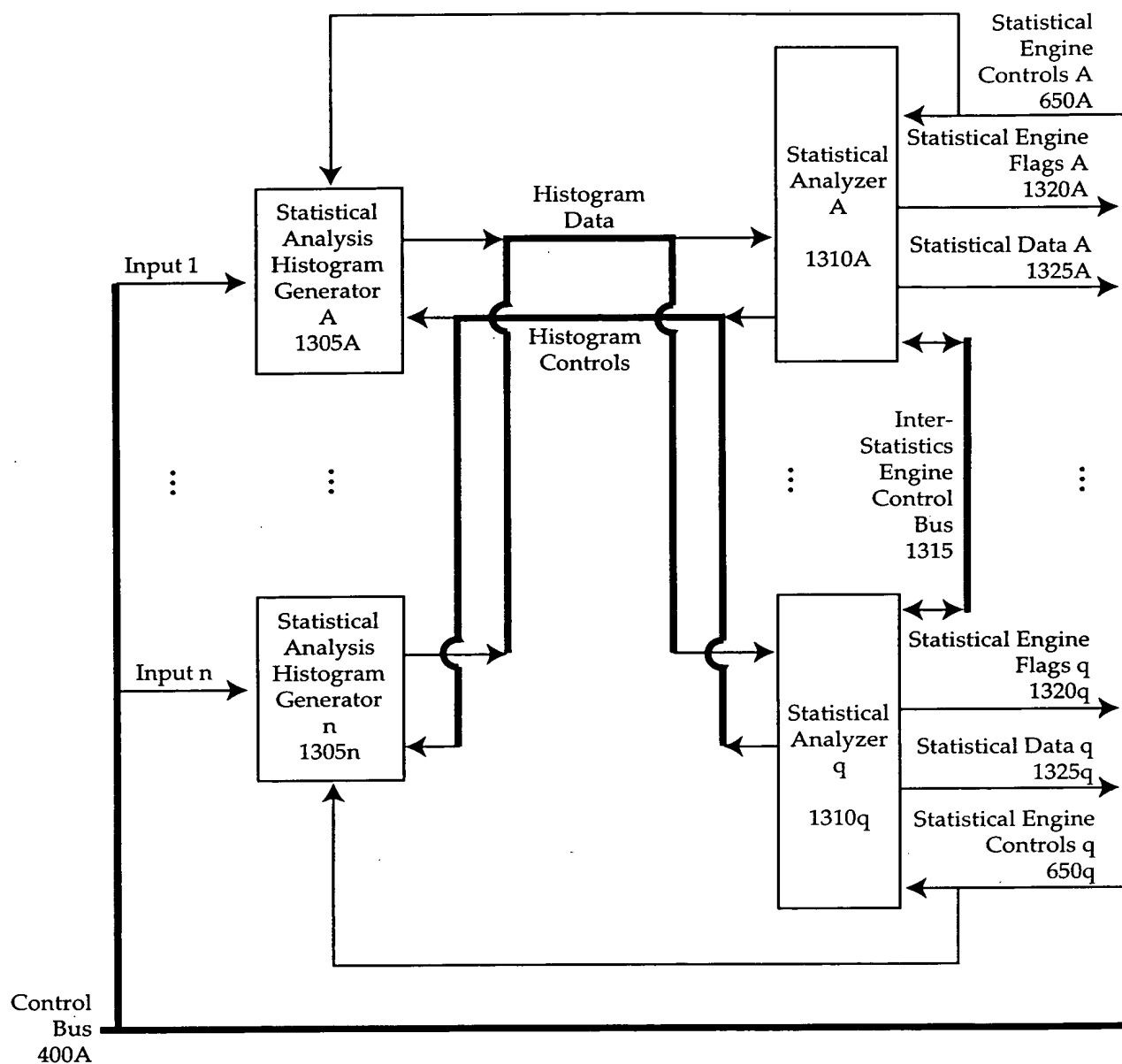


FIG. 13

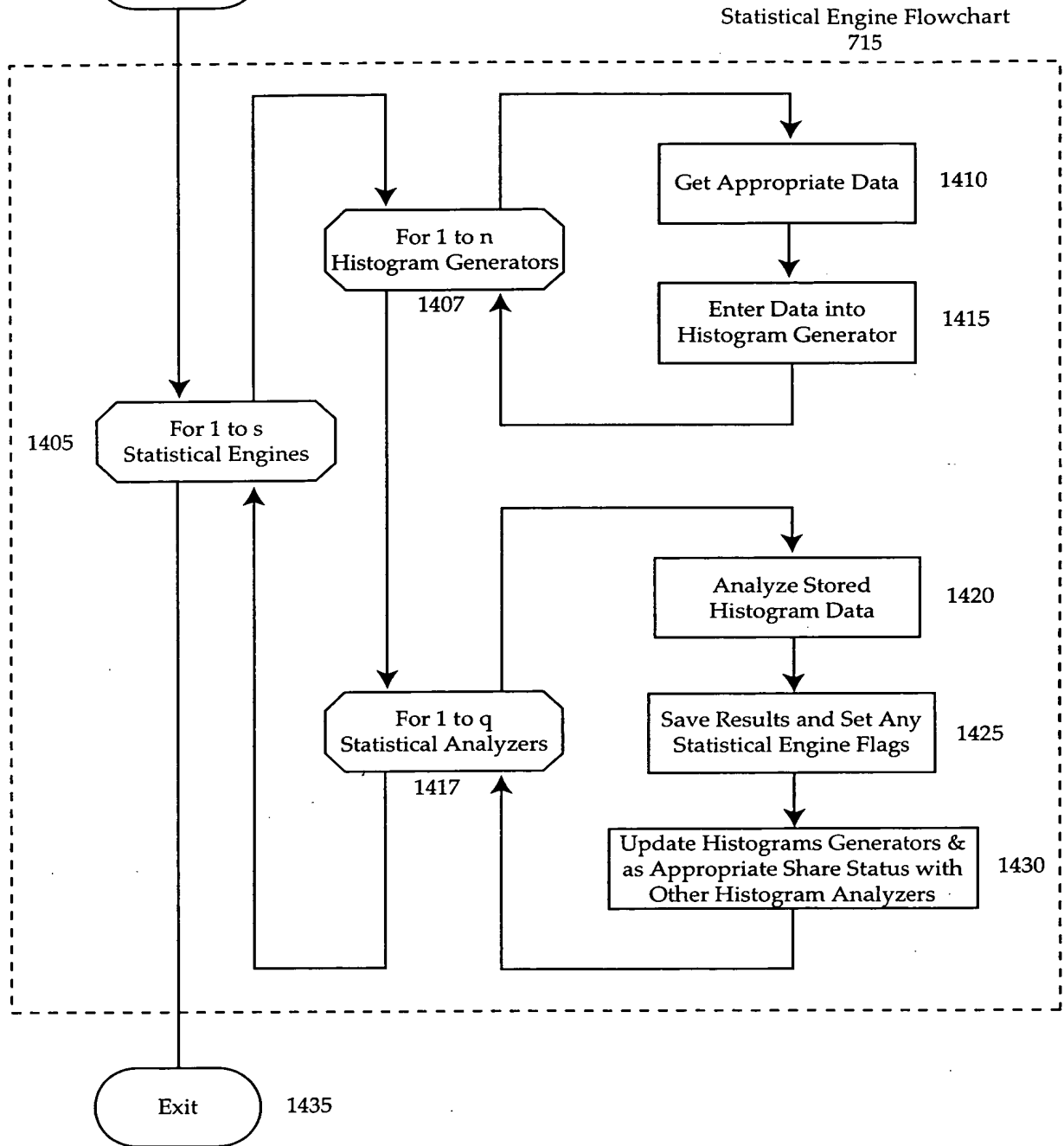


FIG. 14

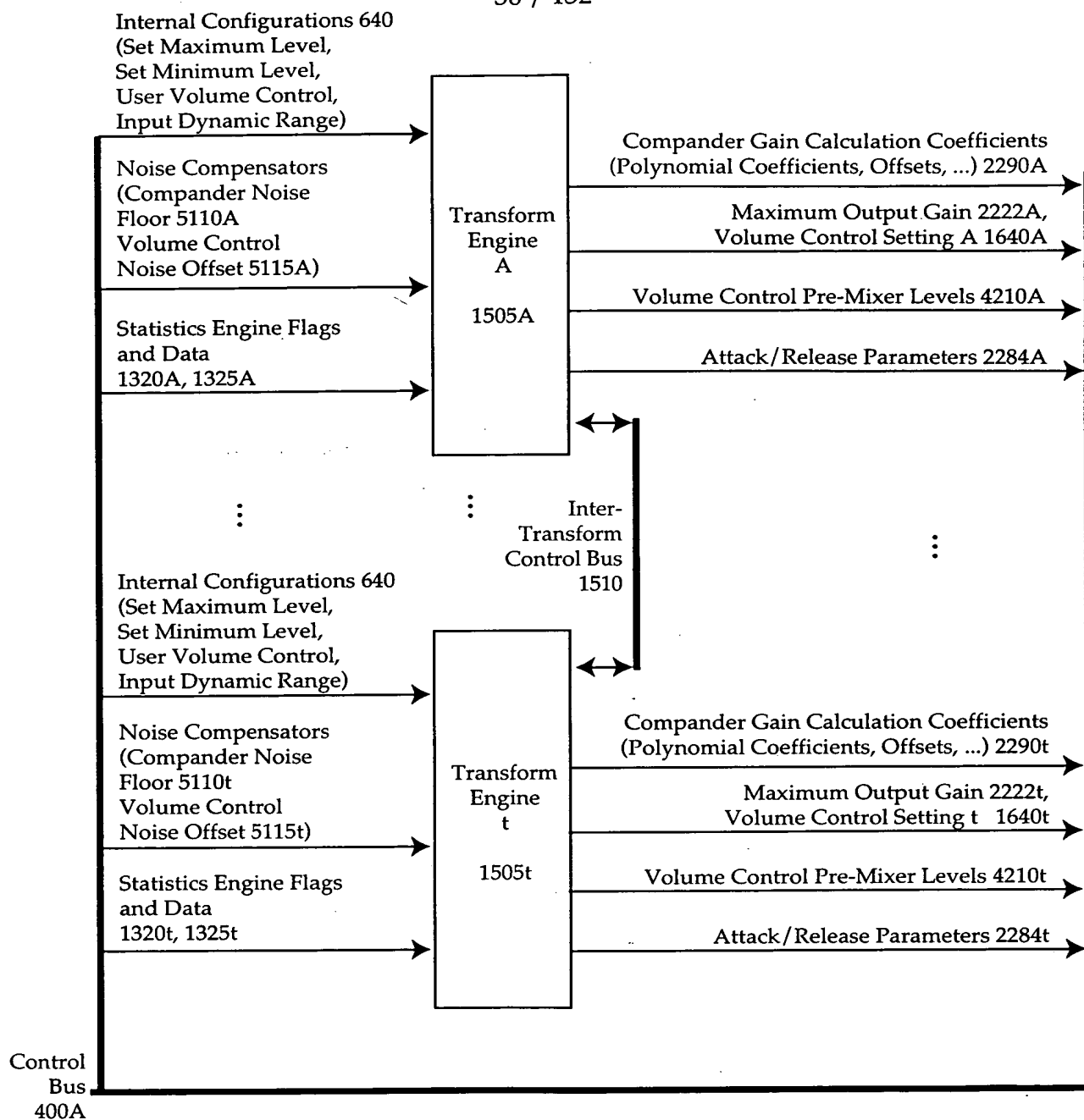


FIG. 15

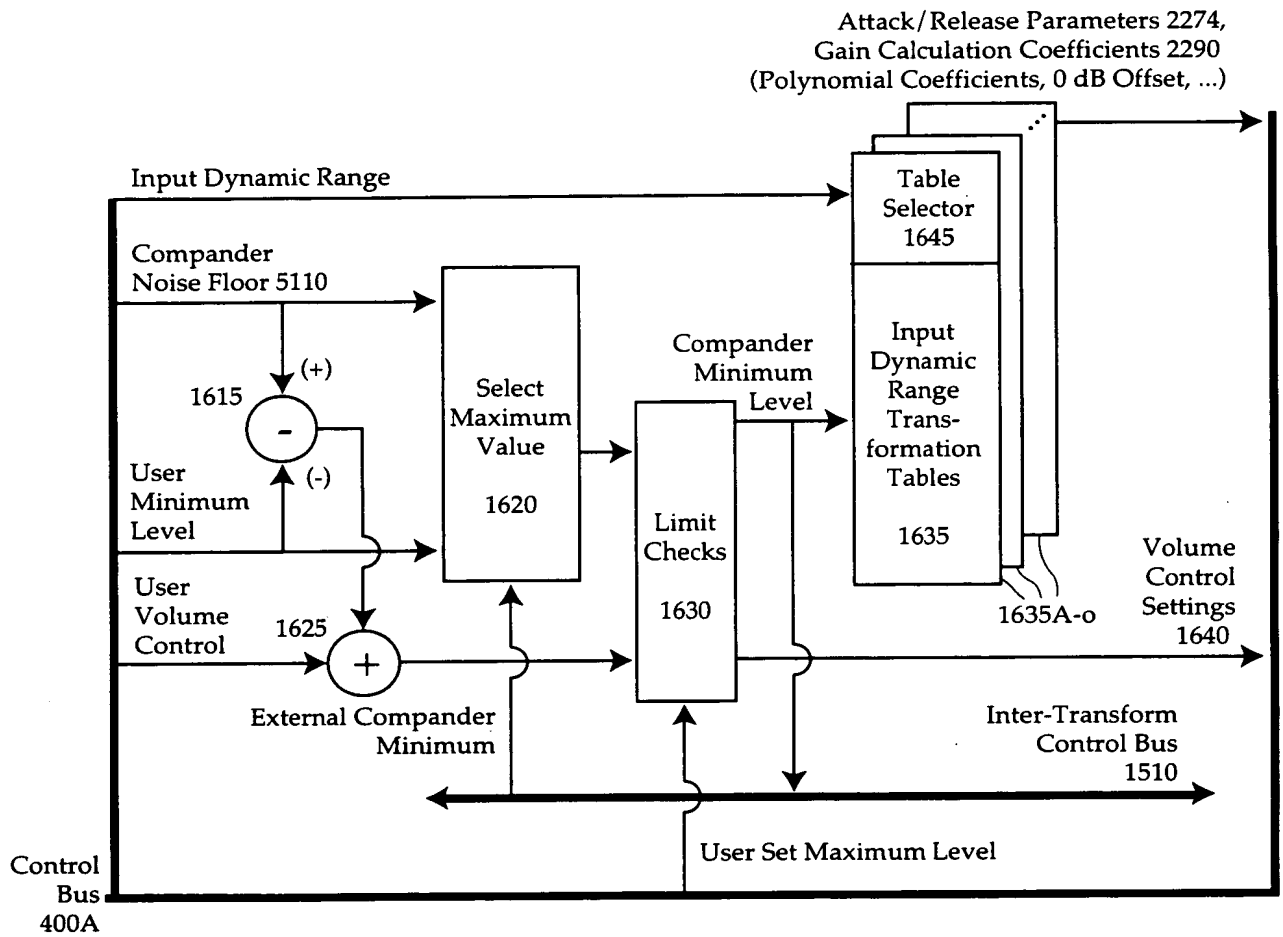


FIG. 16

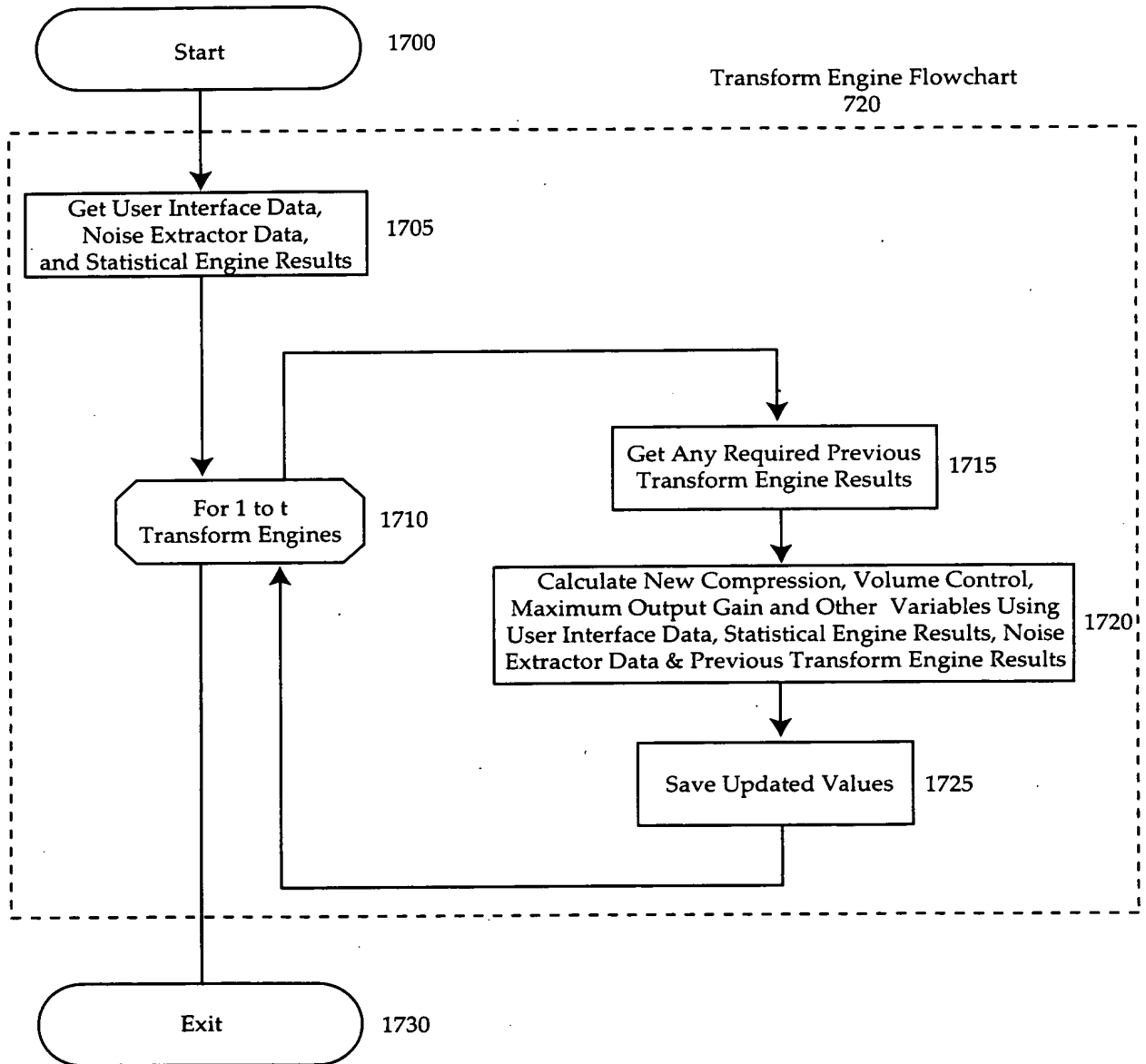


FIG. 17

Central Power Estimator Mixer Combiner
455

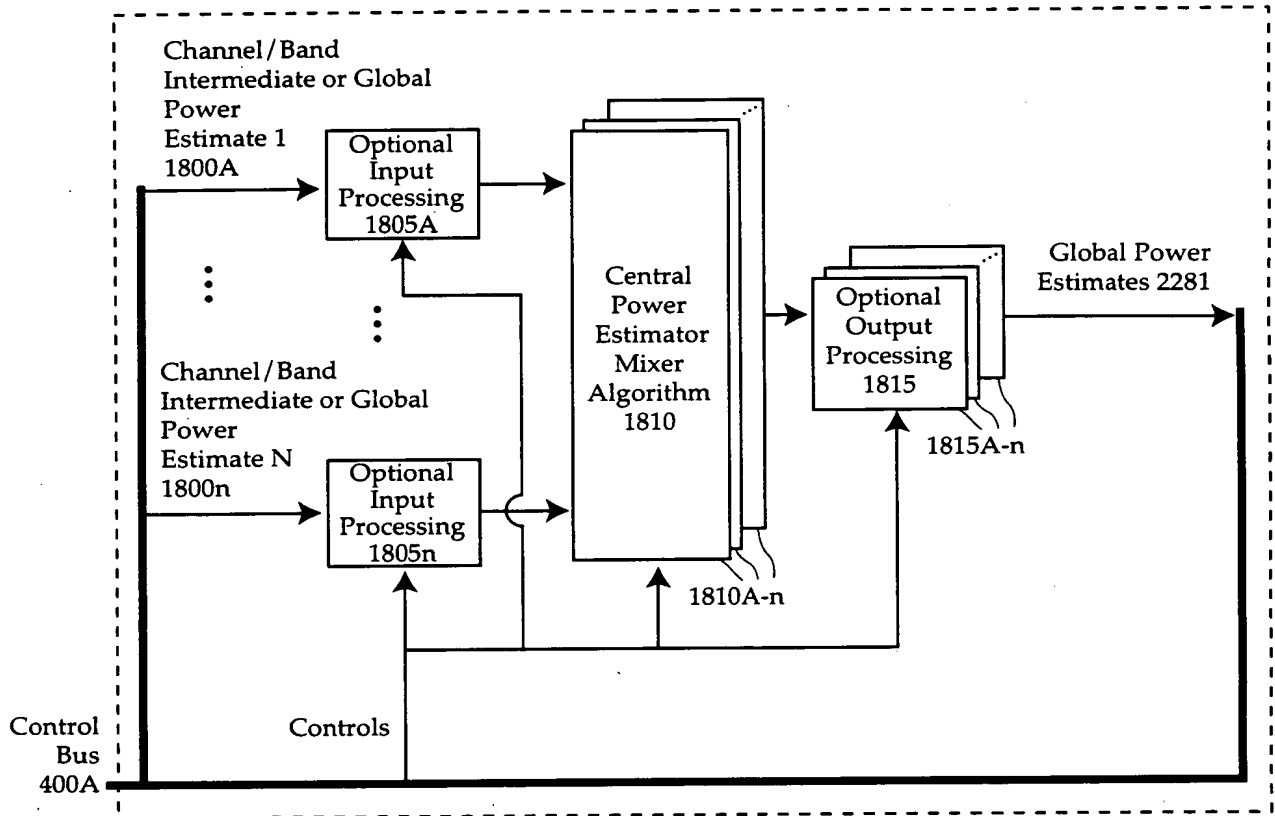


FIG. 18

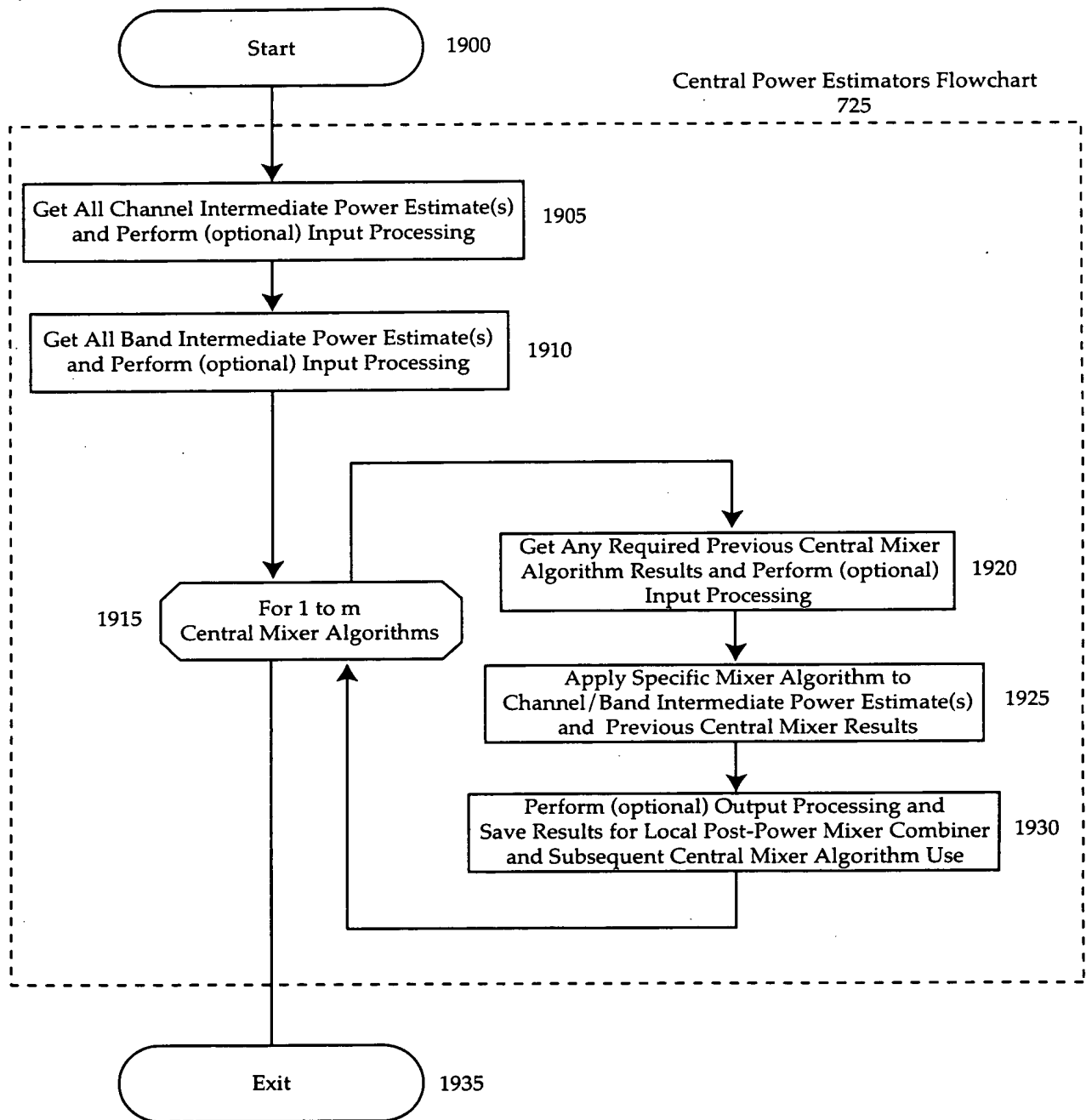
Central Power Estimators Flowchart
725

FIG. 19

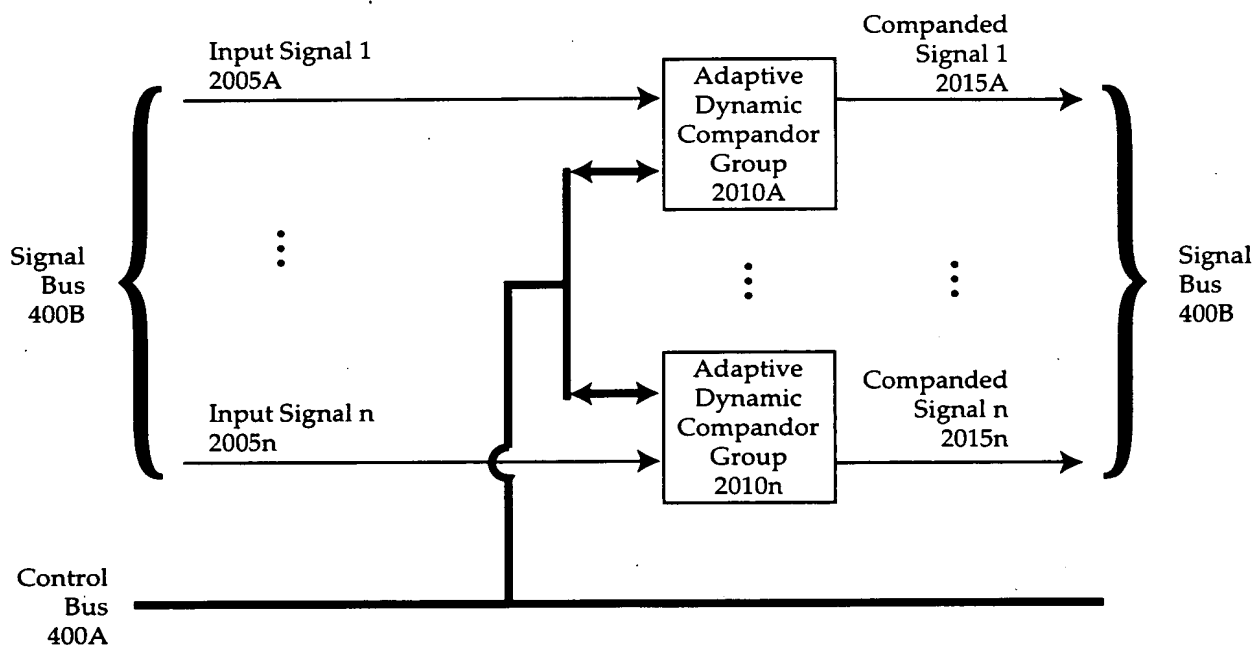


FIG. 20A

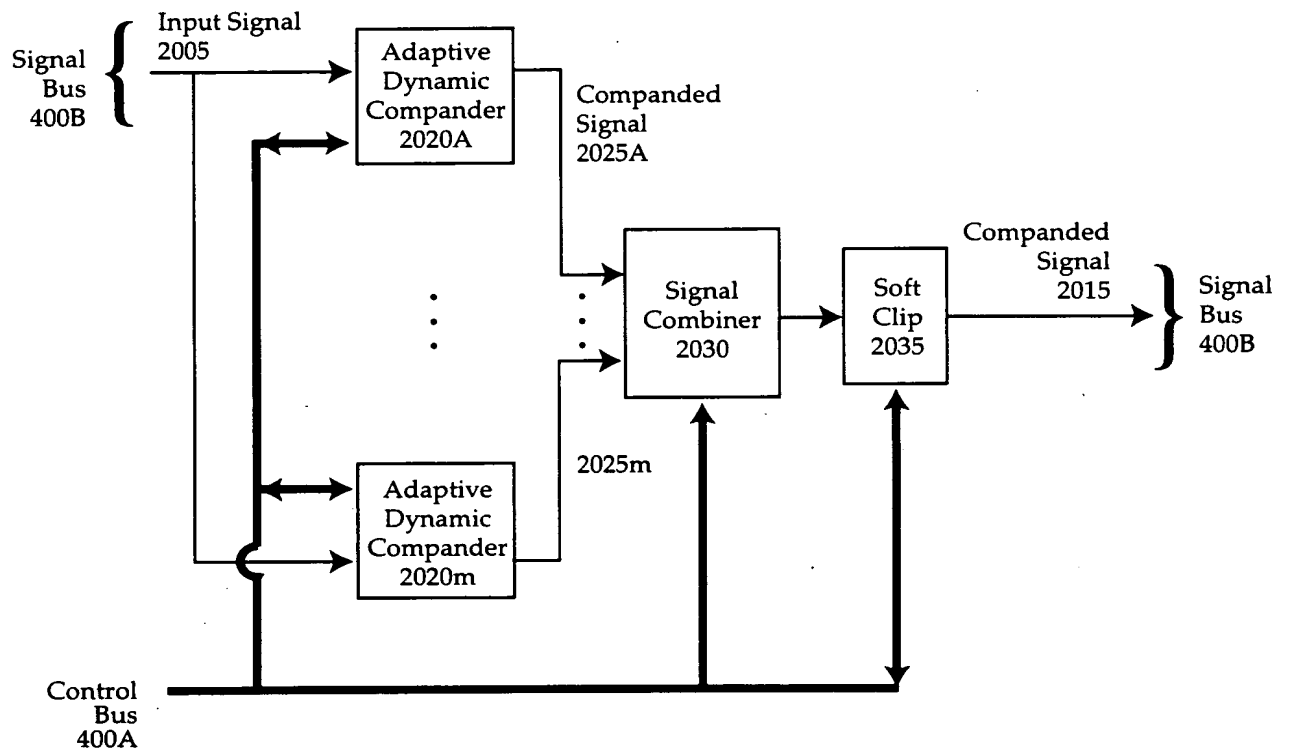


FIG. 20B

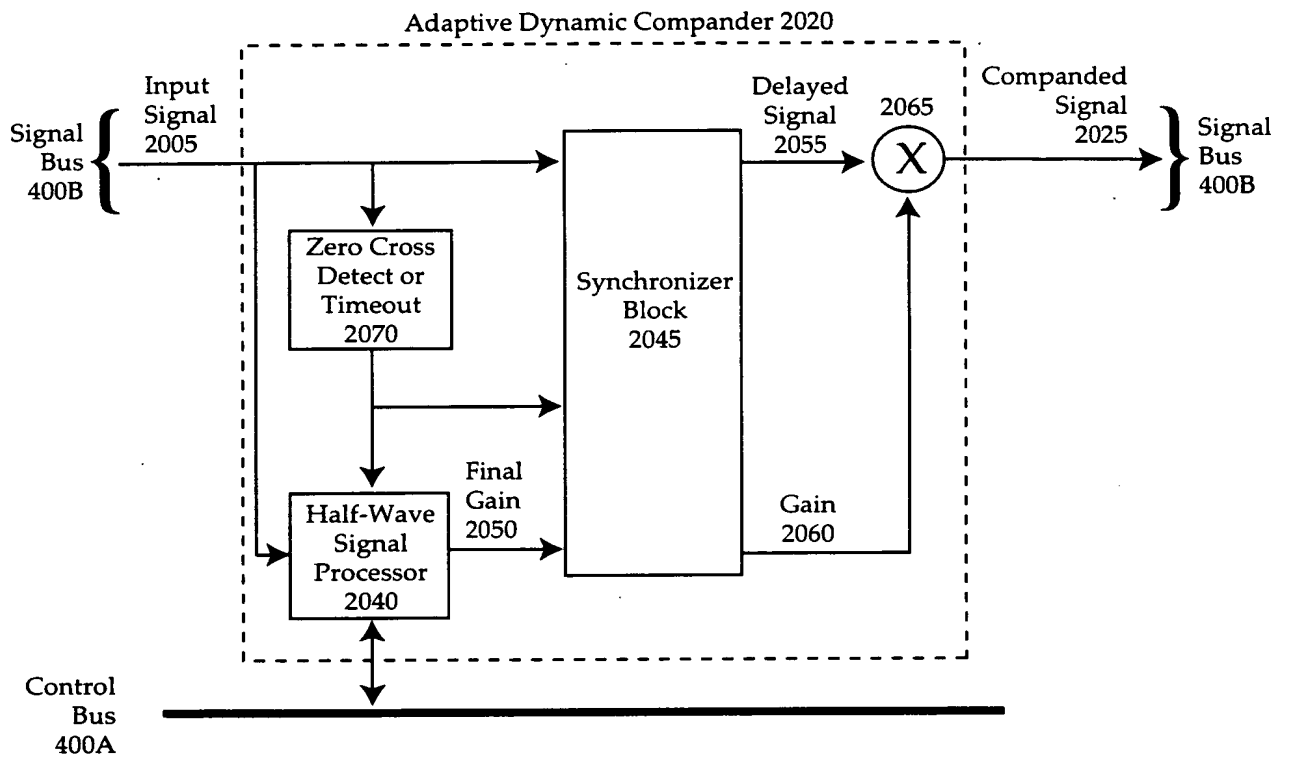


FIG. 20C

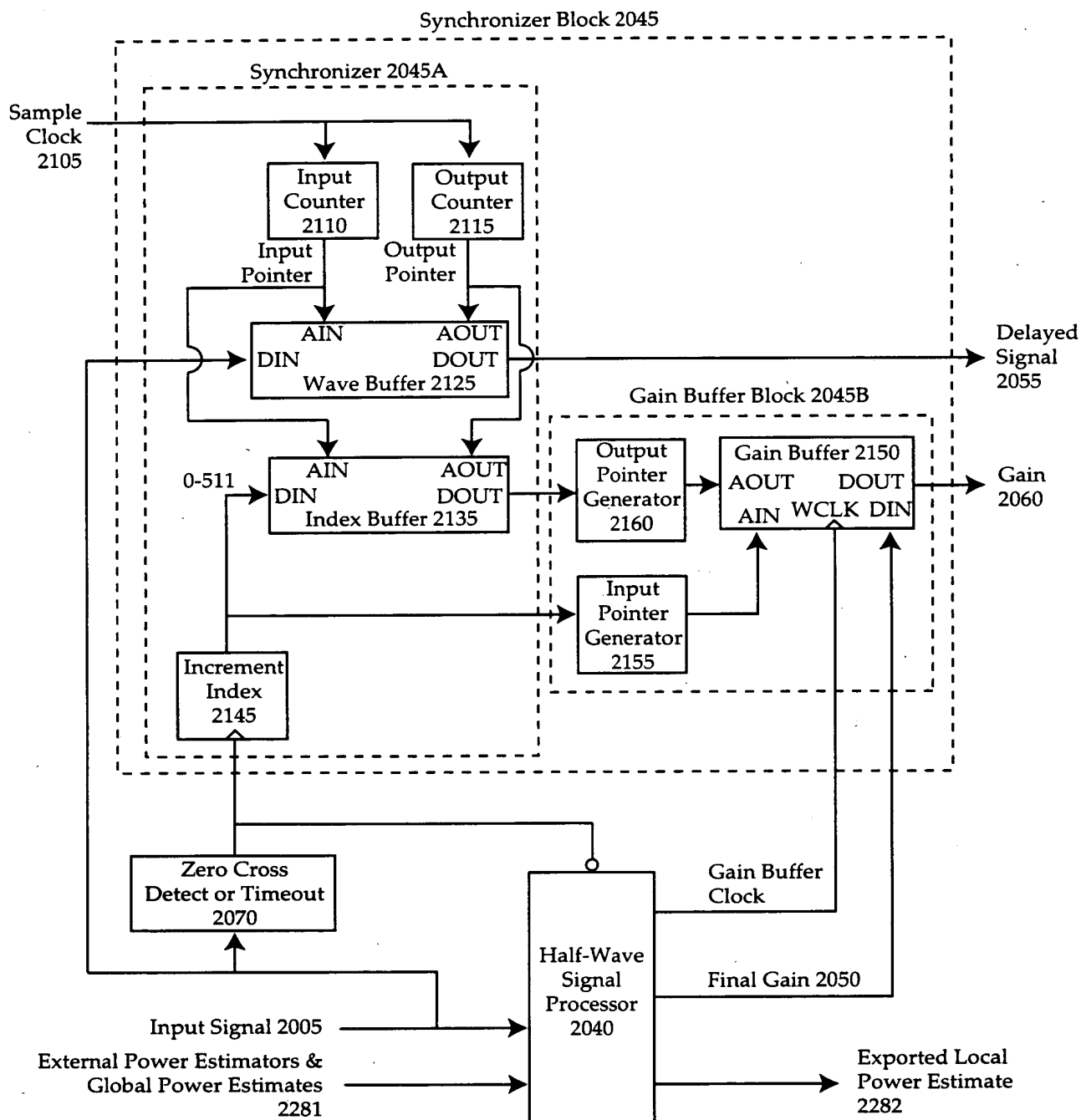
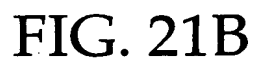


FIG. 21A



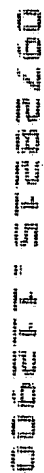


FIG. 21C

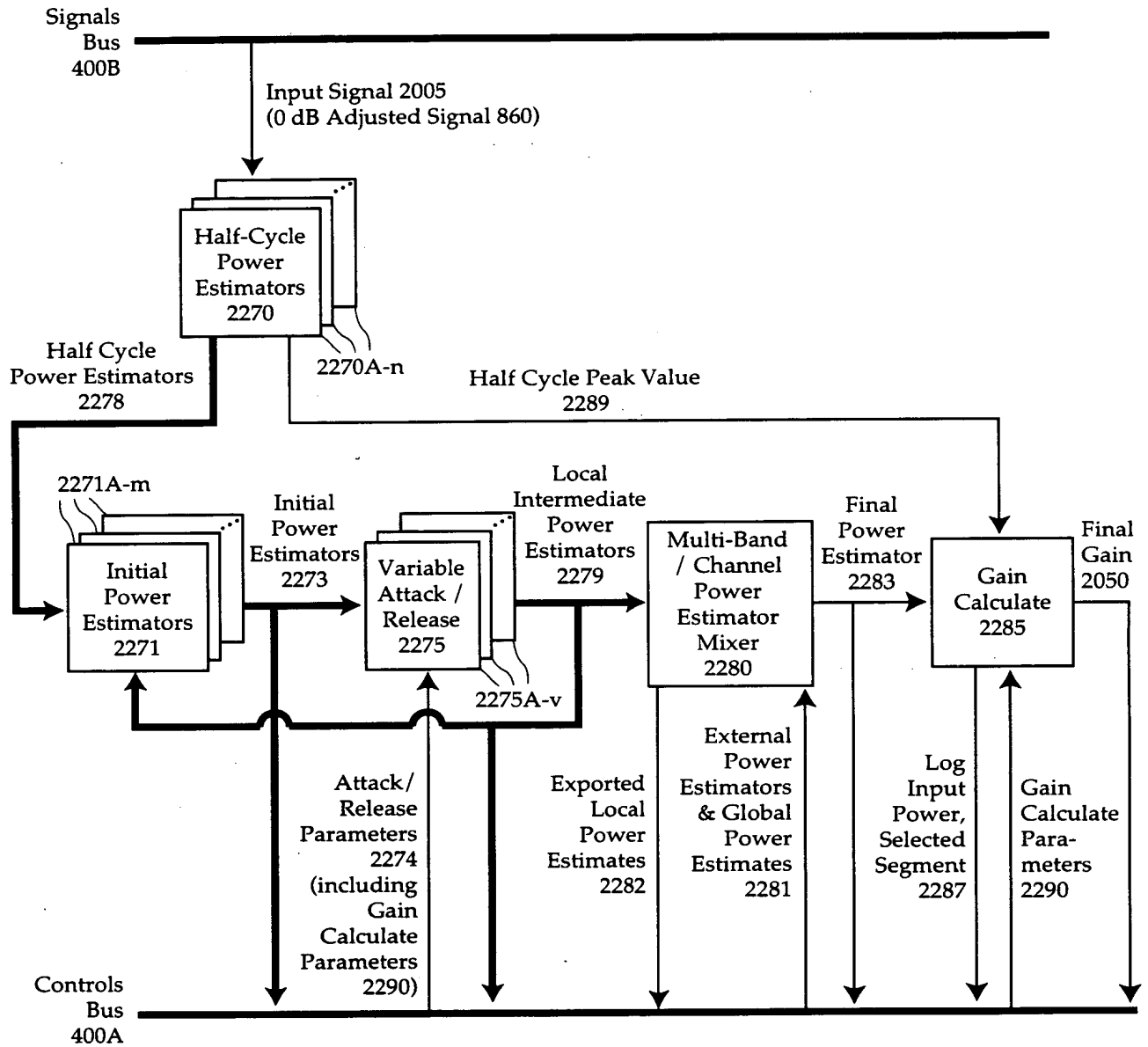


FIG. 22

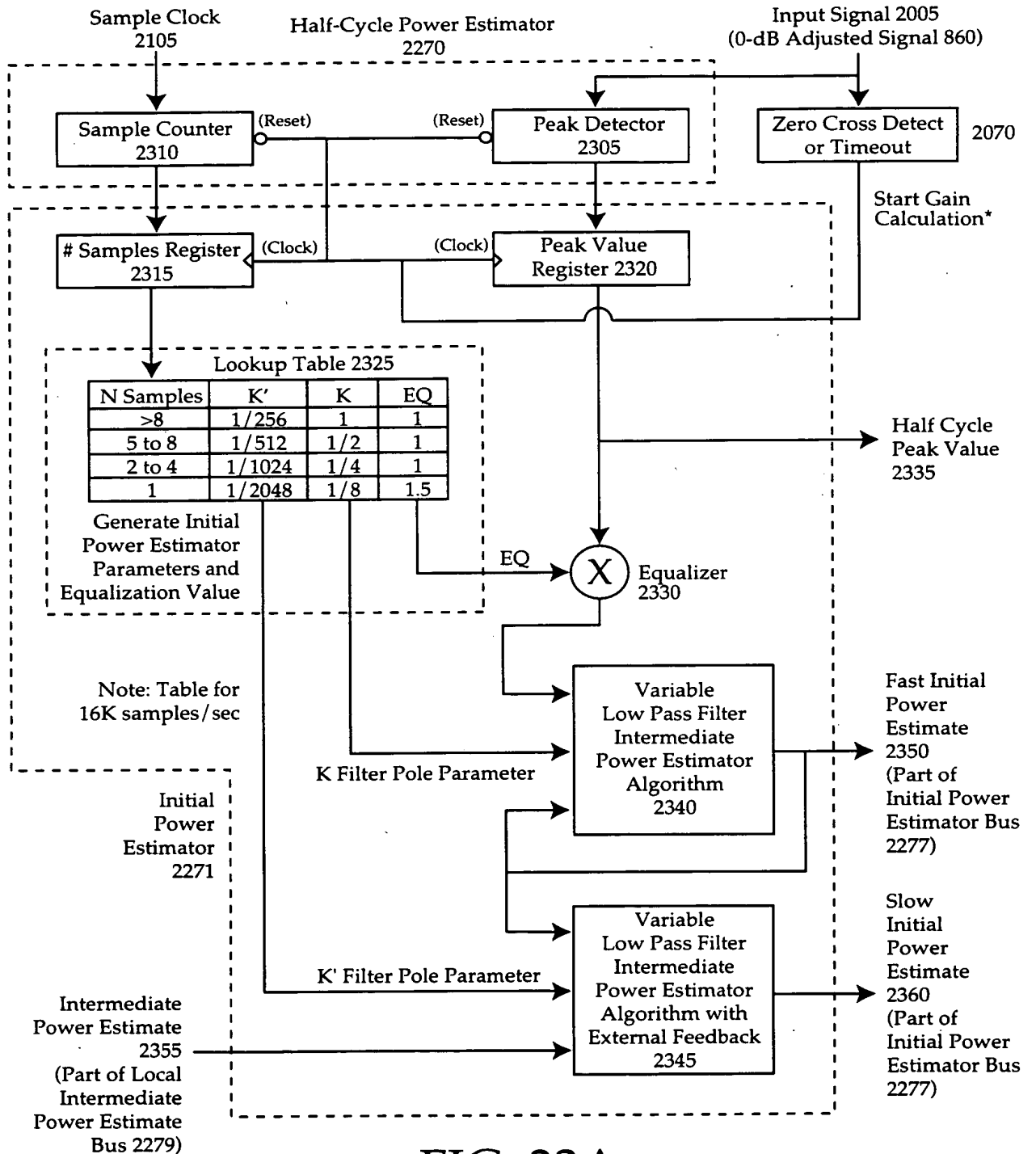


FIG. 23A

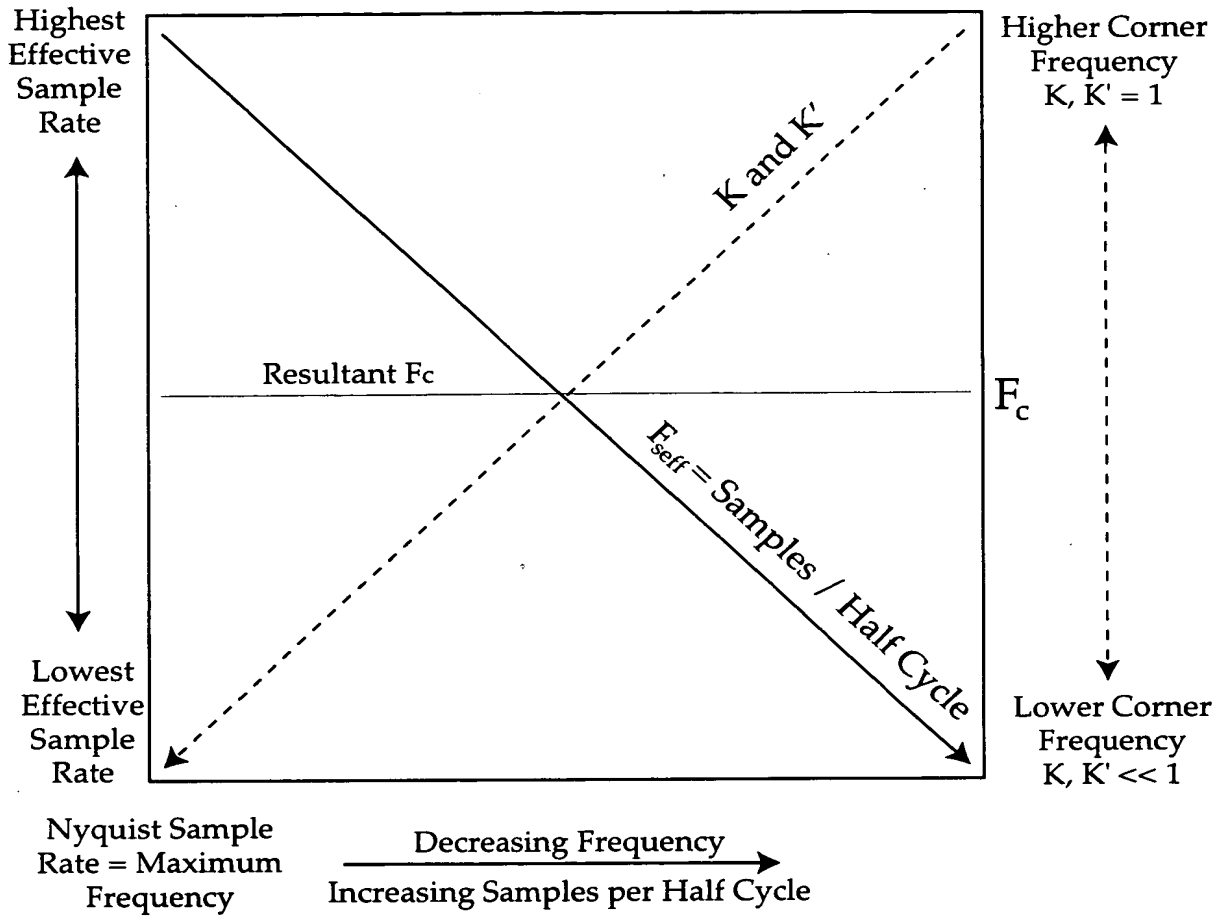


FIG. 23B

1

FIG. 23C

[illegible]

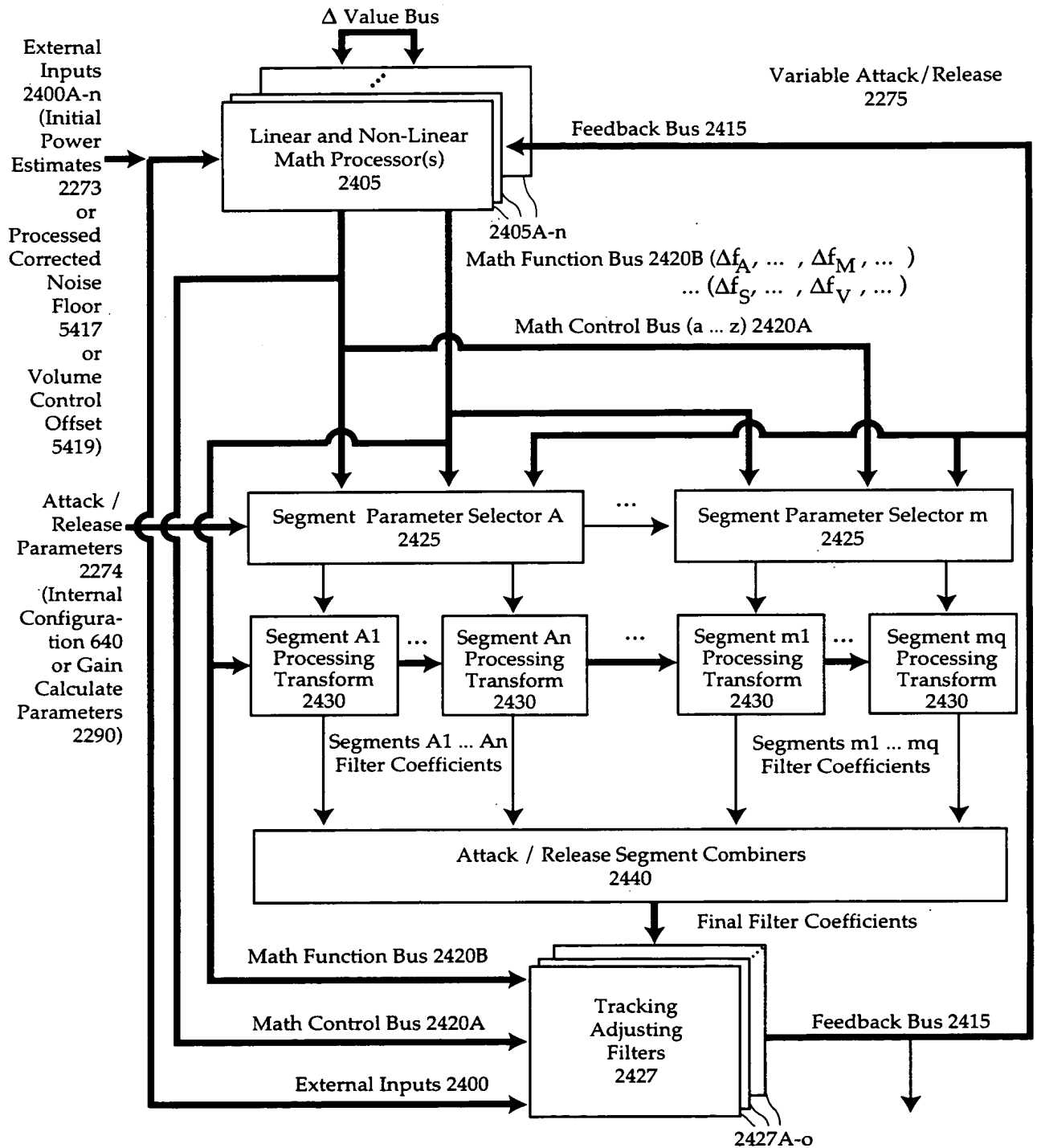


FIG. 24A

Local Intermediate Power Estimates 2279
or Long Duration Noise Floor 5425
or Long Duration Noise Offset 5427

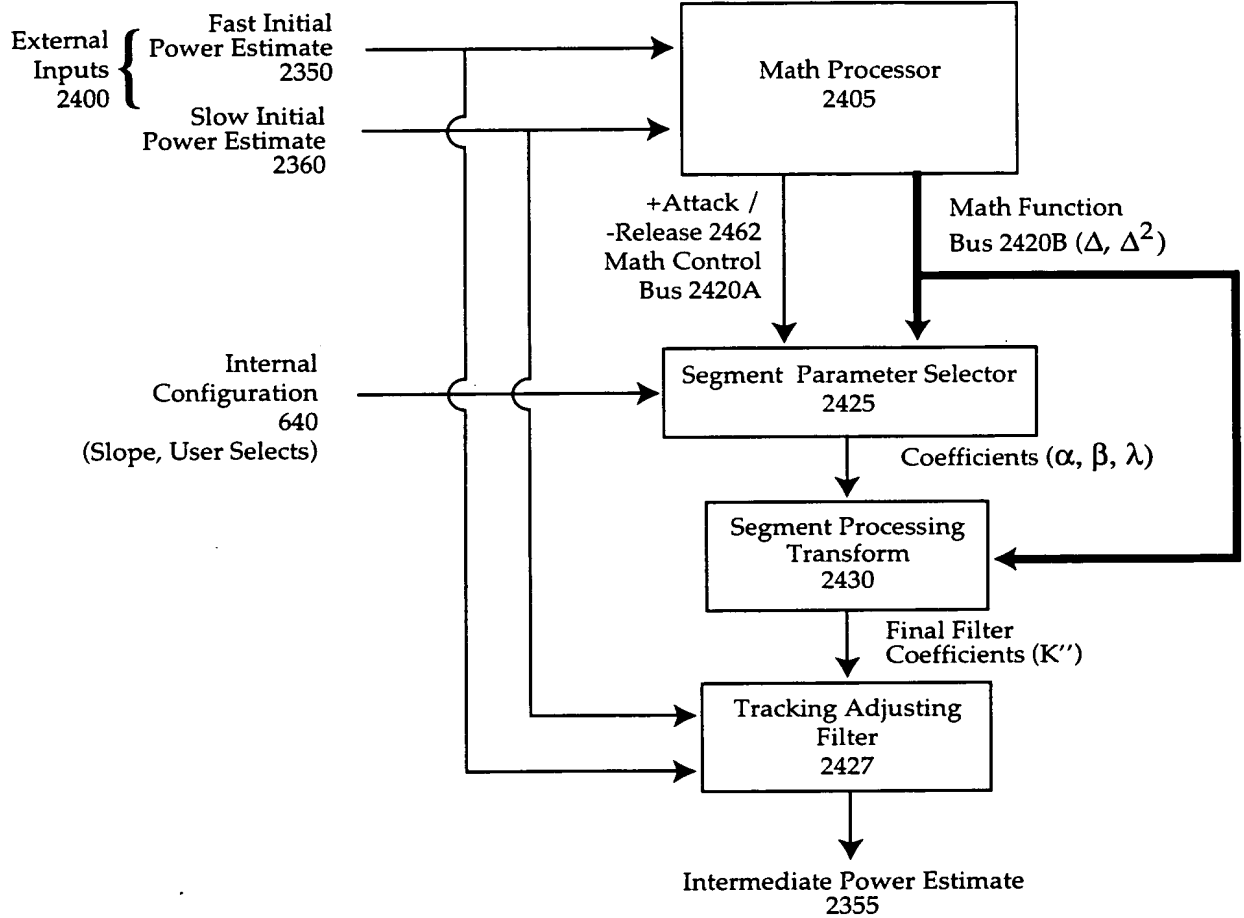
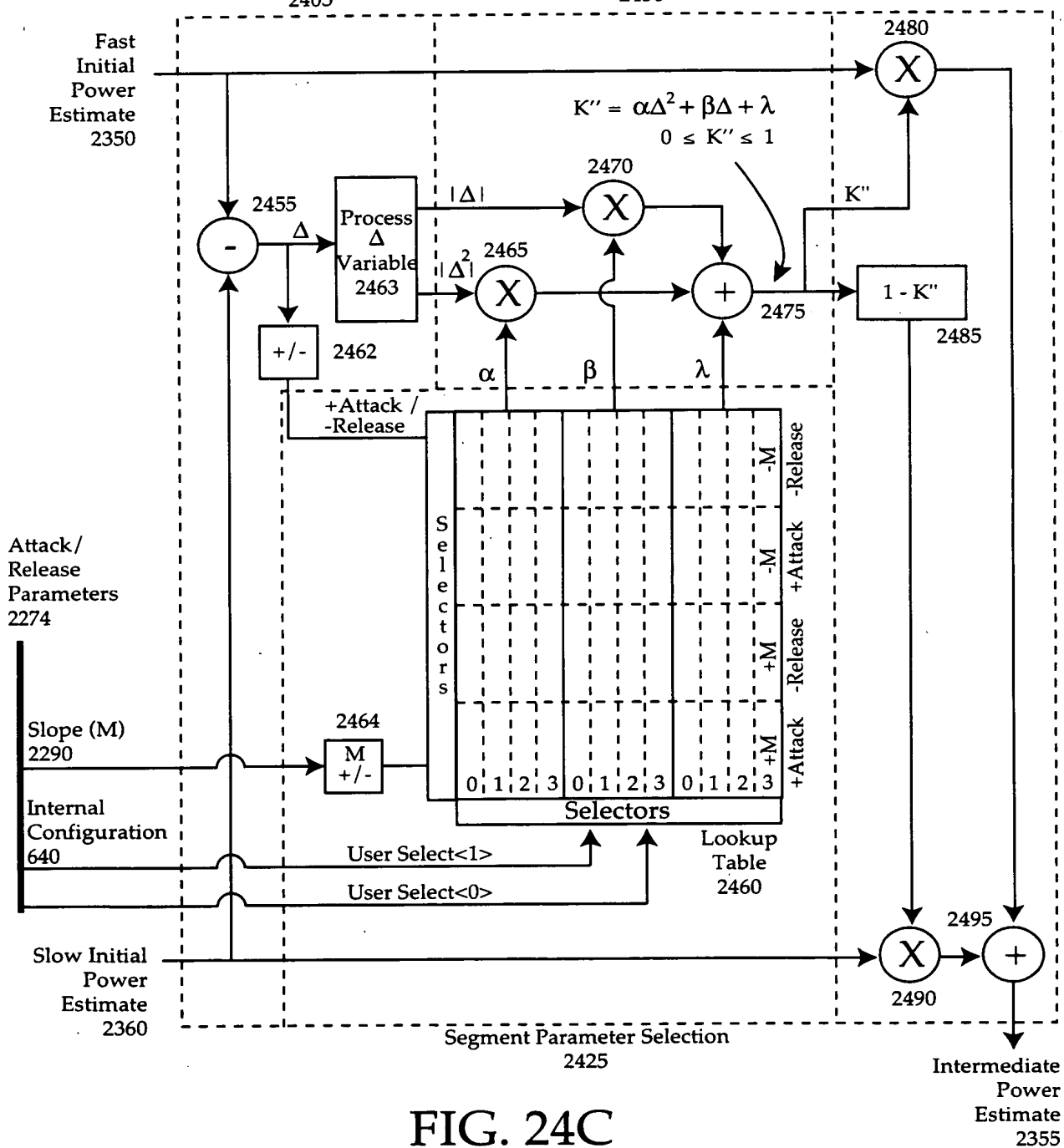


FIG. 24B



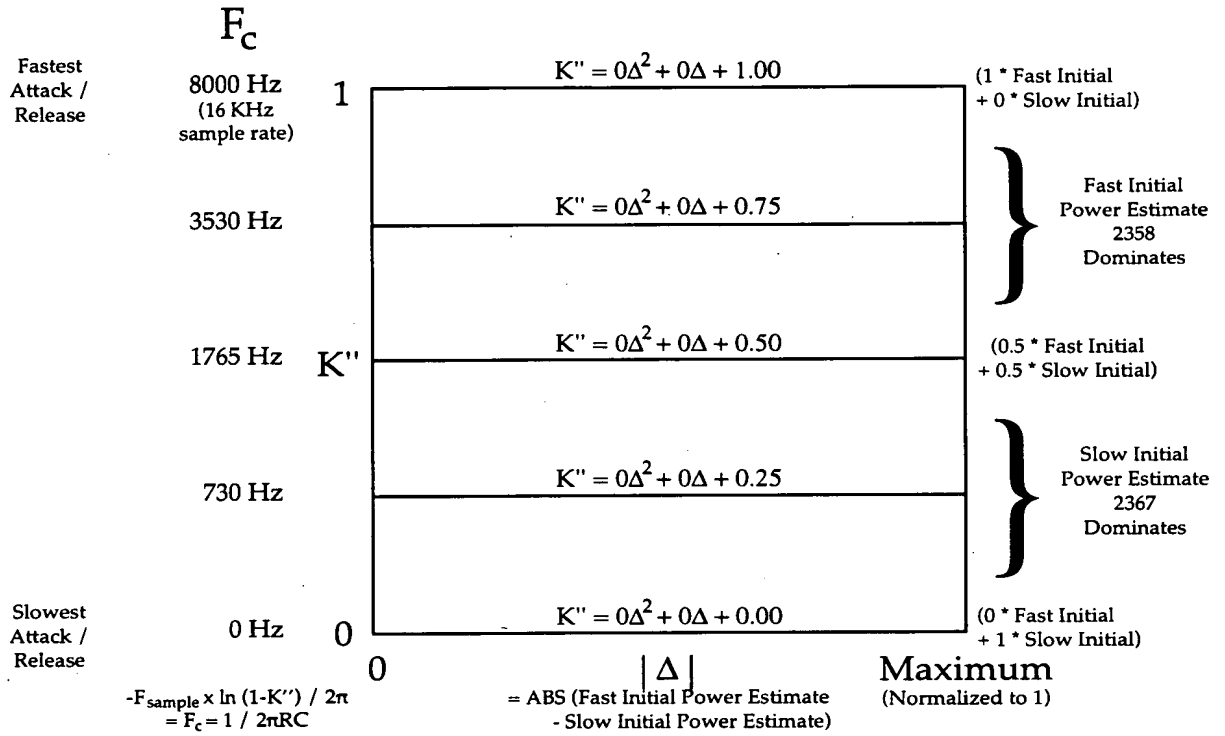


FIG. 24D

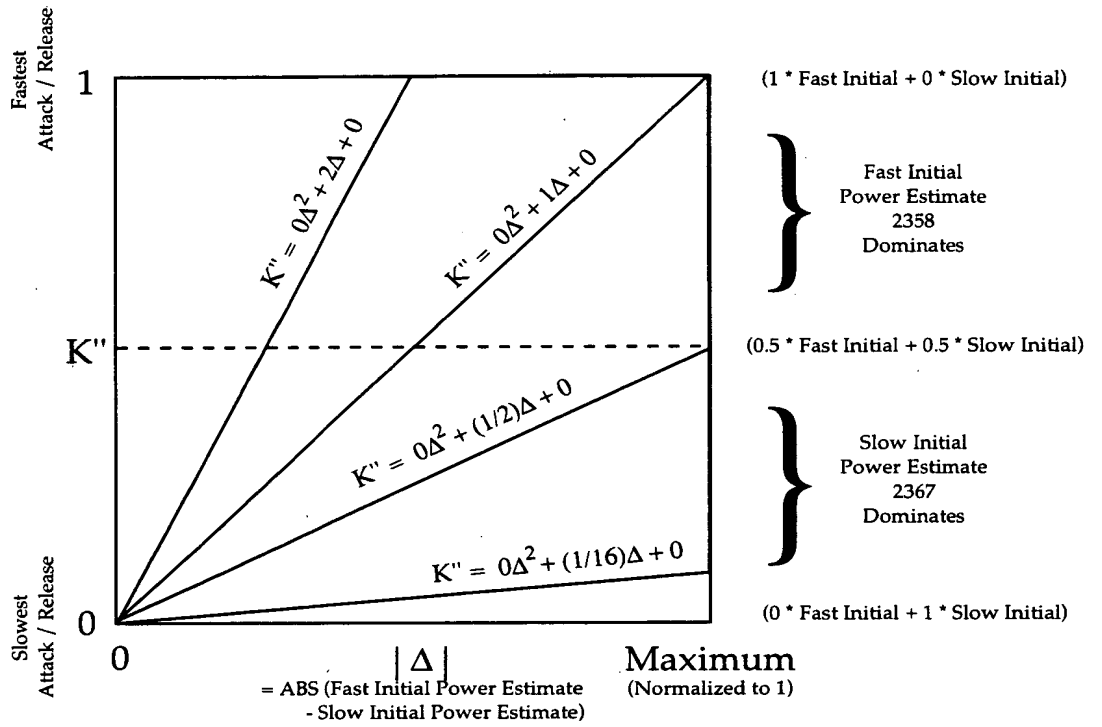


FIG. 24E

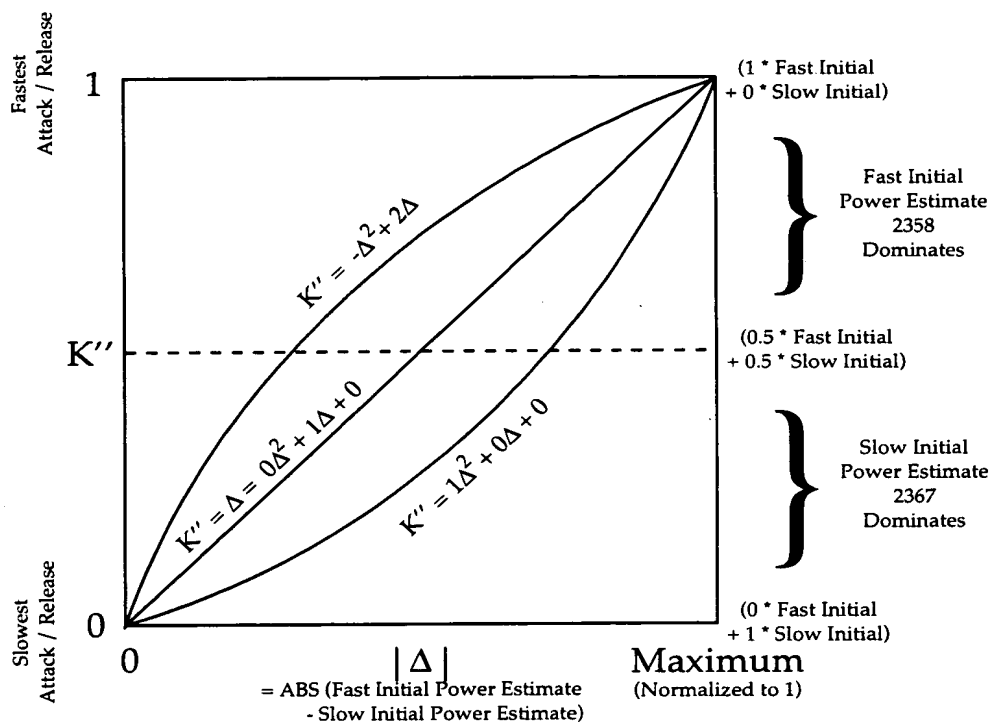


FIG. 24F

00621-51222260

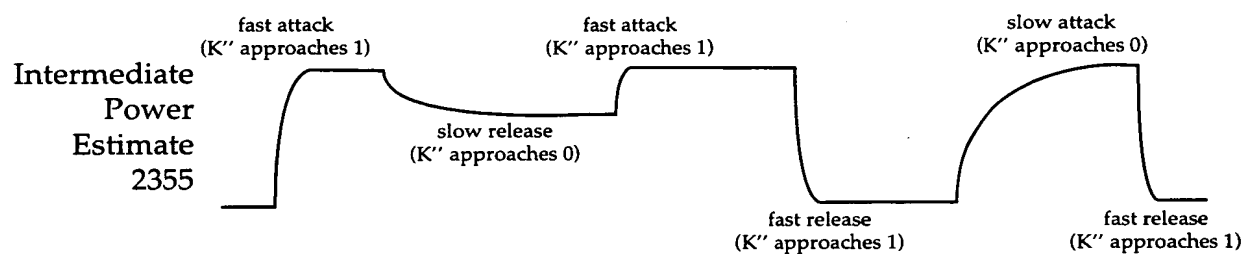
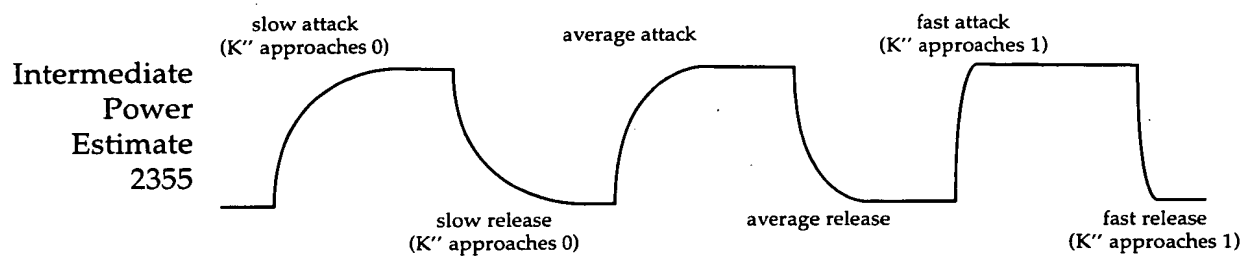


FIG. 24G

[illegible]

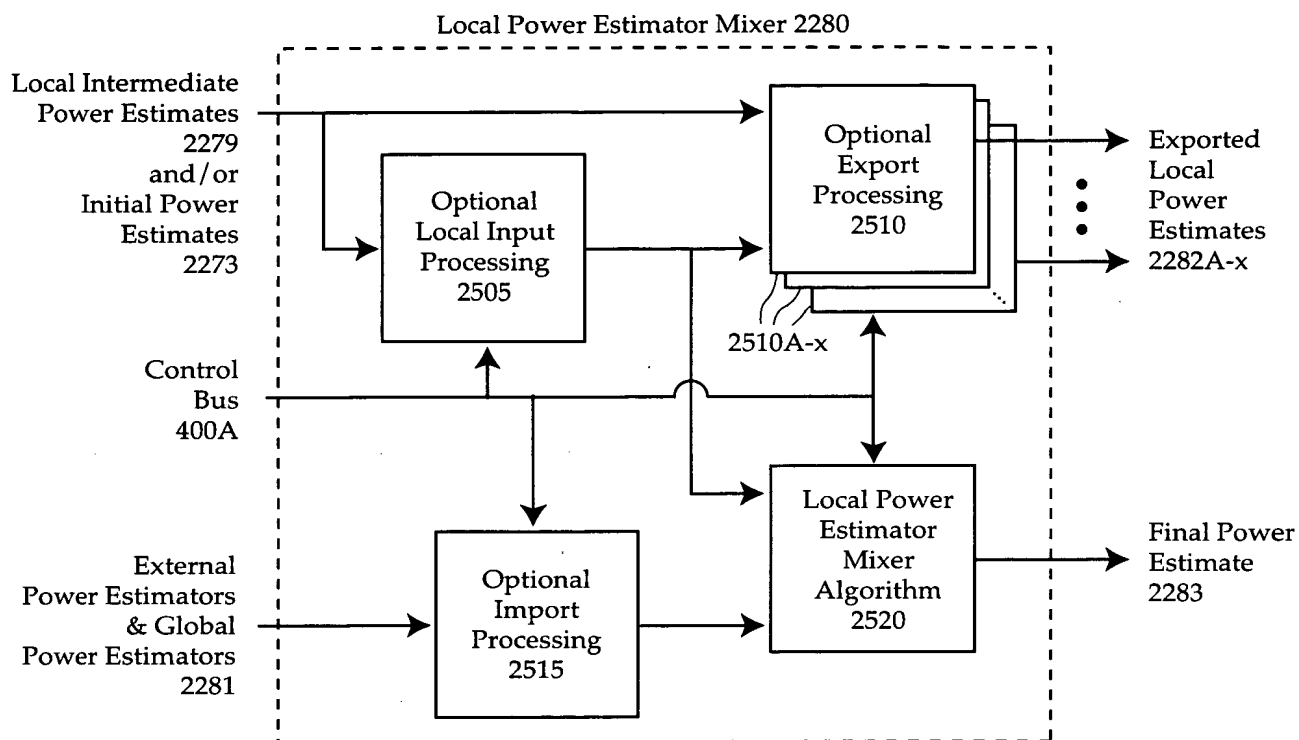


FIG. 25A

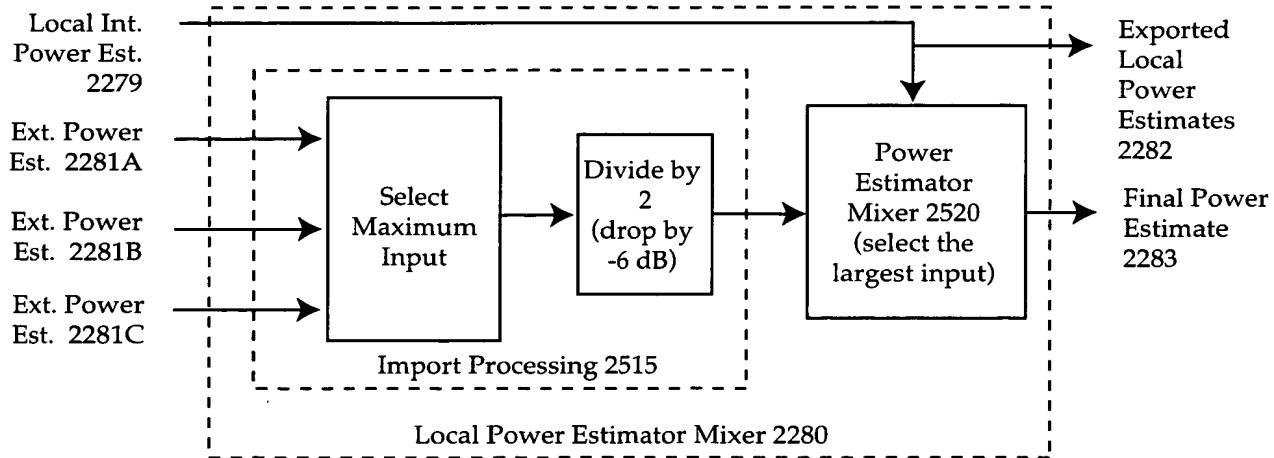


FIG. 25B

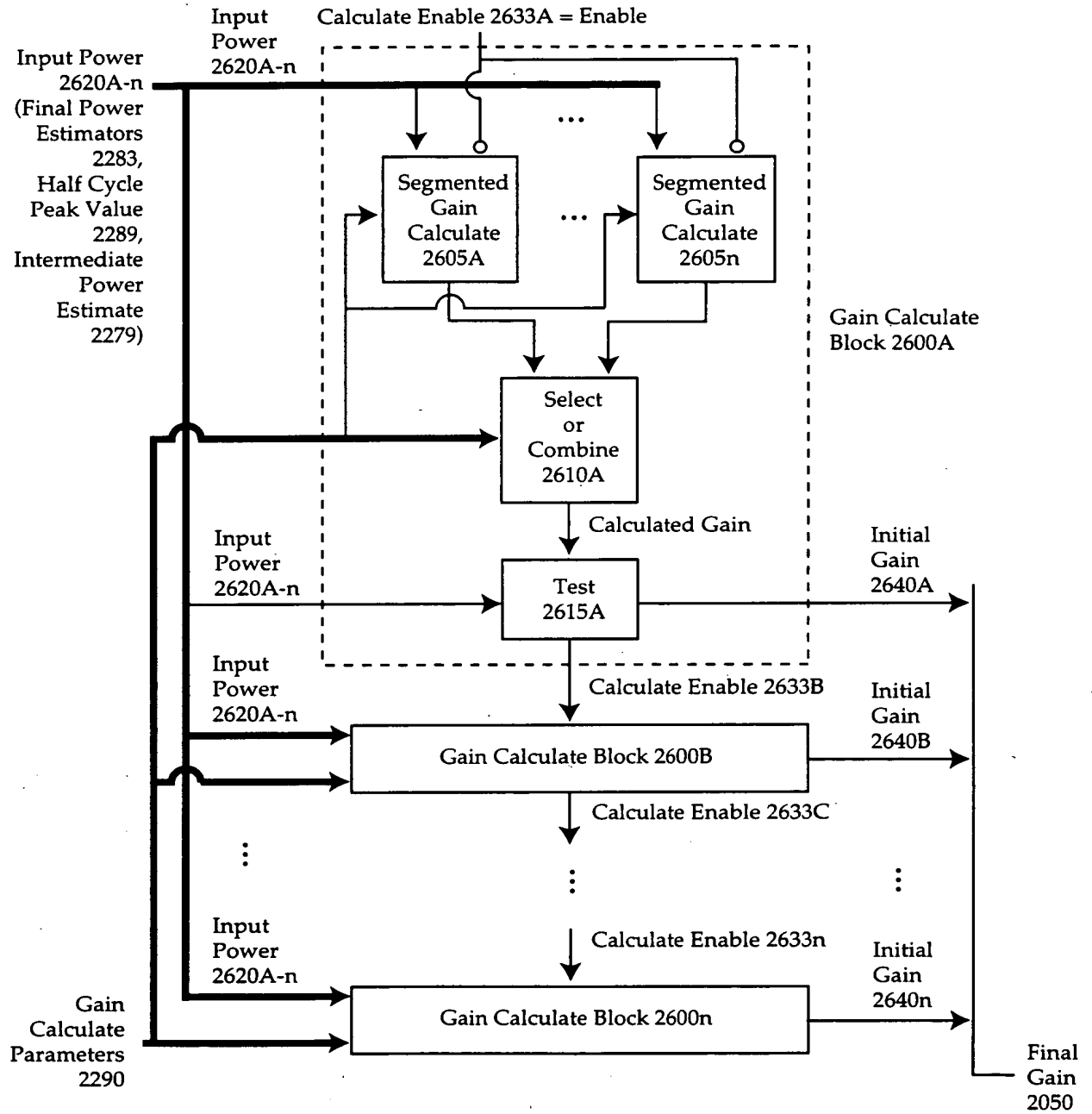


FIG. 26A

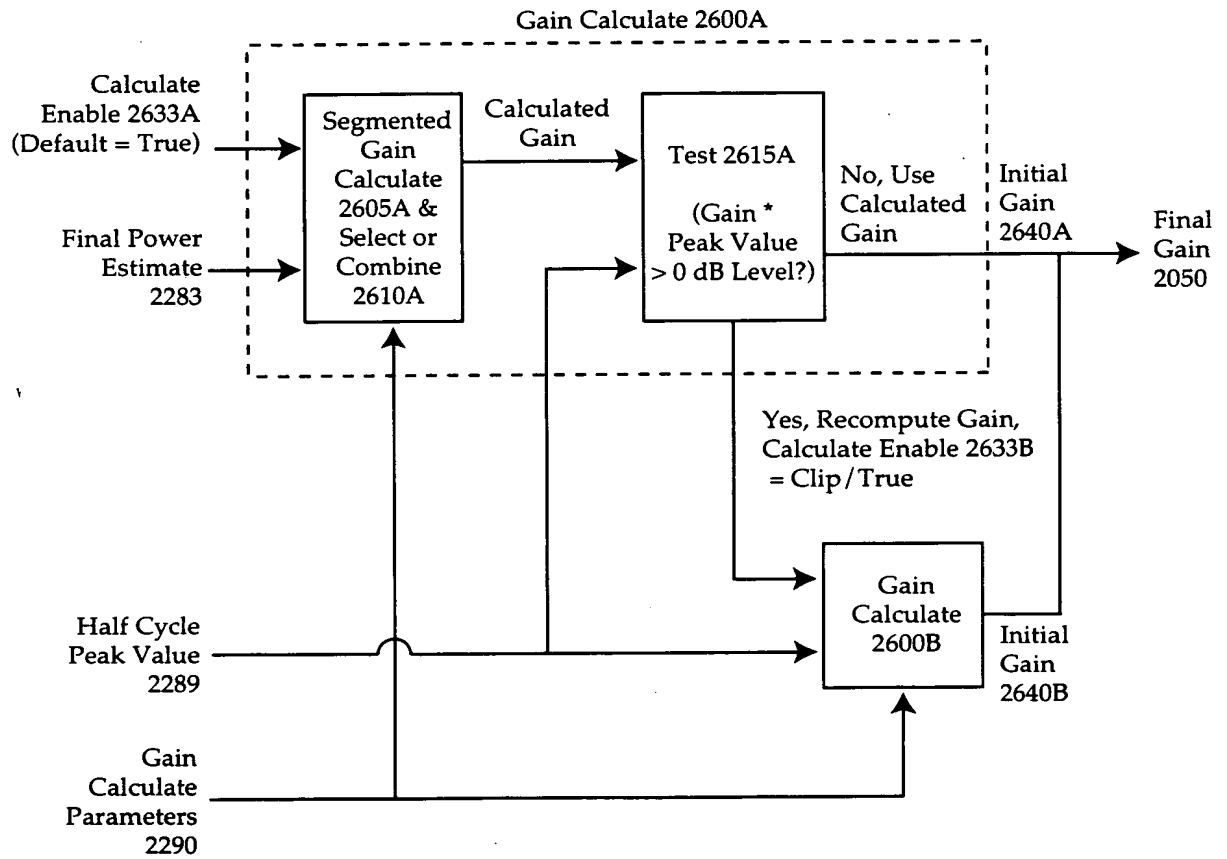


FIG. 26B

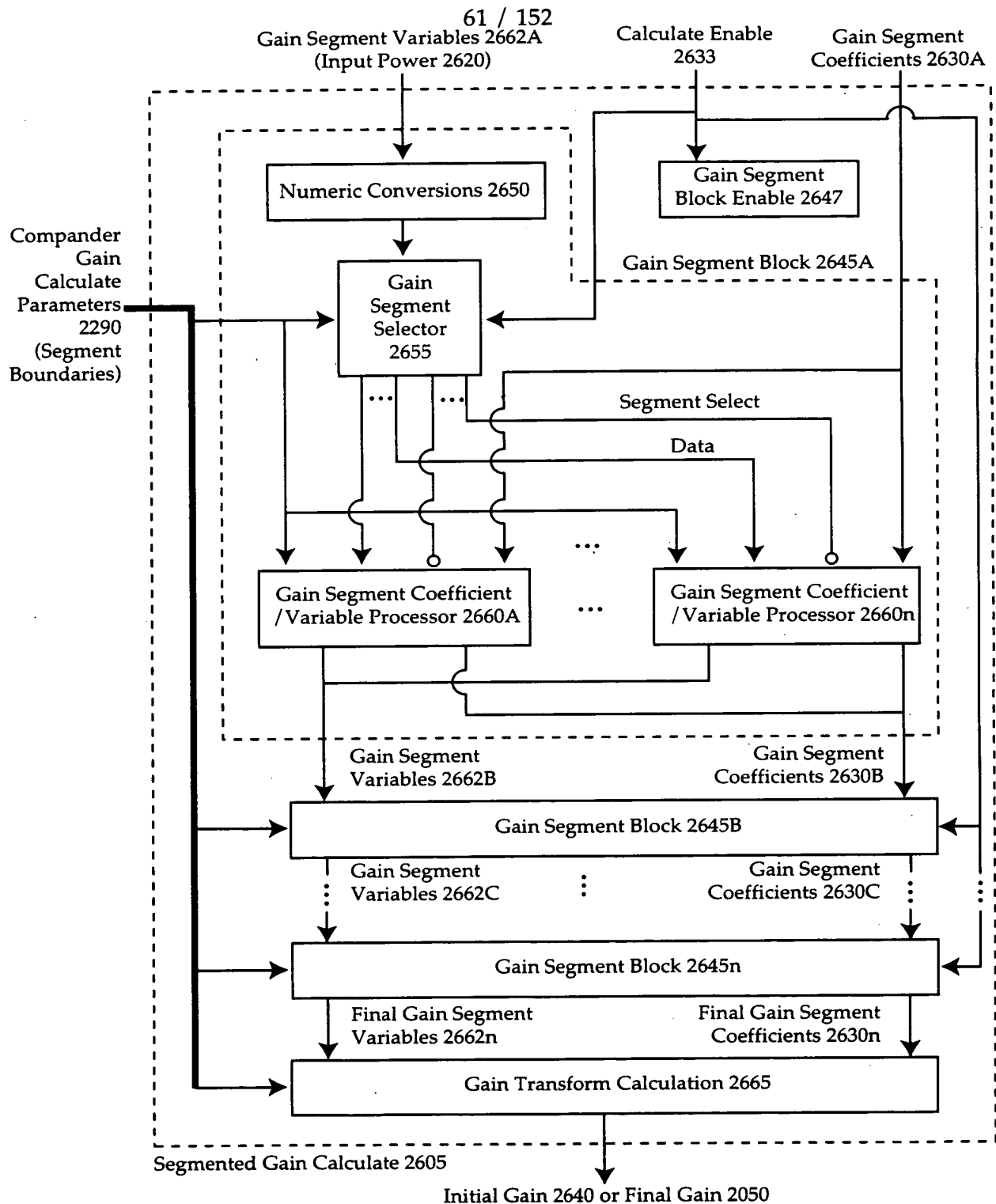


FIG. 26C

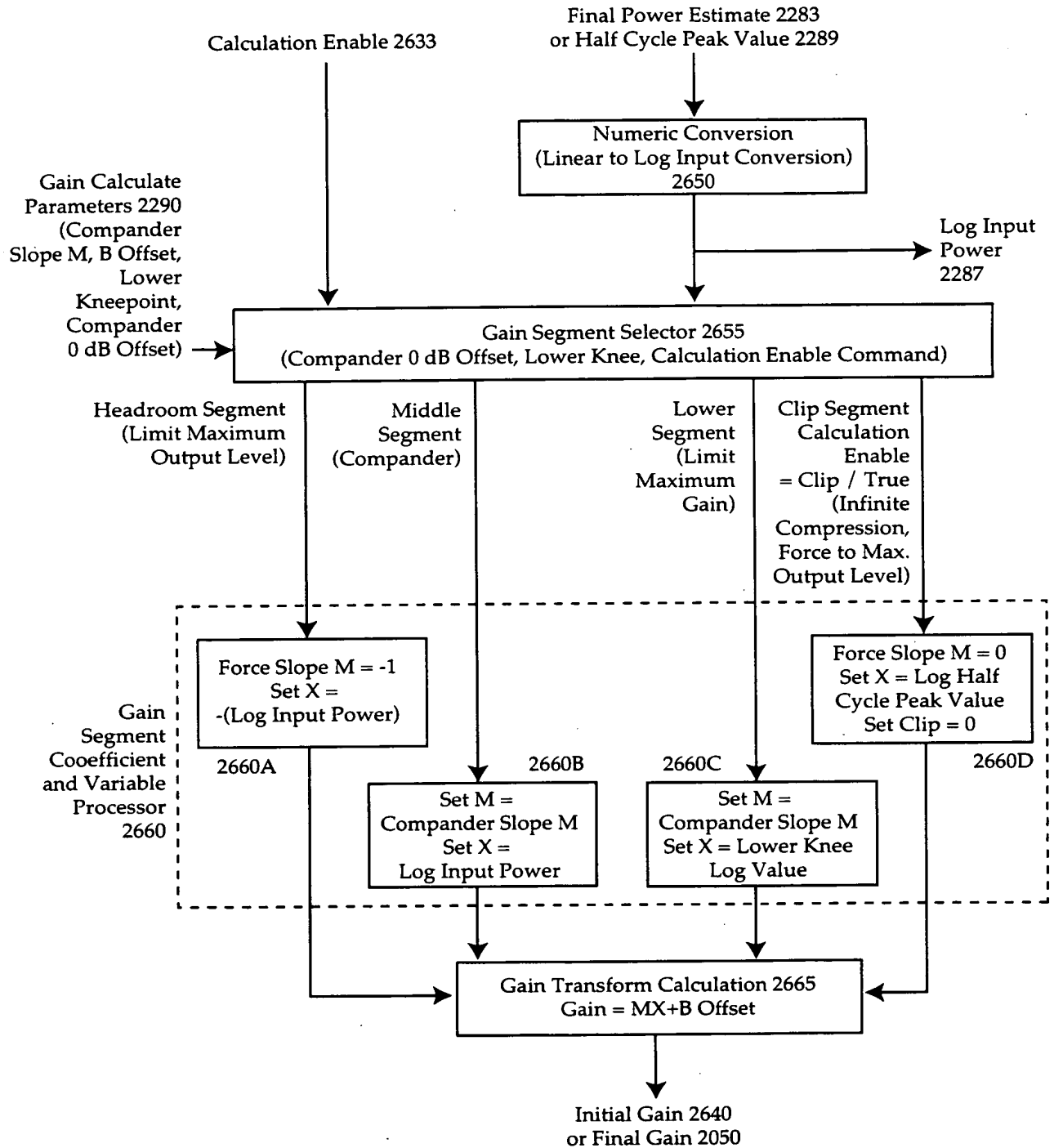


FIG. 26D

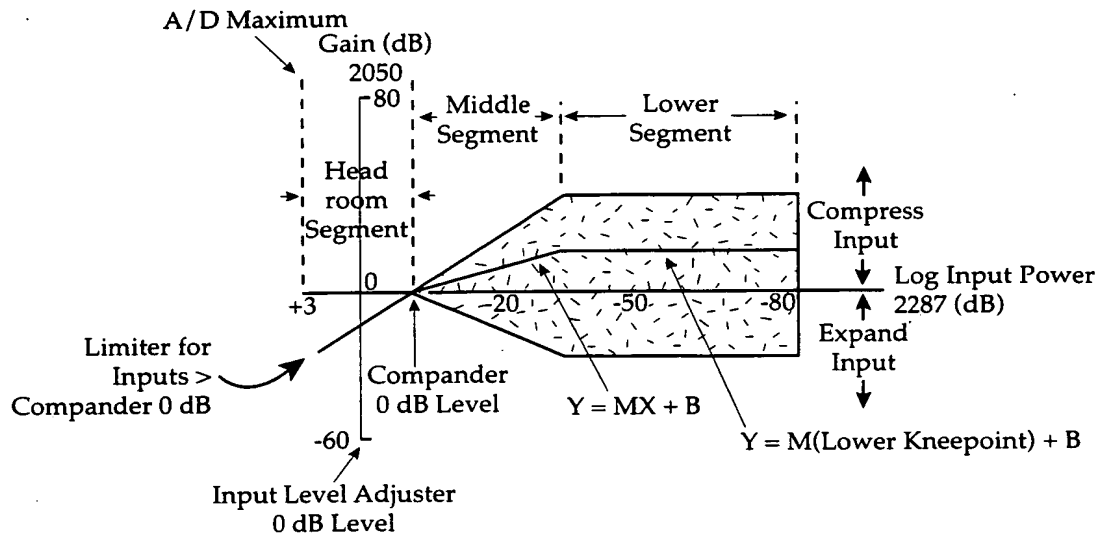


FIG. 26E

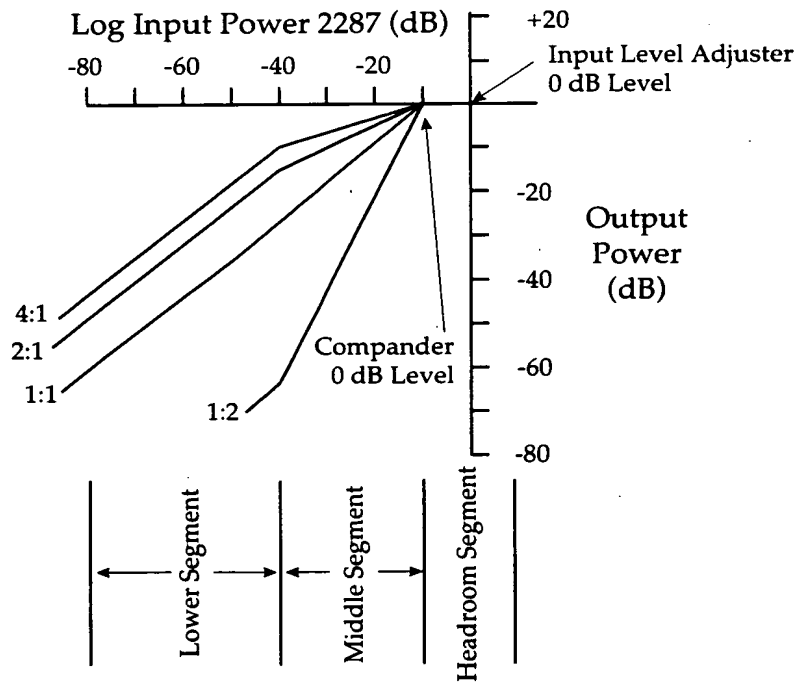


FIG. 26F

006215-112000

$$Y = MX + B$$

= Gain Table Address

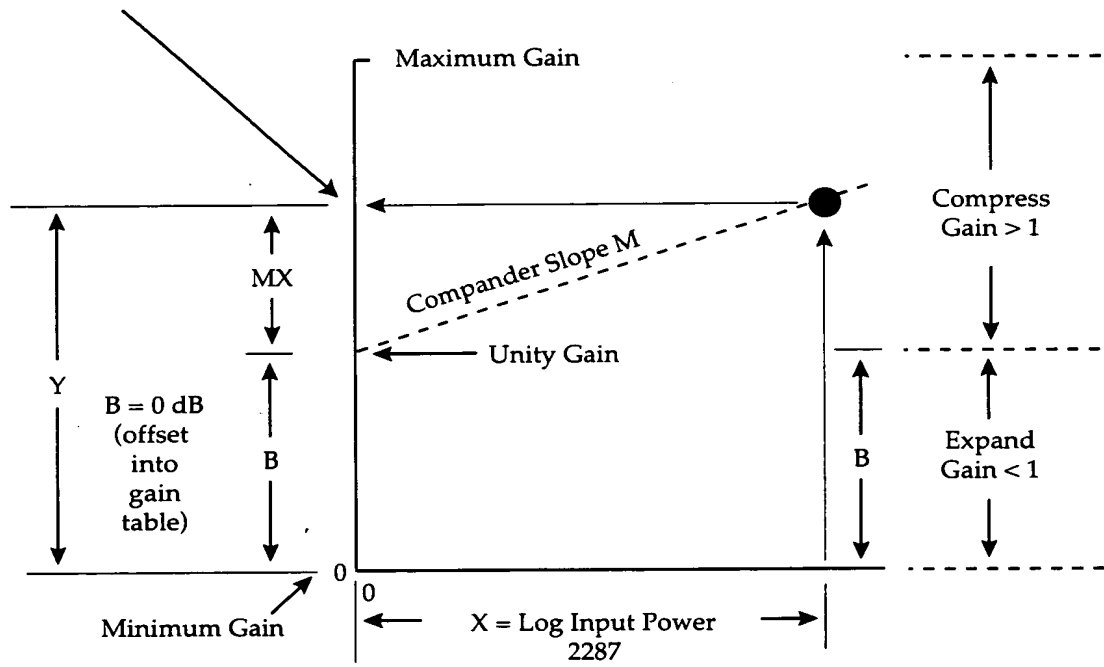


FIG. 26G

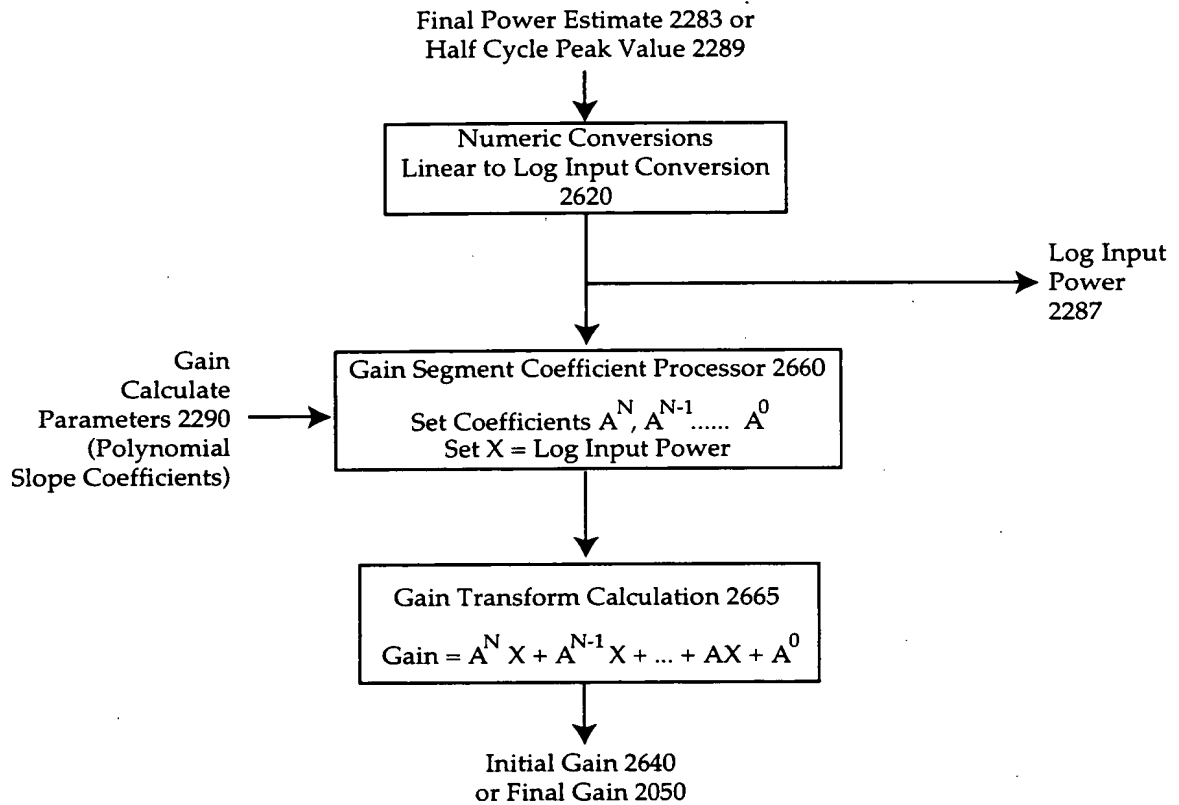


FIG. 26H

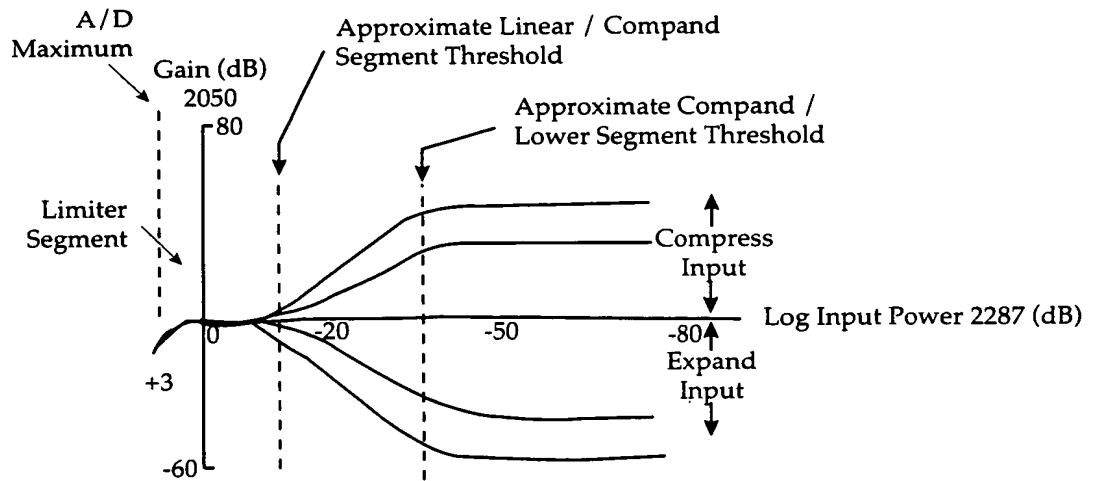


FIG. 26I

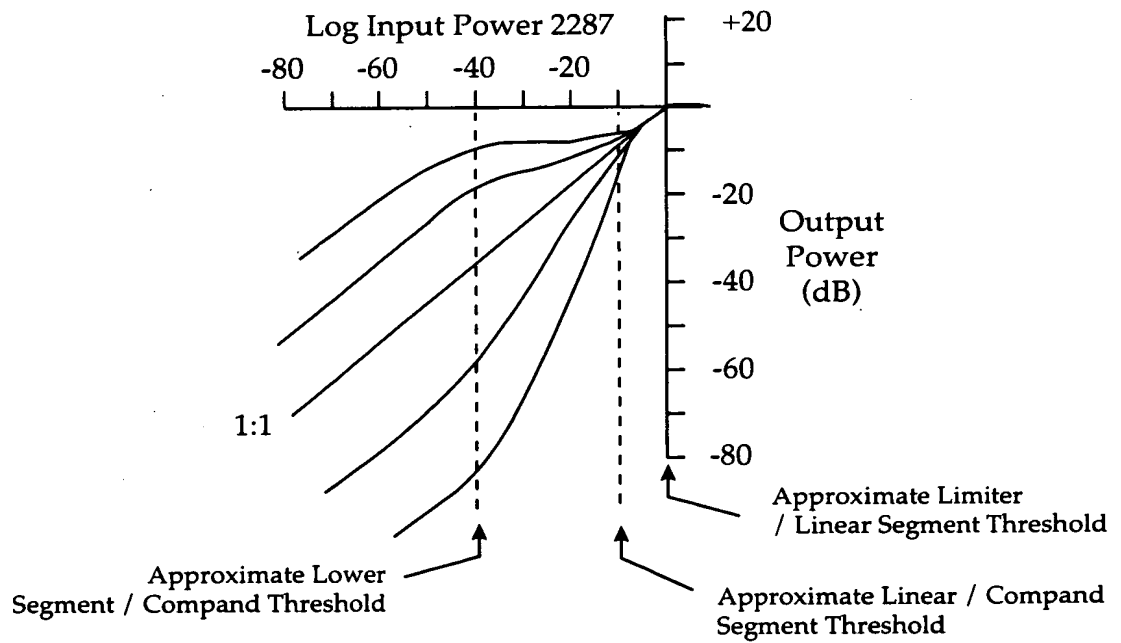


FIG. 26J

00728215-112900

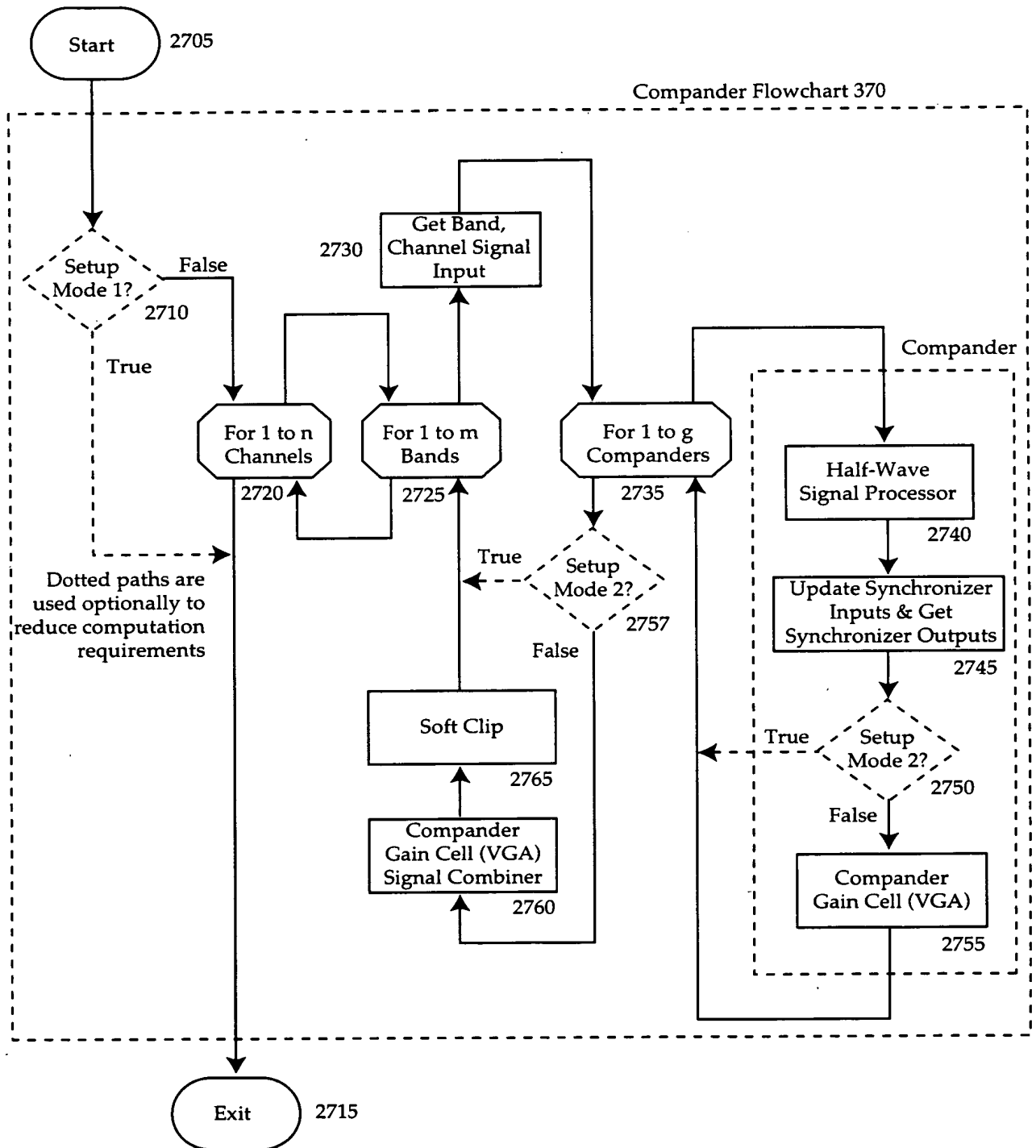


FIG. 27A

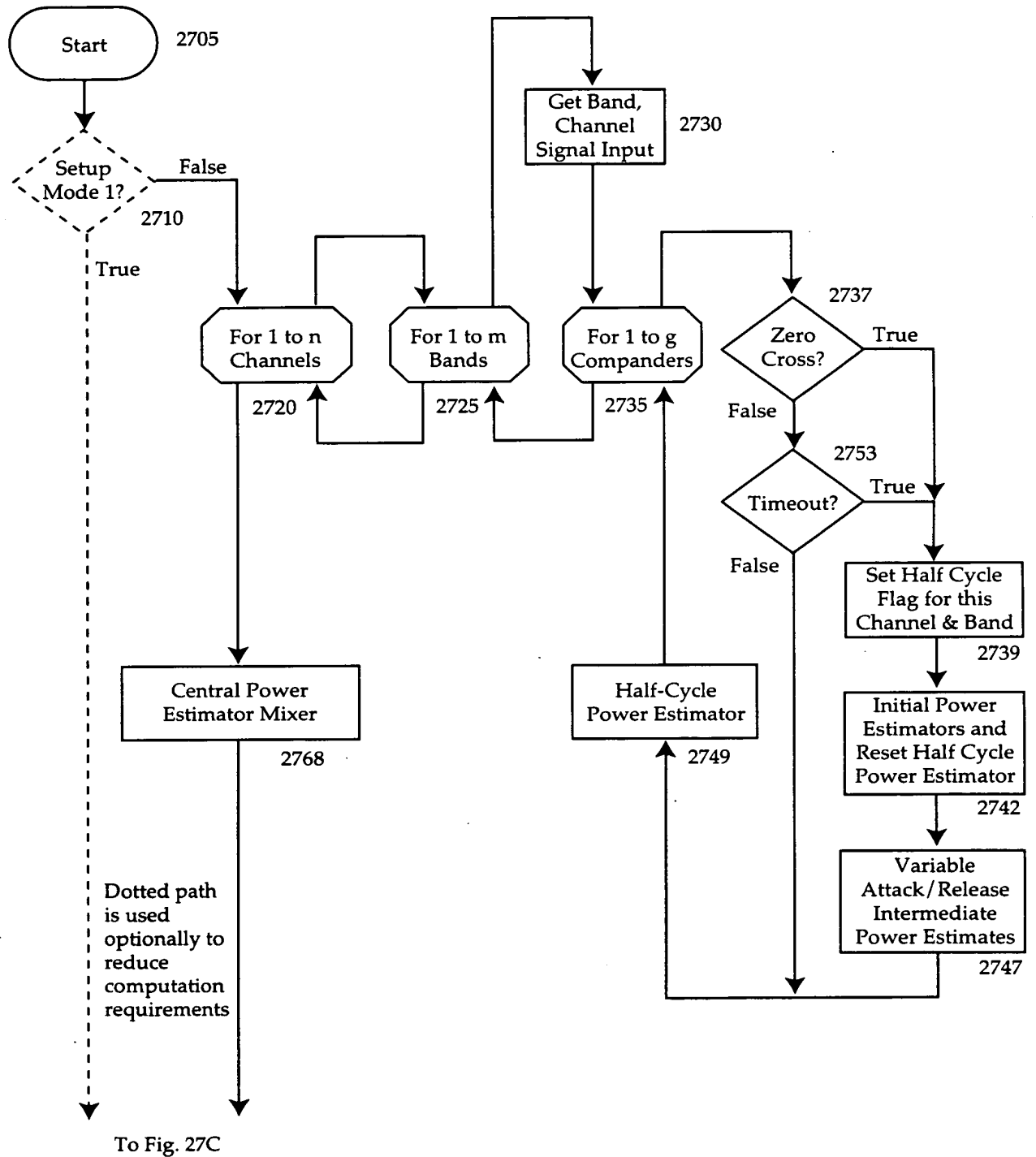


FIG. 27B

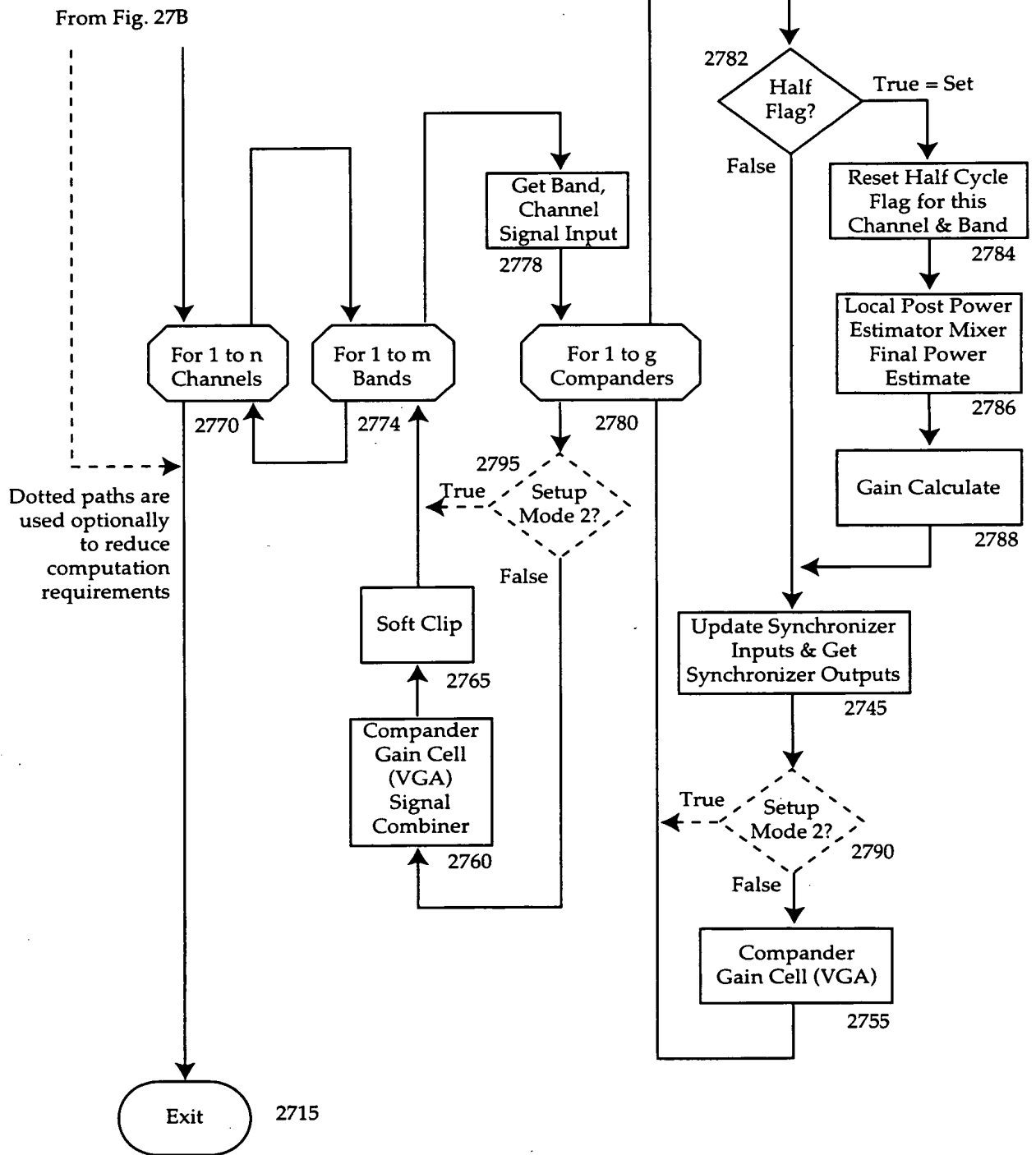


FIG. 27C

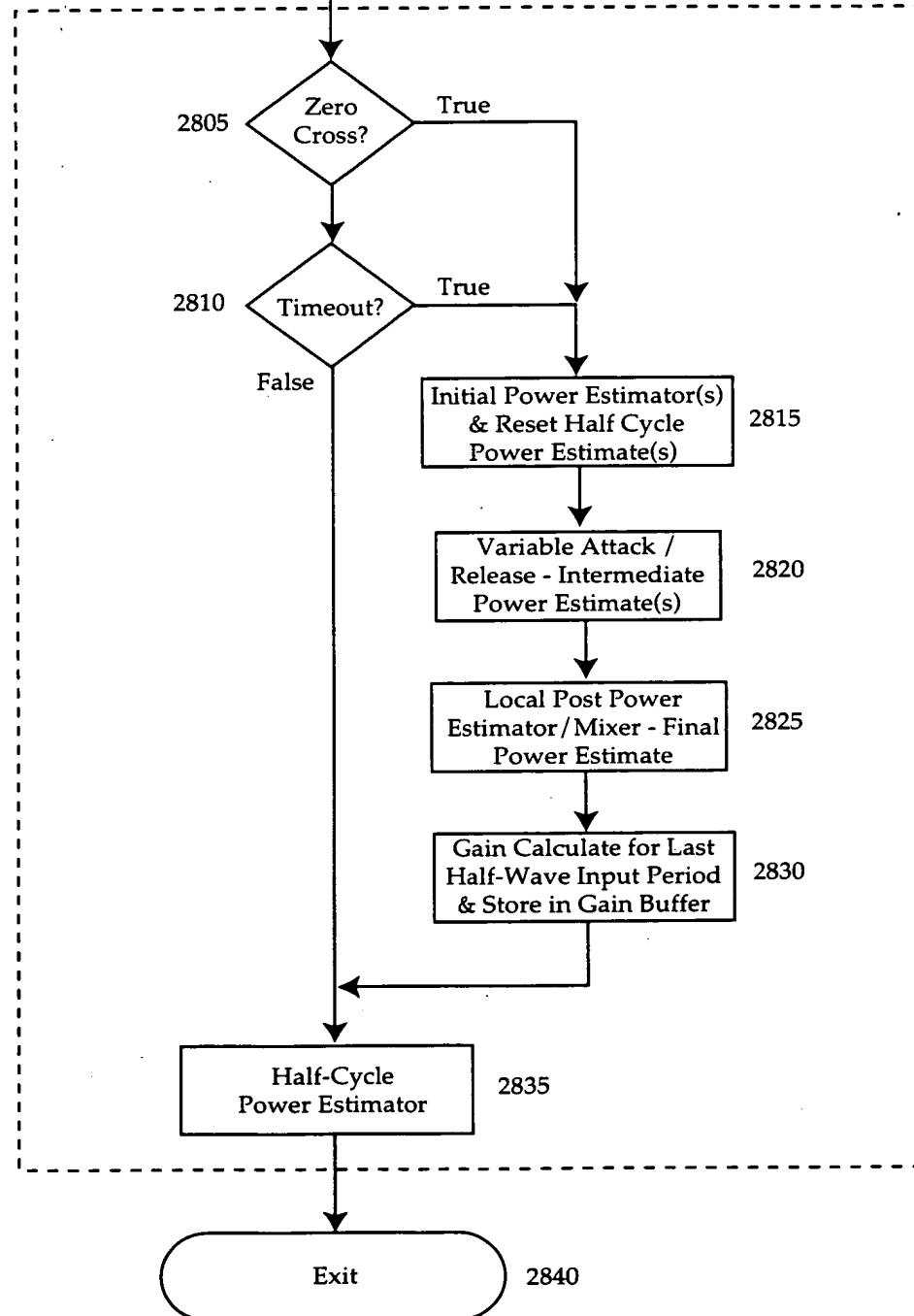


FIG. 28

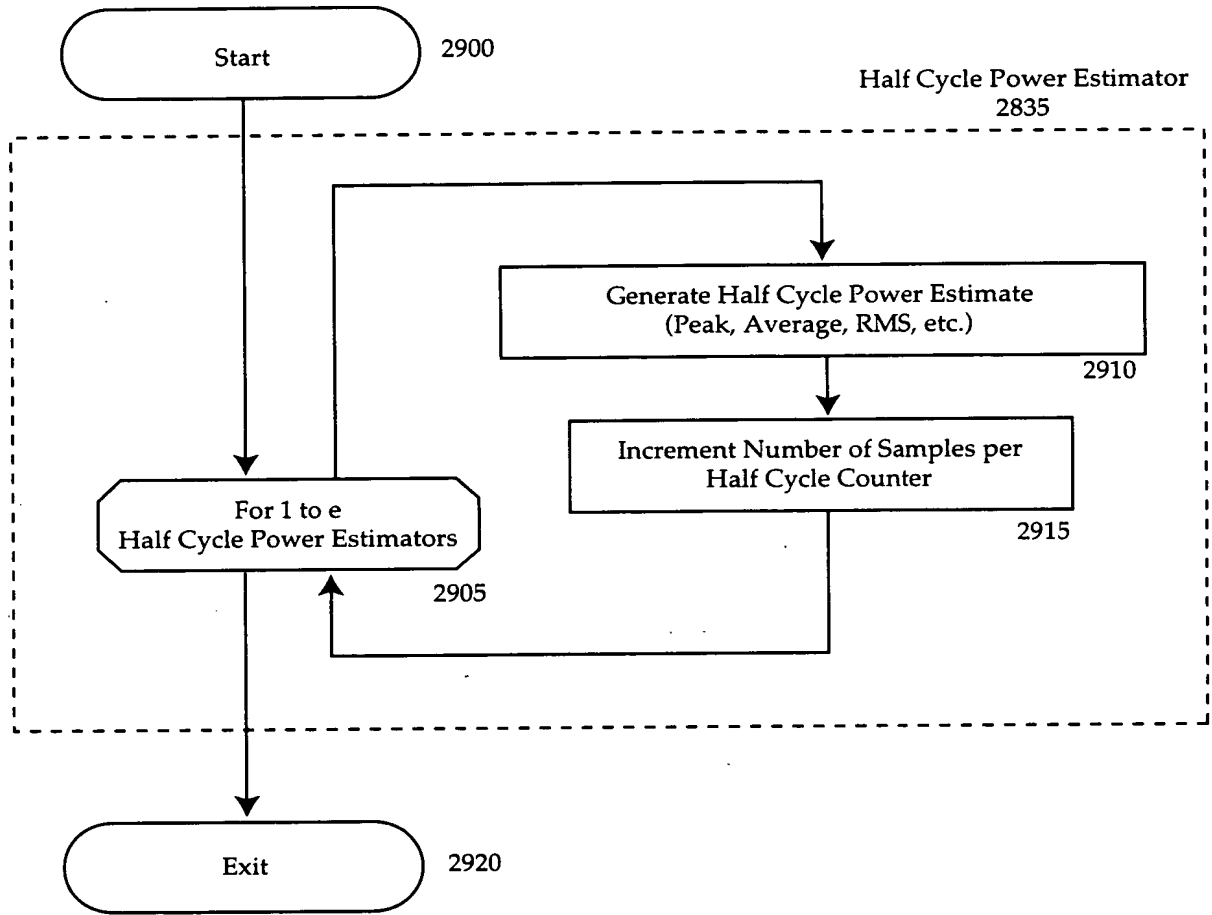


FIG. 29

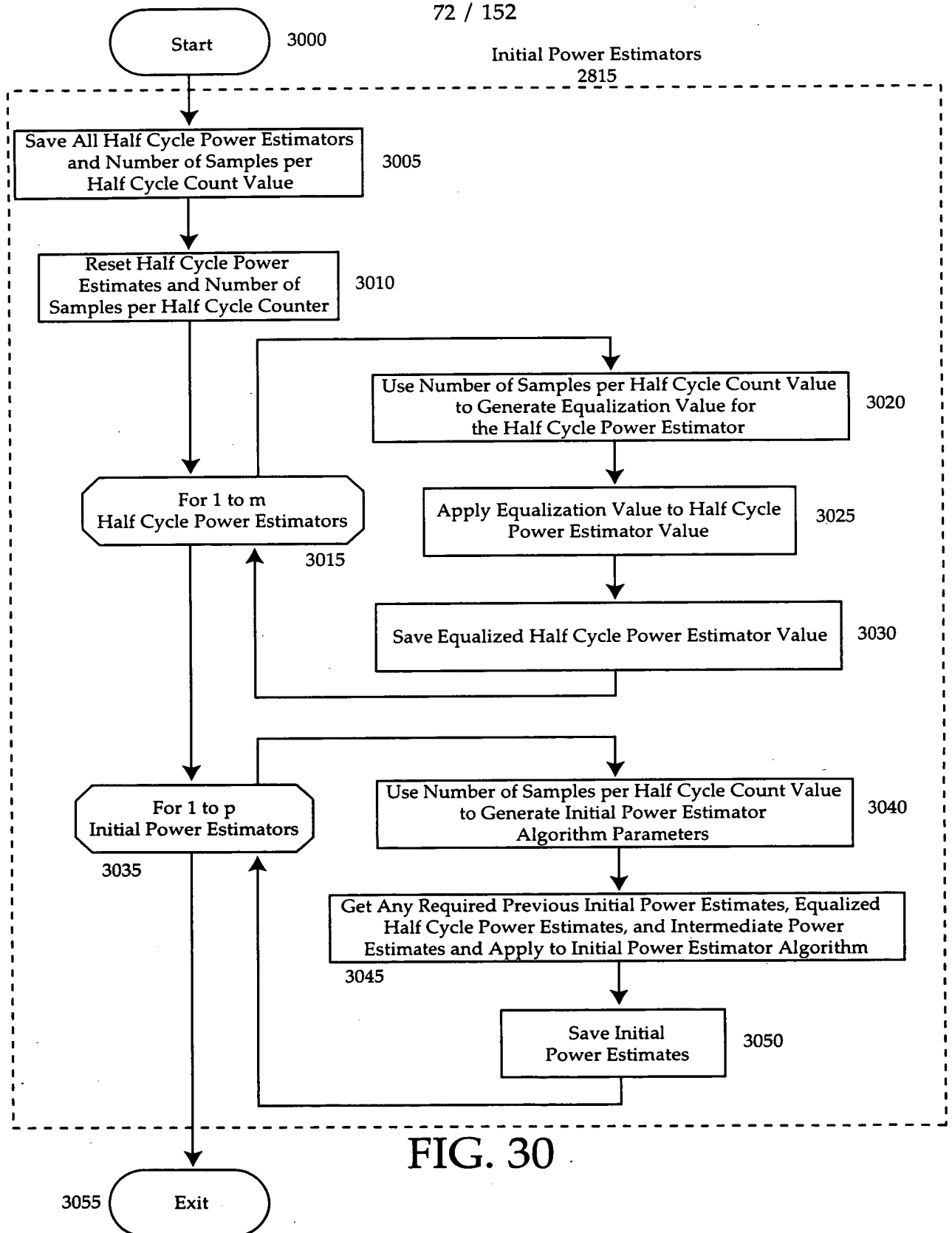
Initial Power Estimators
2815

FIG. 30

00621-5122/60

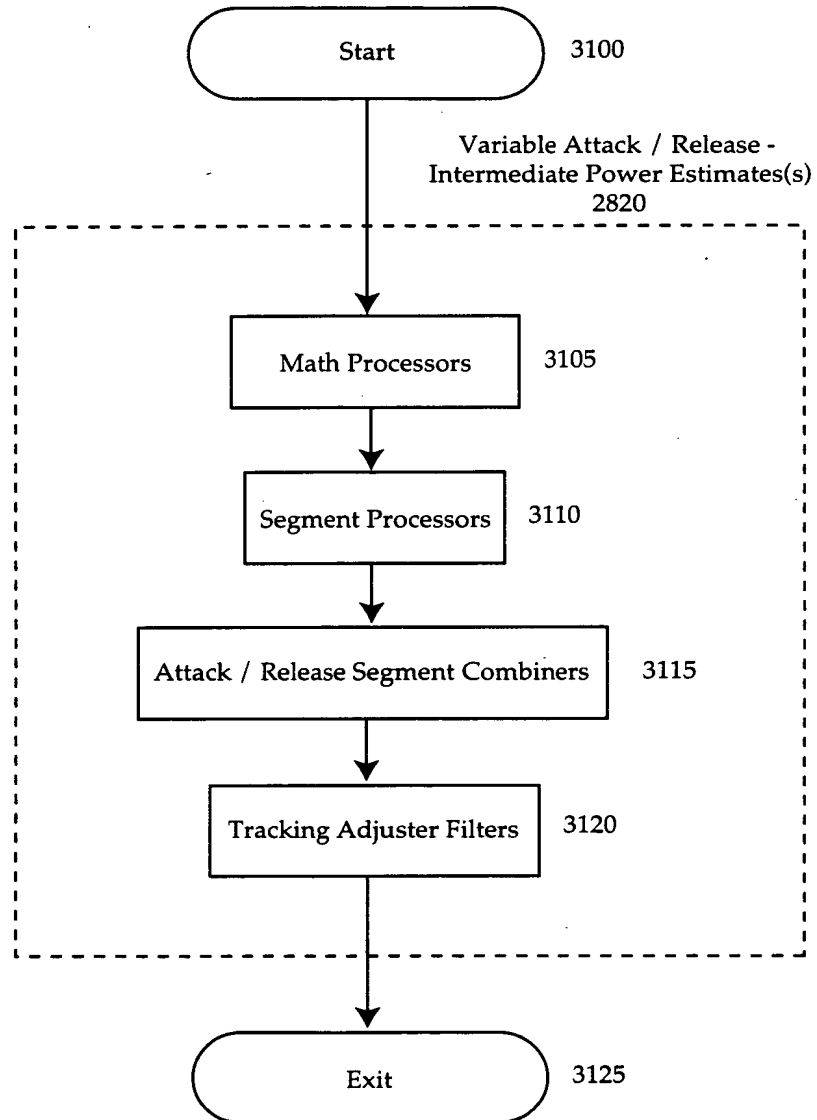
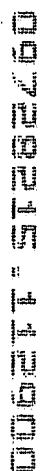


FIG. 31



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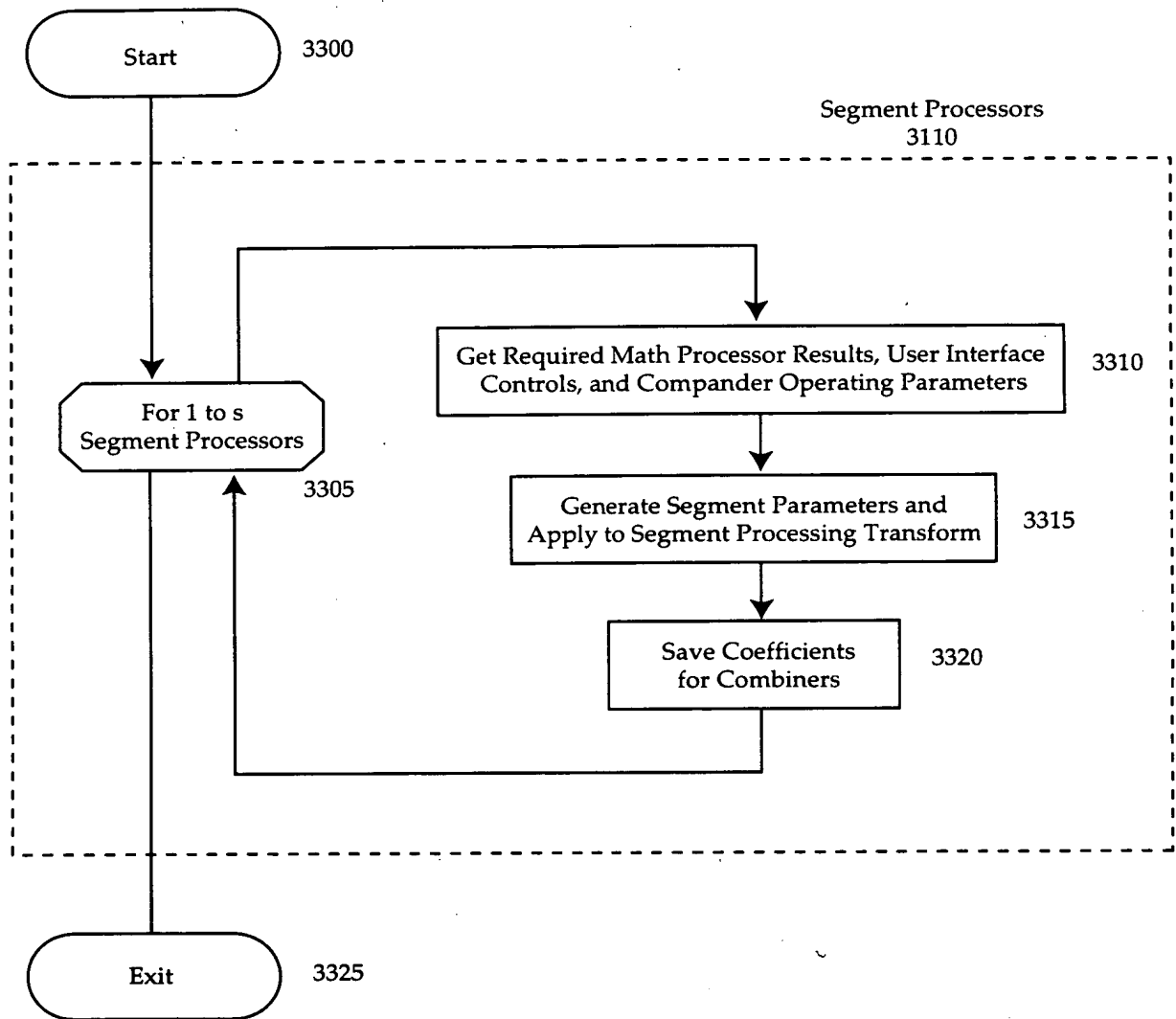


FIG. 33

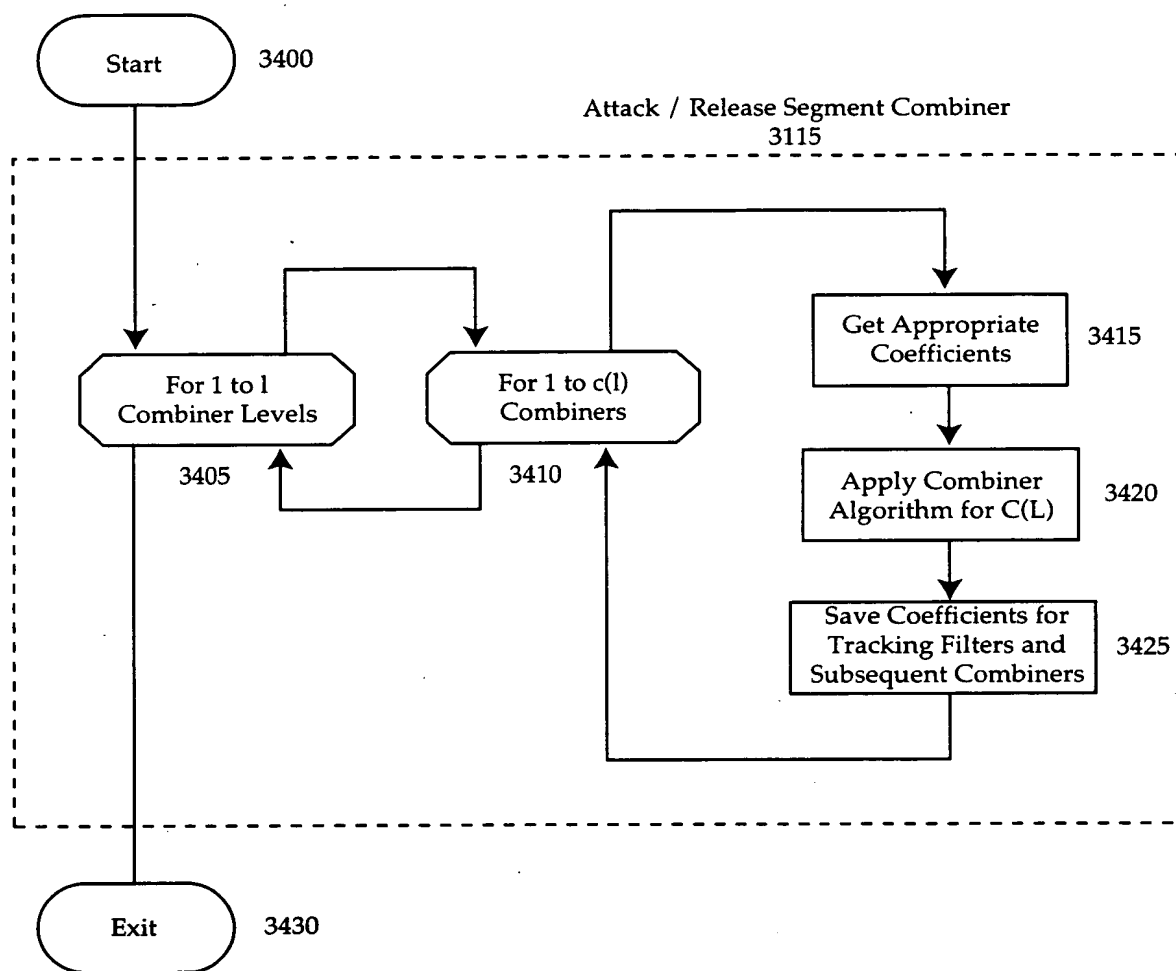
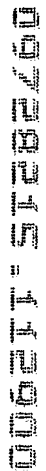


FIG. 34



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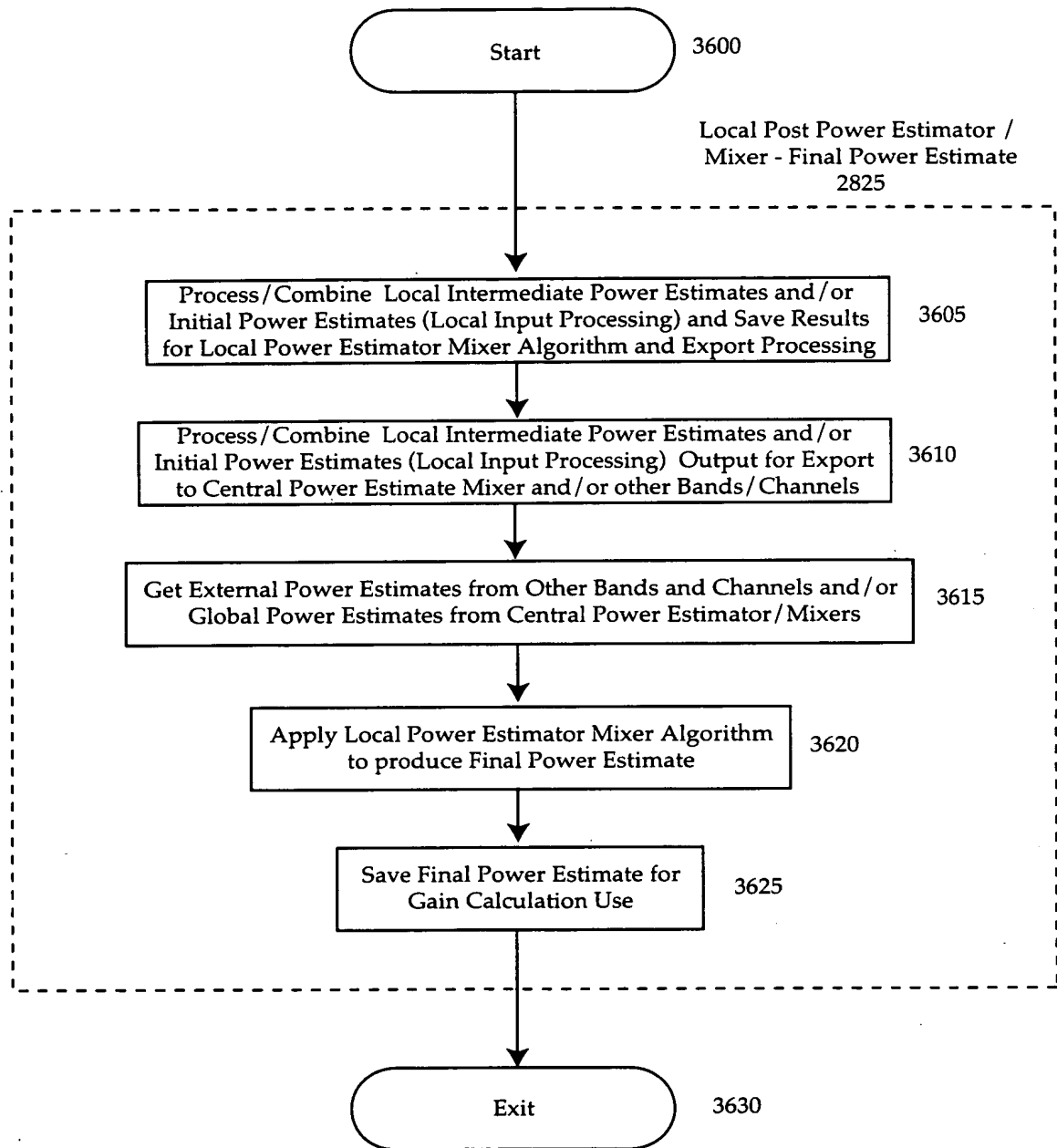


FIG. 36

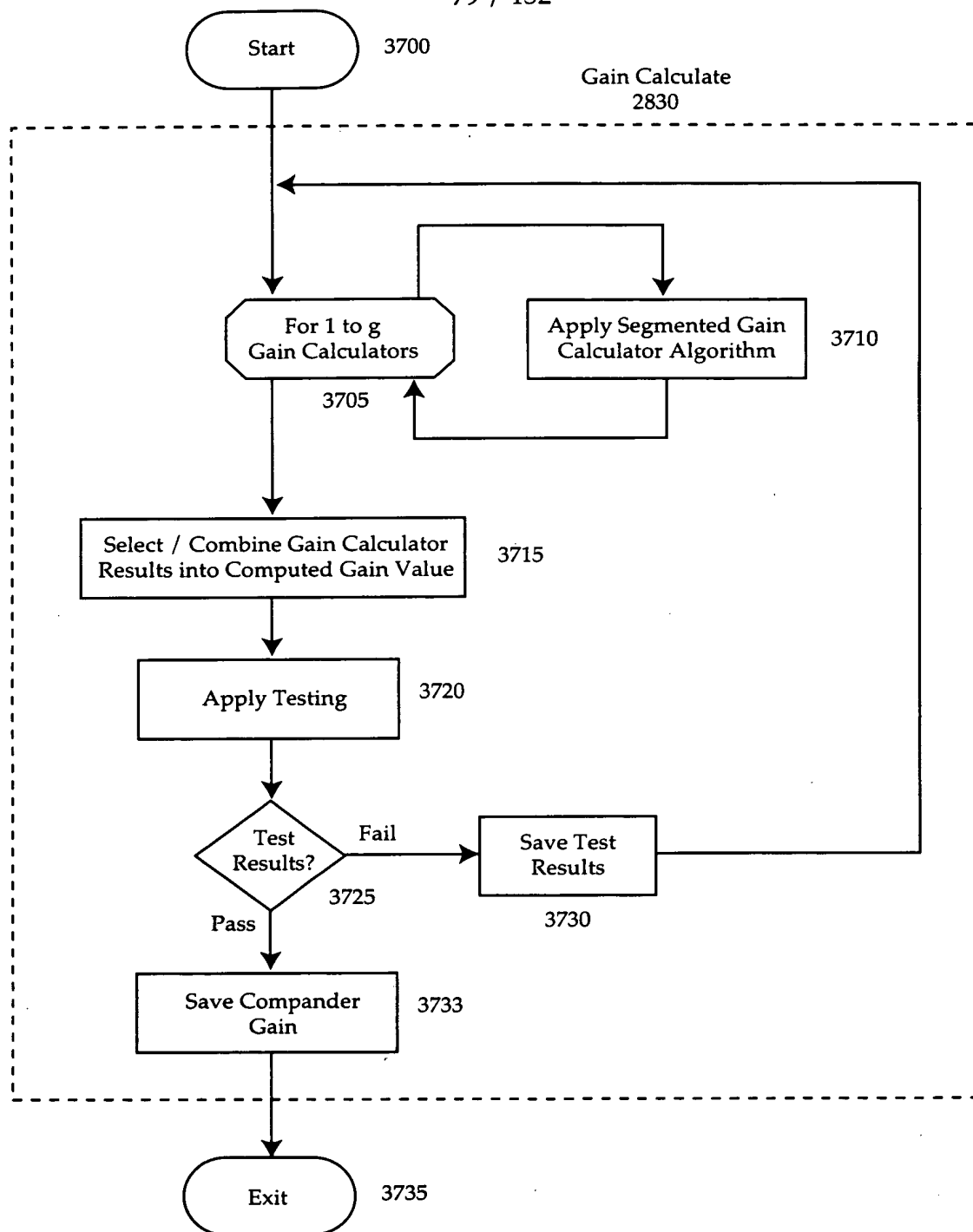


FIG. 37A

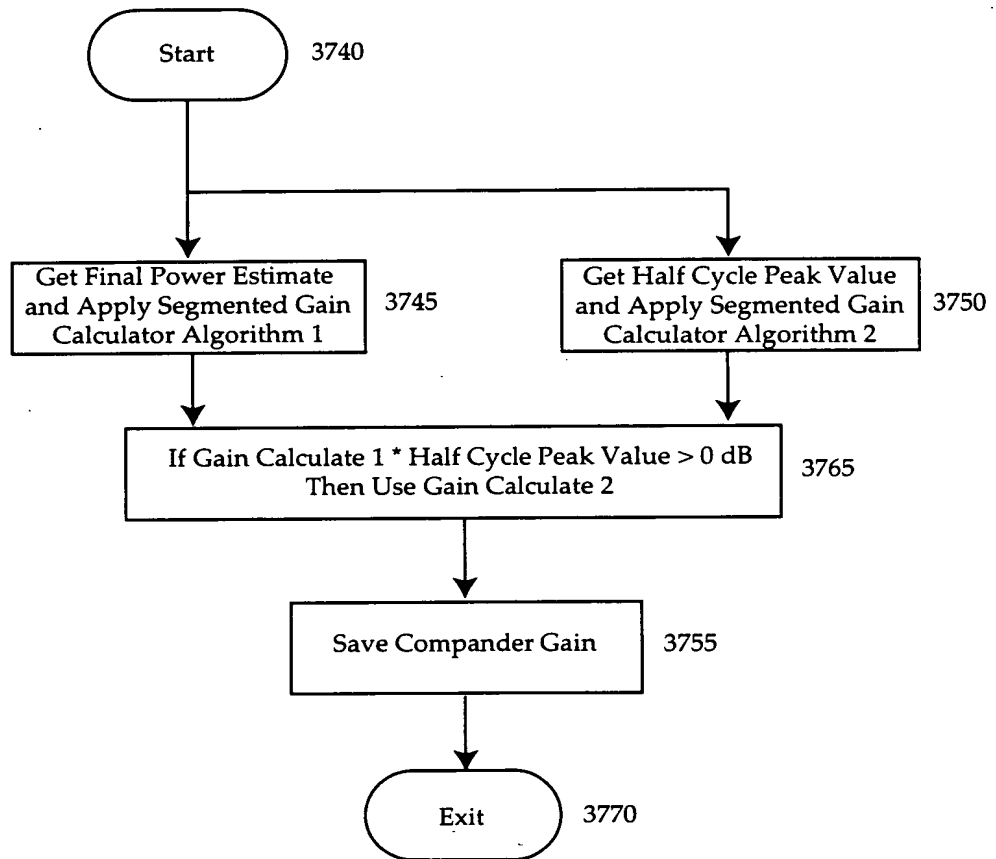


FIG. 37B

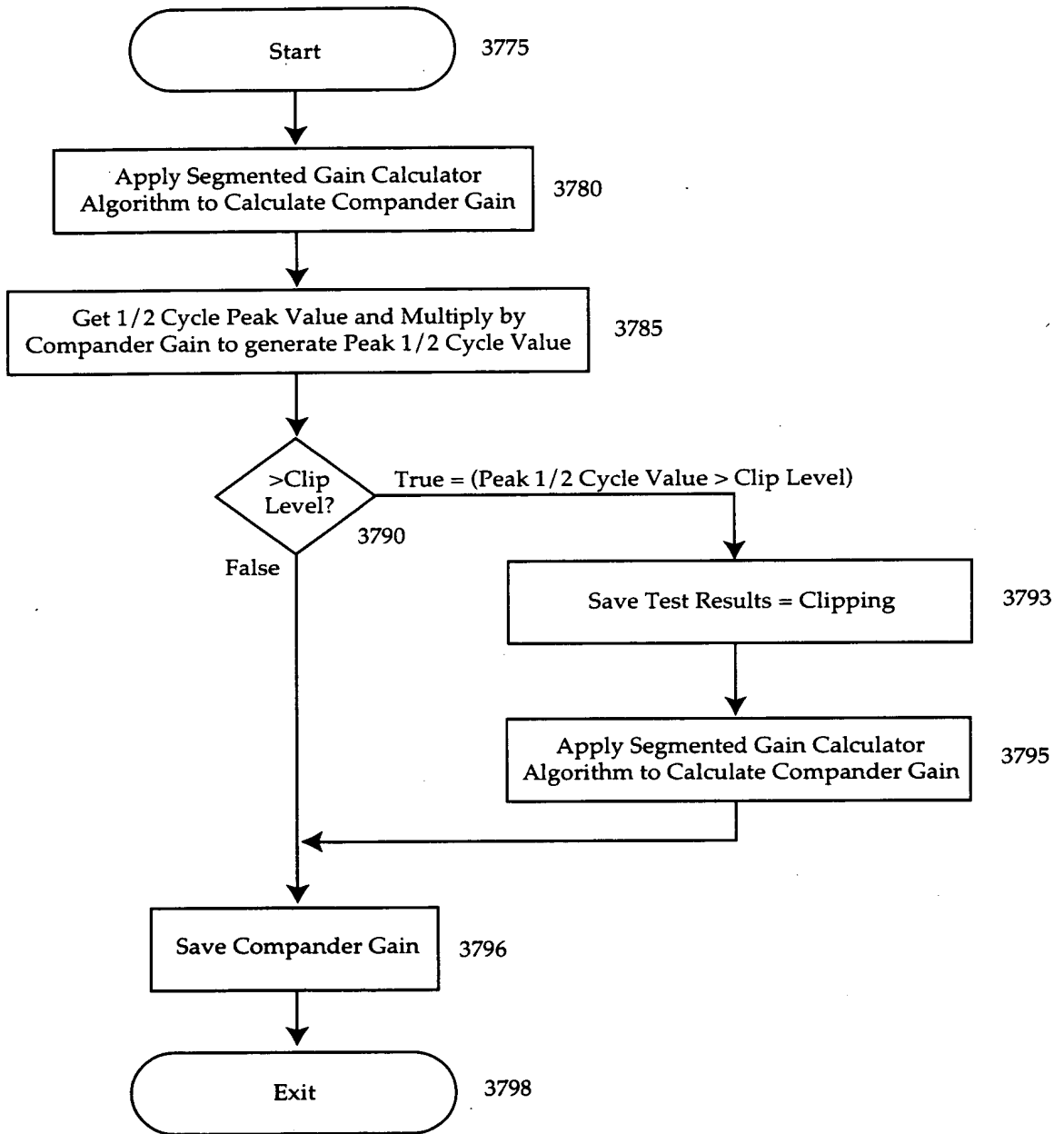


FIG. 37C



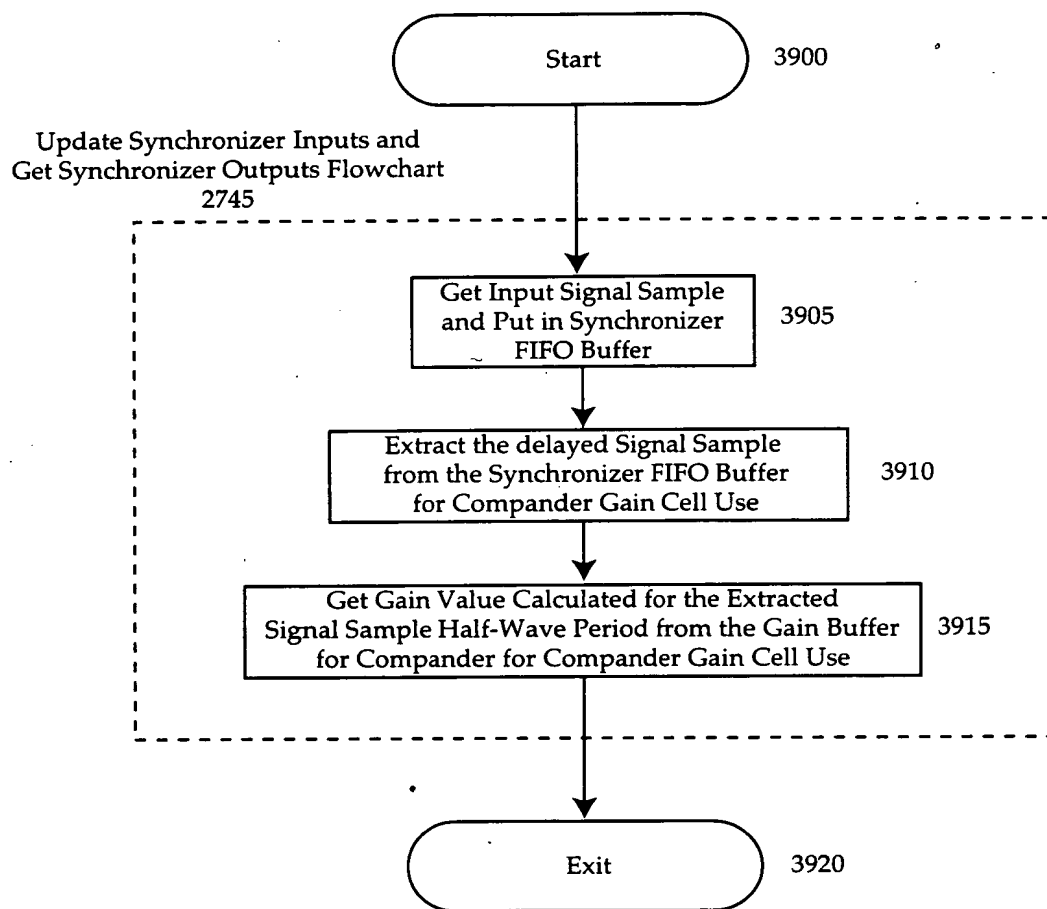


FIG. 39

Softclip 2765

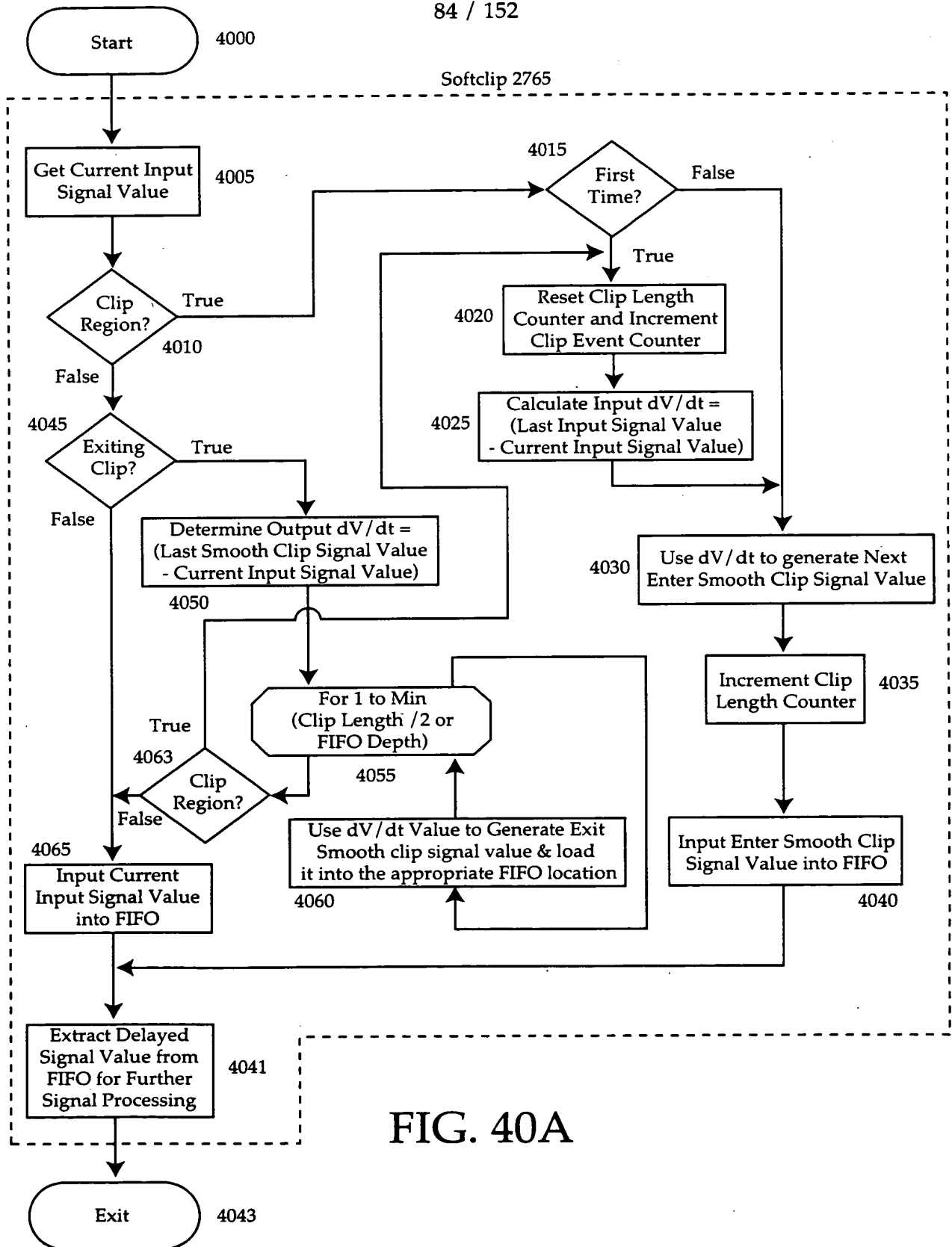


FIG. 40A

00728215-112900

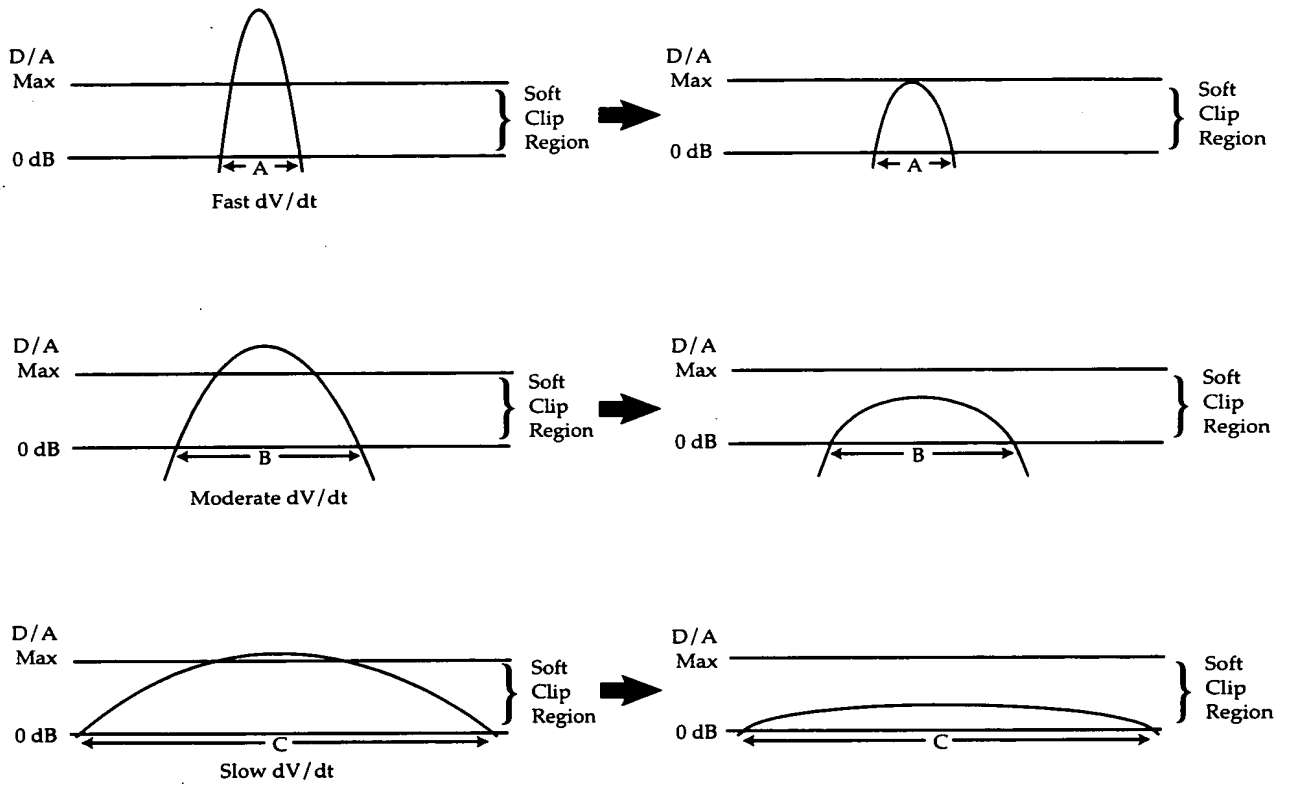


FIG. 40B

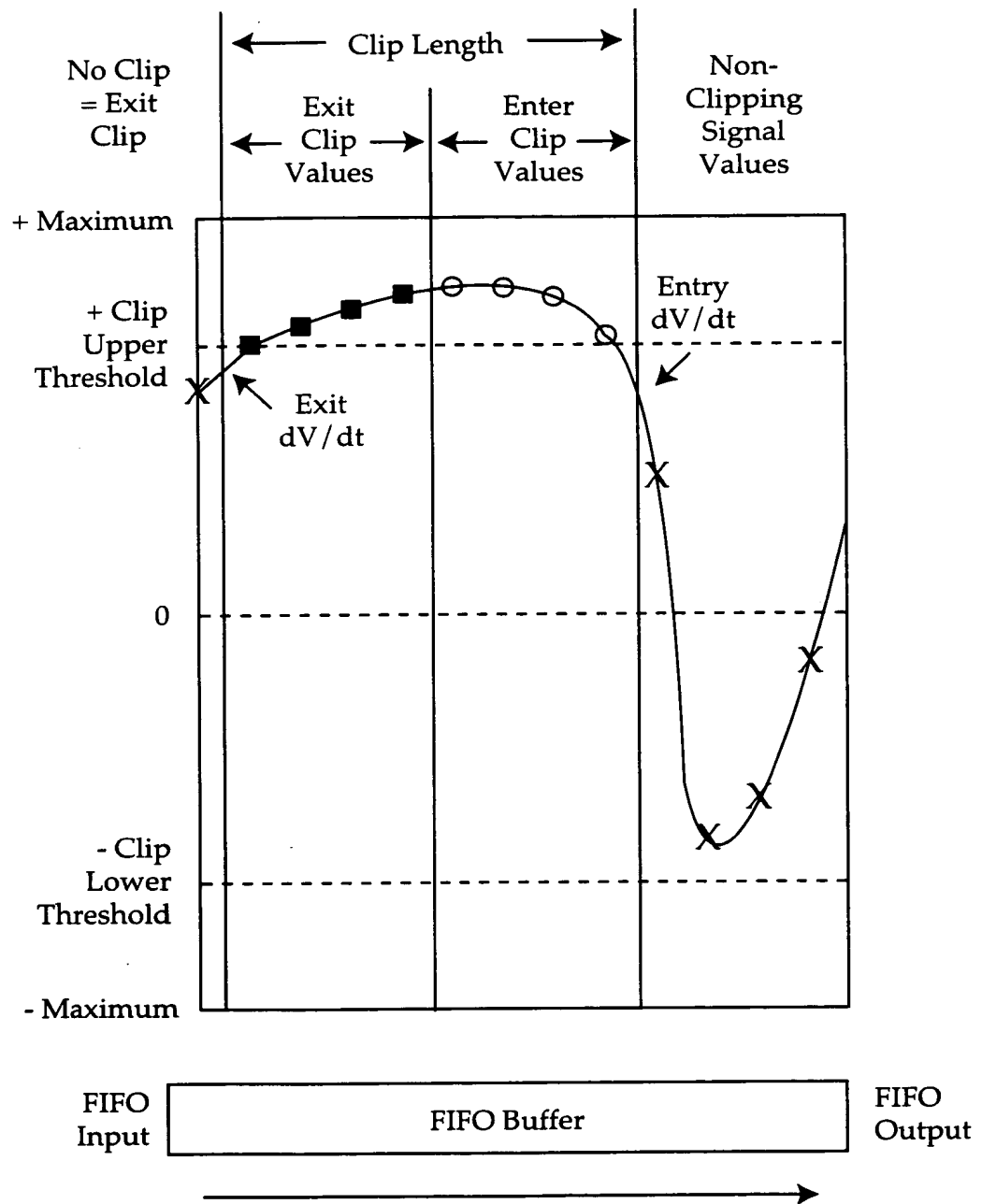


FIG. 40C

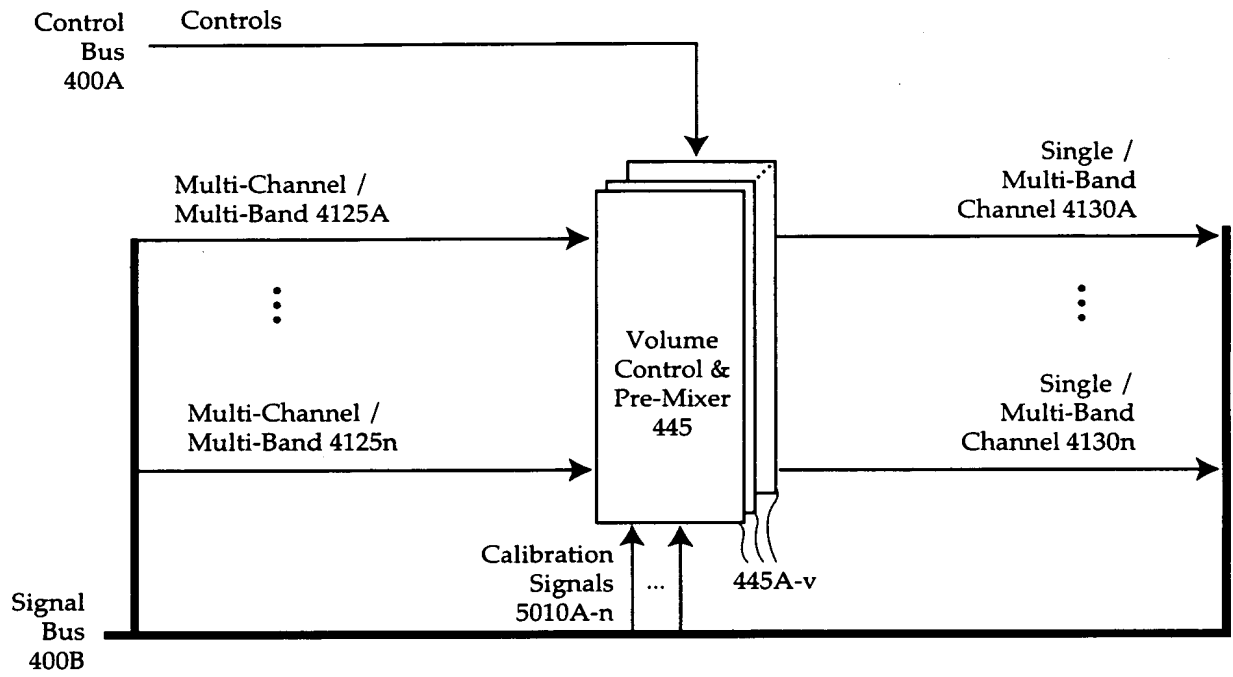


FIG. 41A

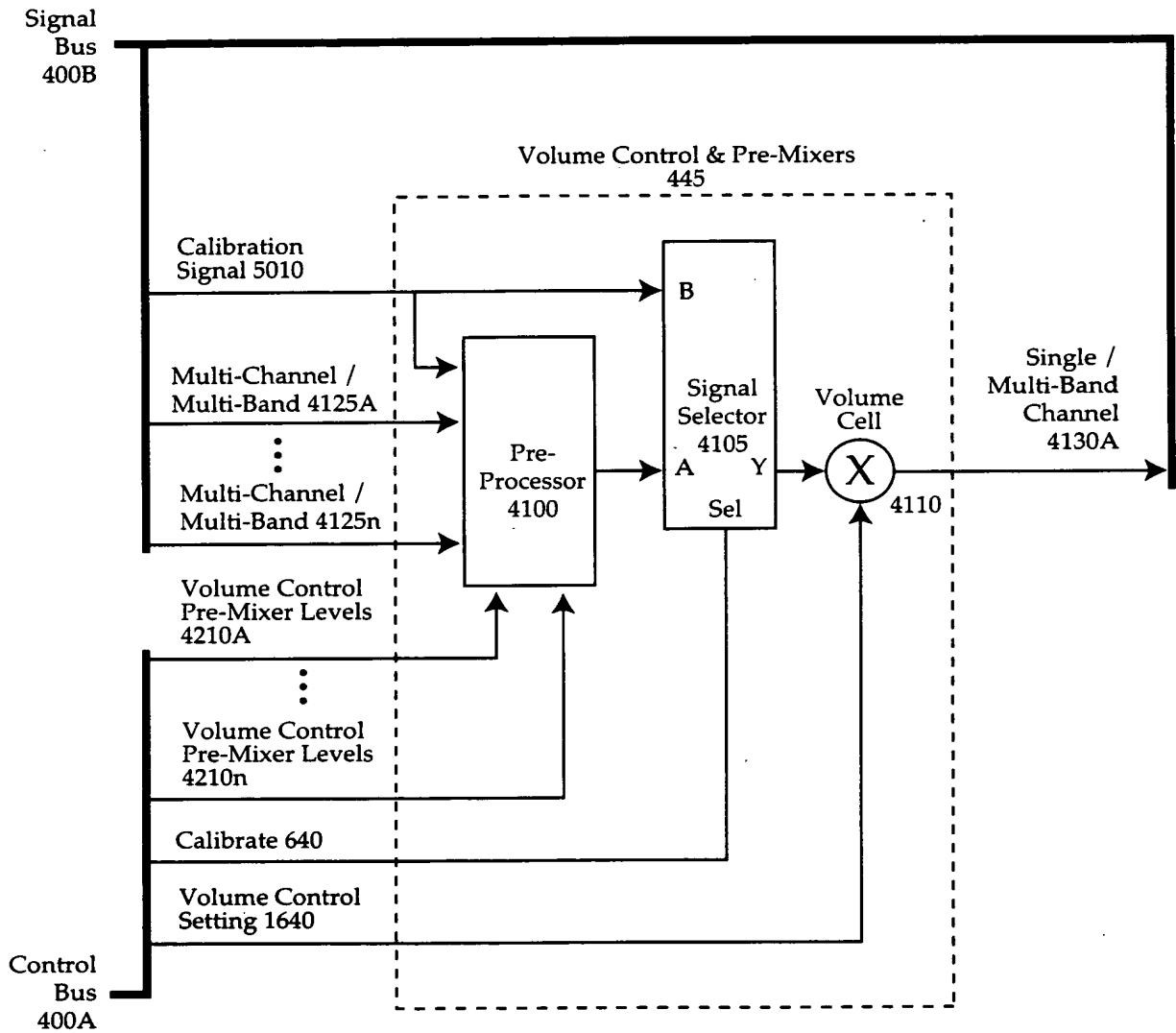


FIG. 41B

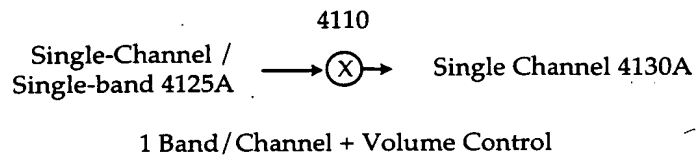


FIG. 42A

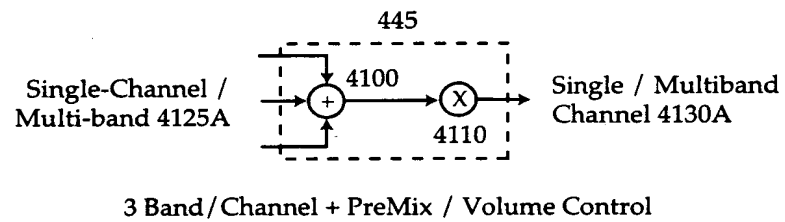
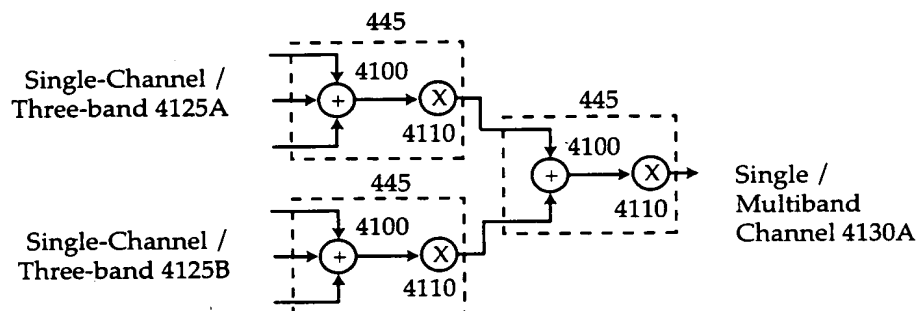


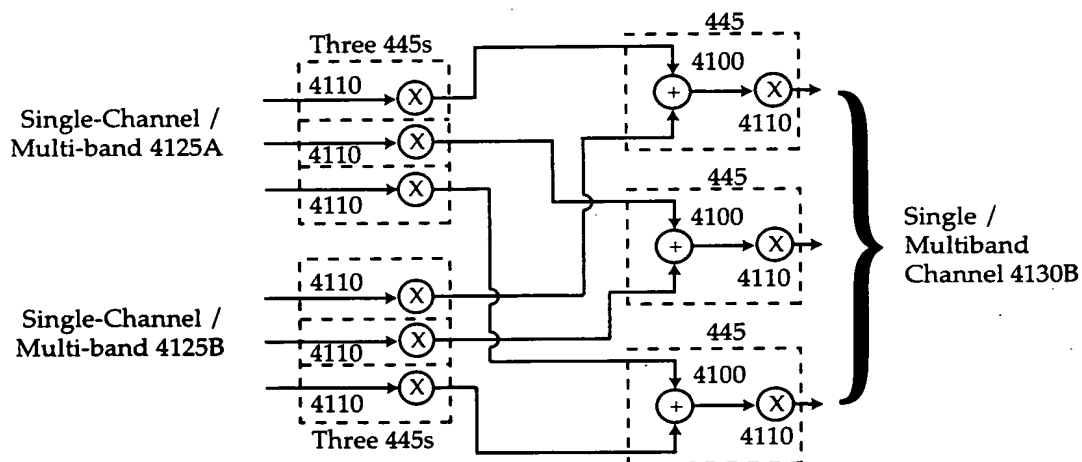
FIG. 42B

[illegible]



3 Band/Channel + Pre-Mix / Volume Control > Premix Volume Control

FIG. 42C



3 Band/Channel + 3 Volume Control > 3 Premix Volume Control

FIG. 42D

007-6215-1138

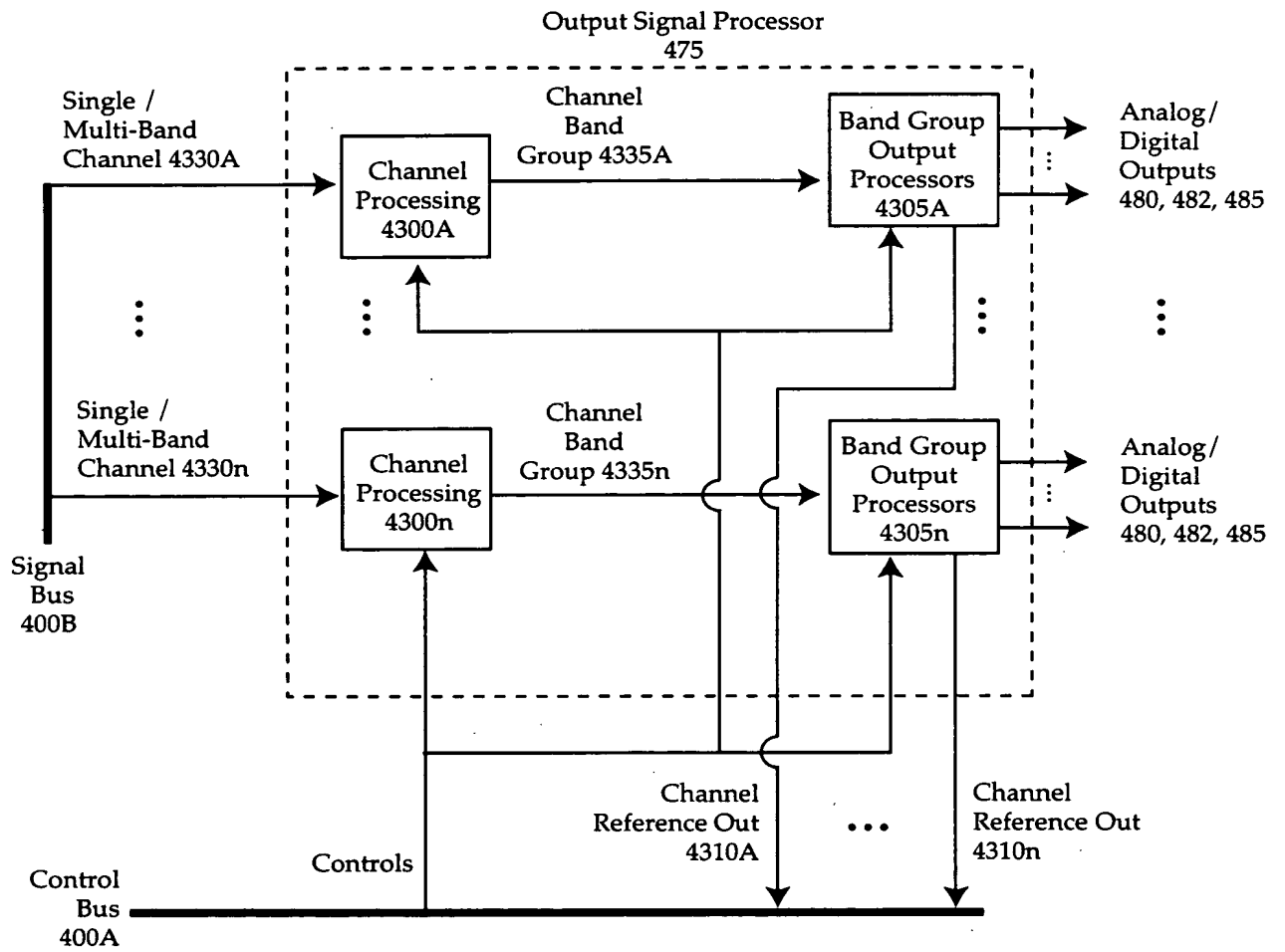


FIG. 43A

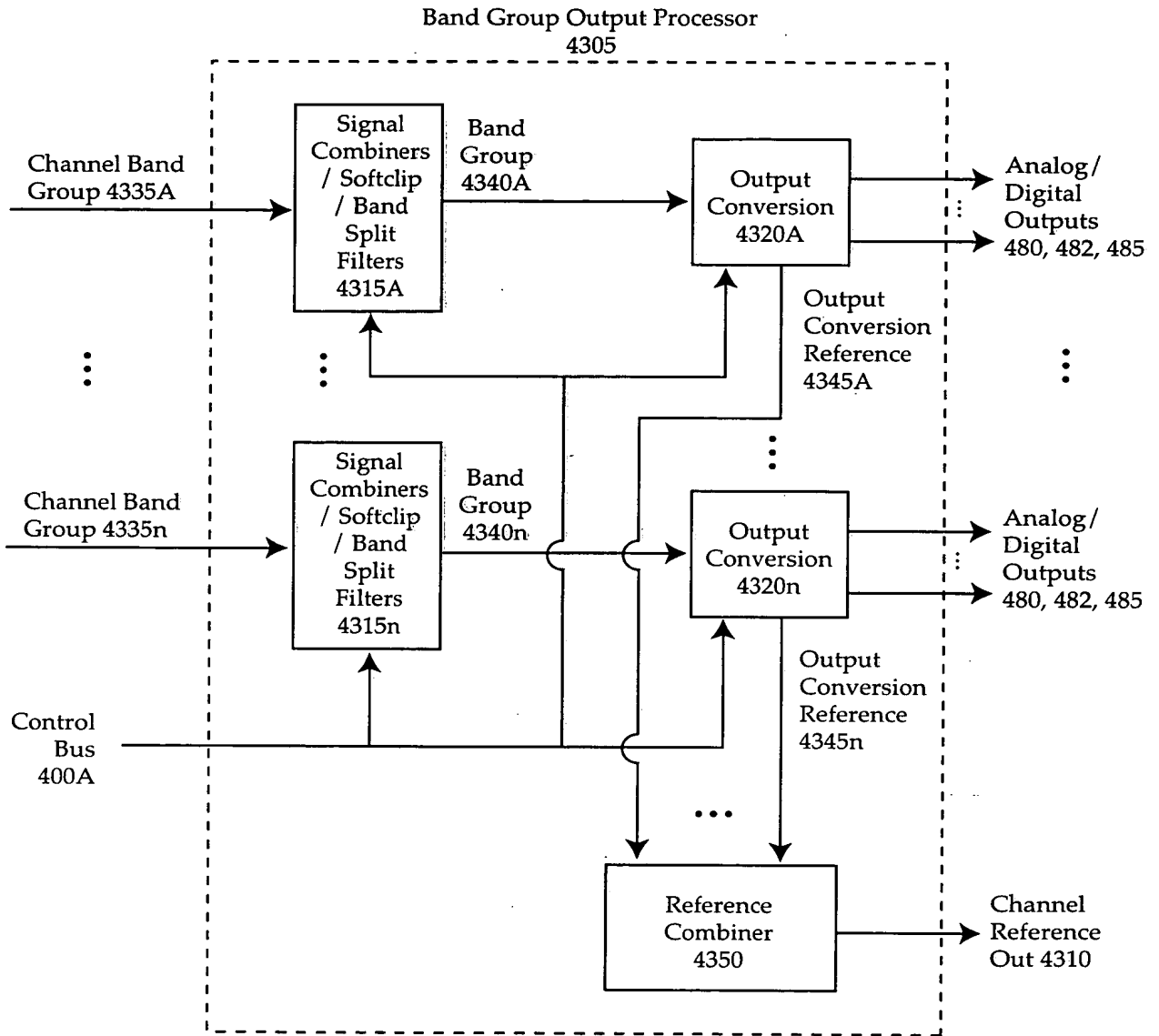
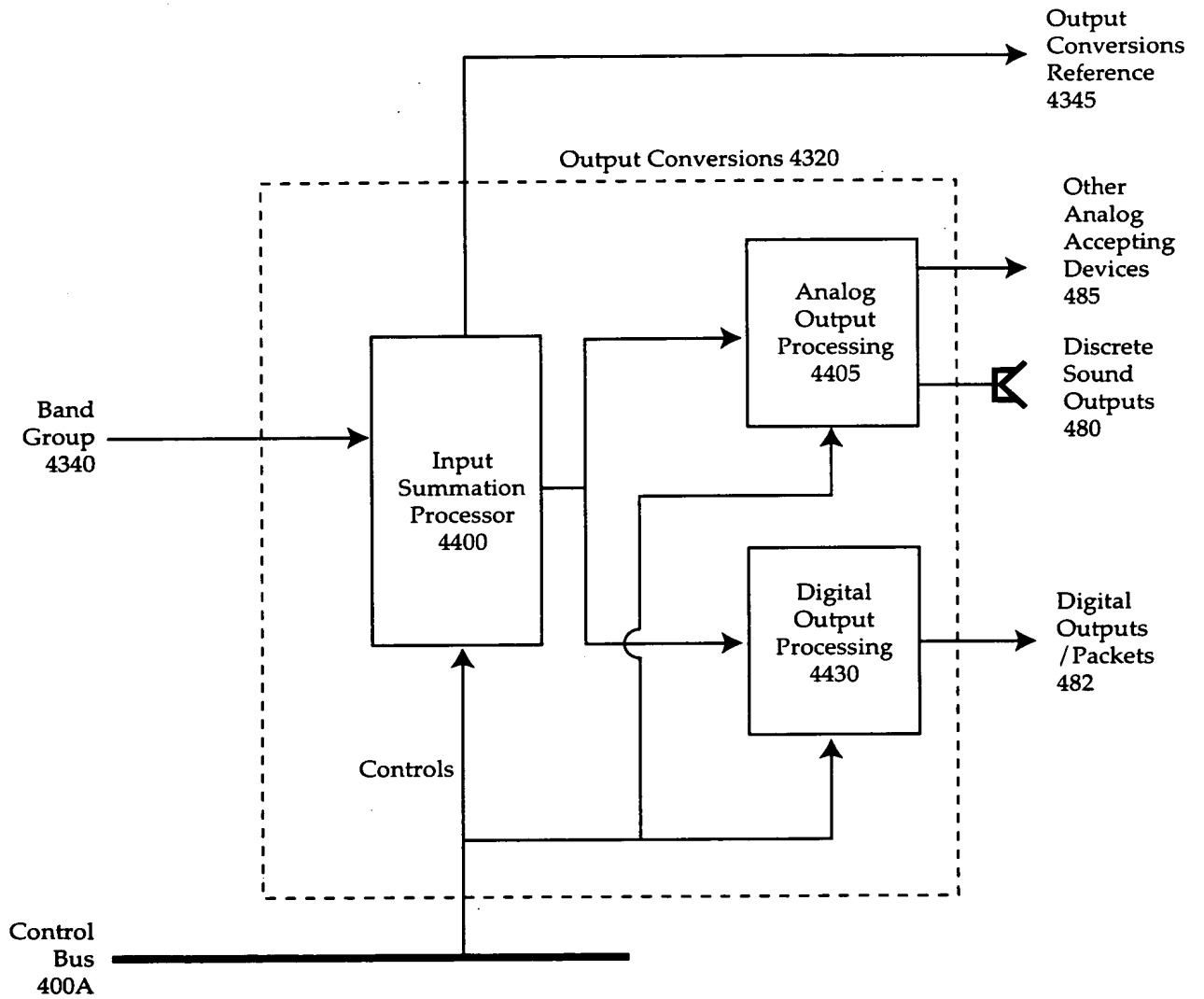


FIG. 43B



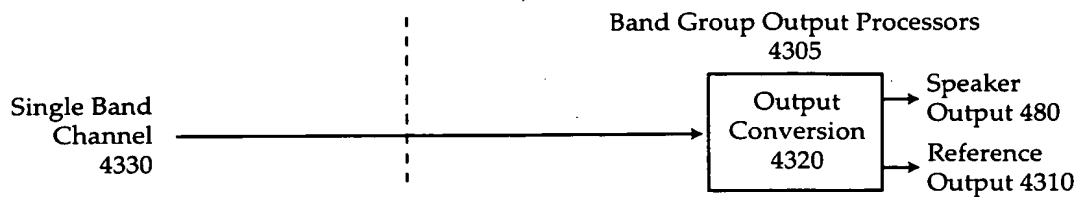


FIG. 45A

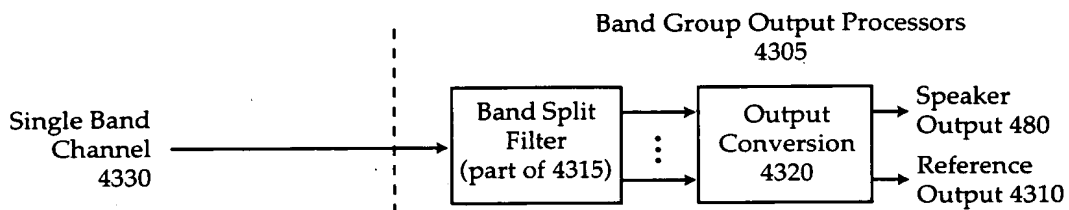


FIG. 45B

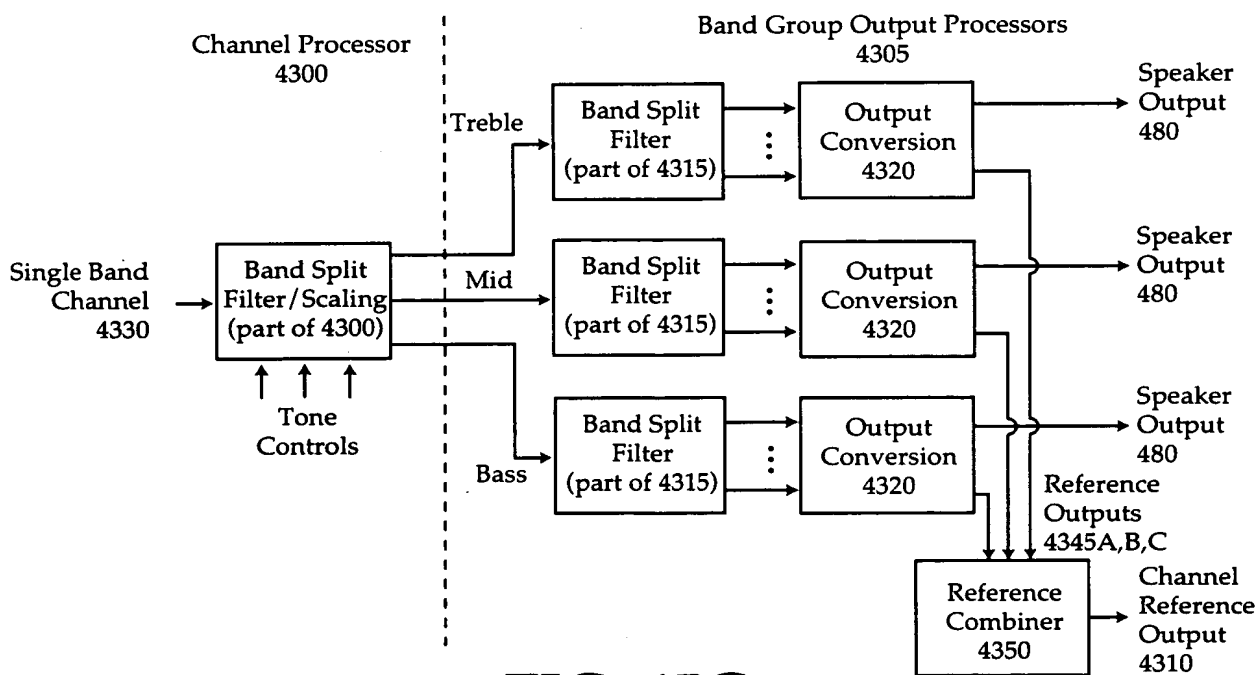
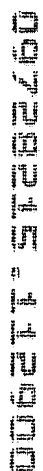


FIG. 45C



1

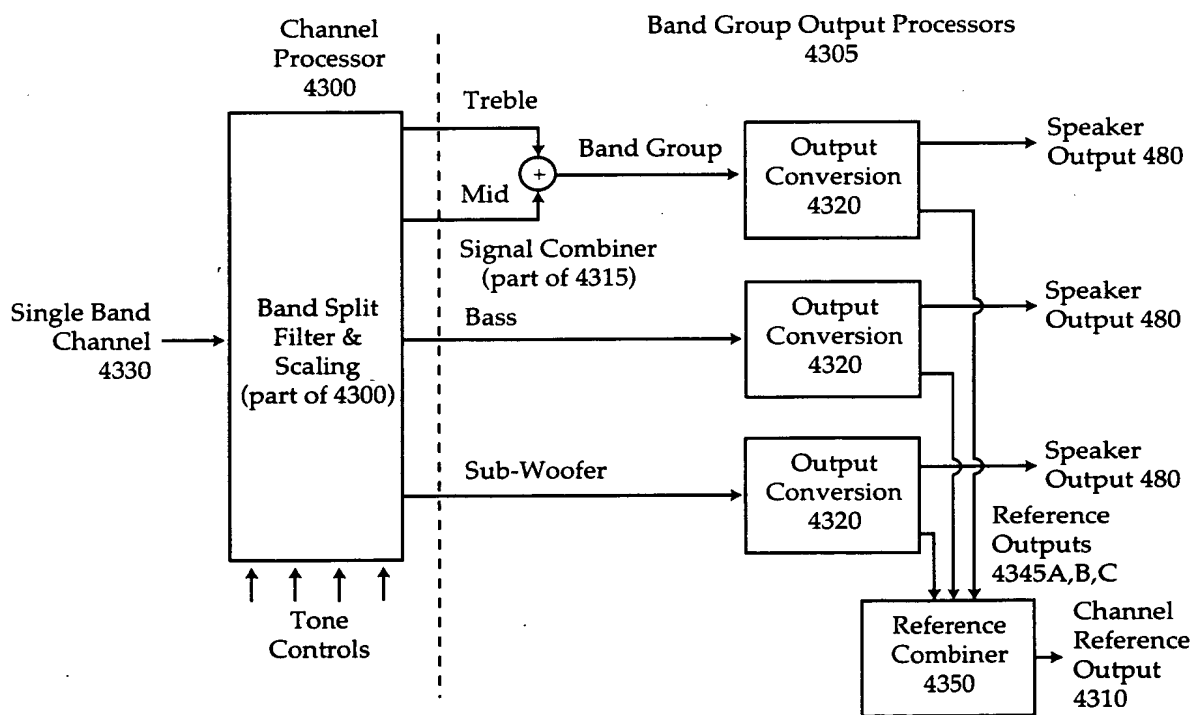


FIG. 45E

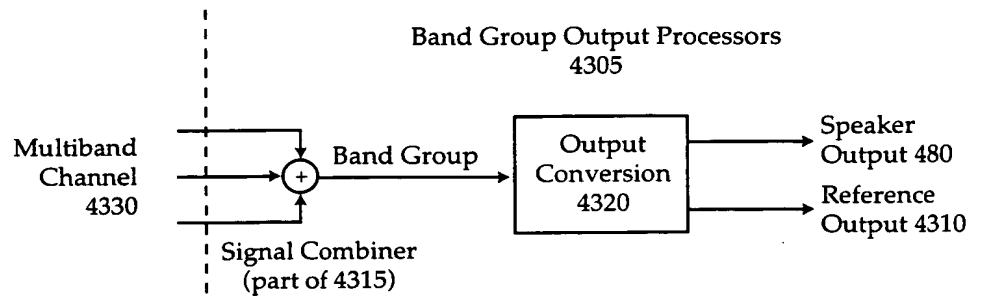


FIG. 45F

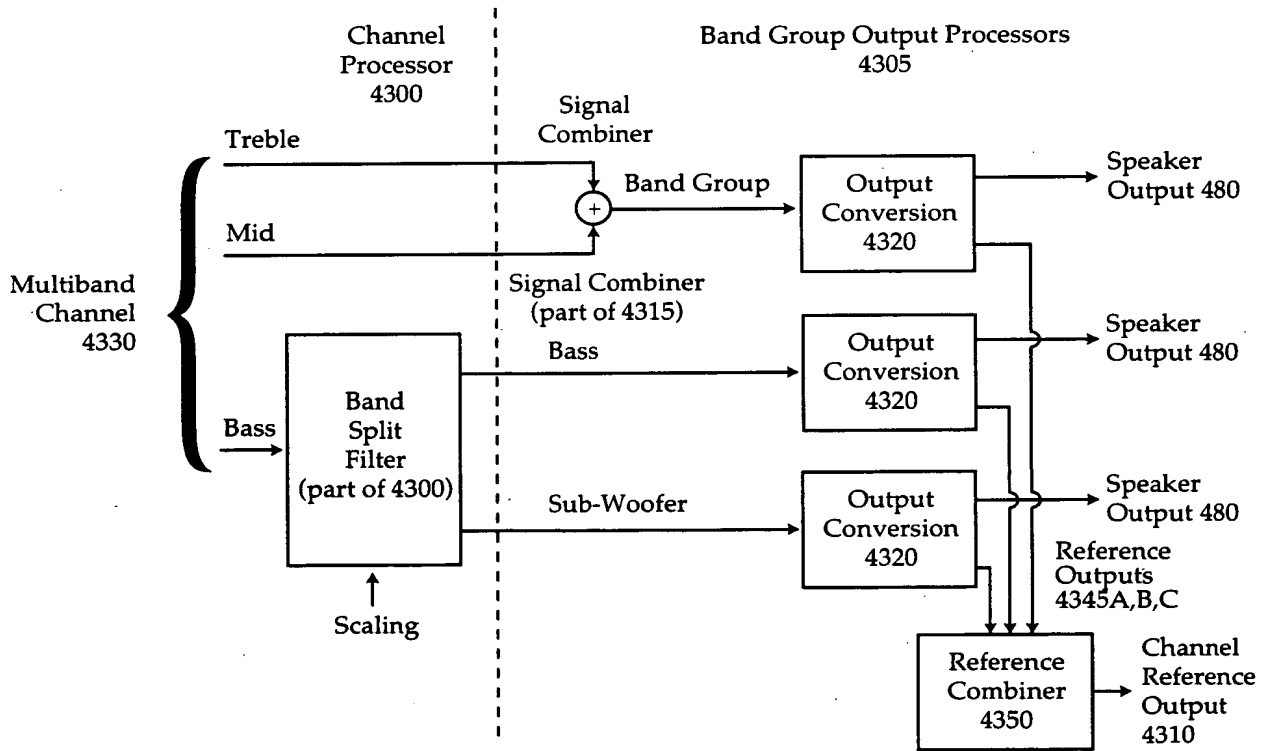


FIG. 45G

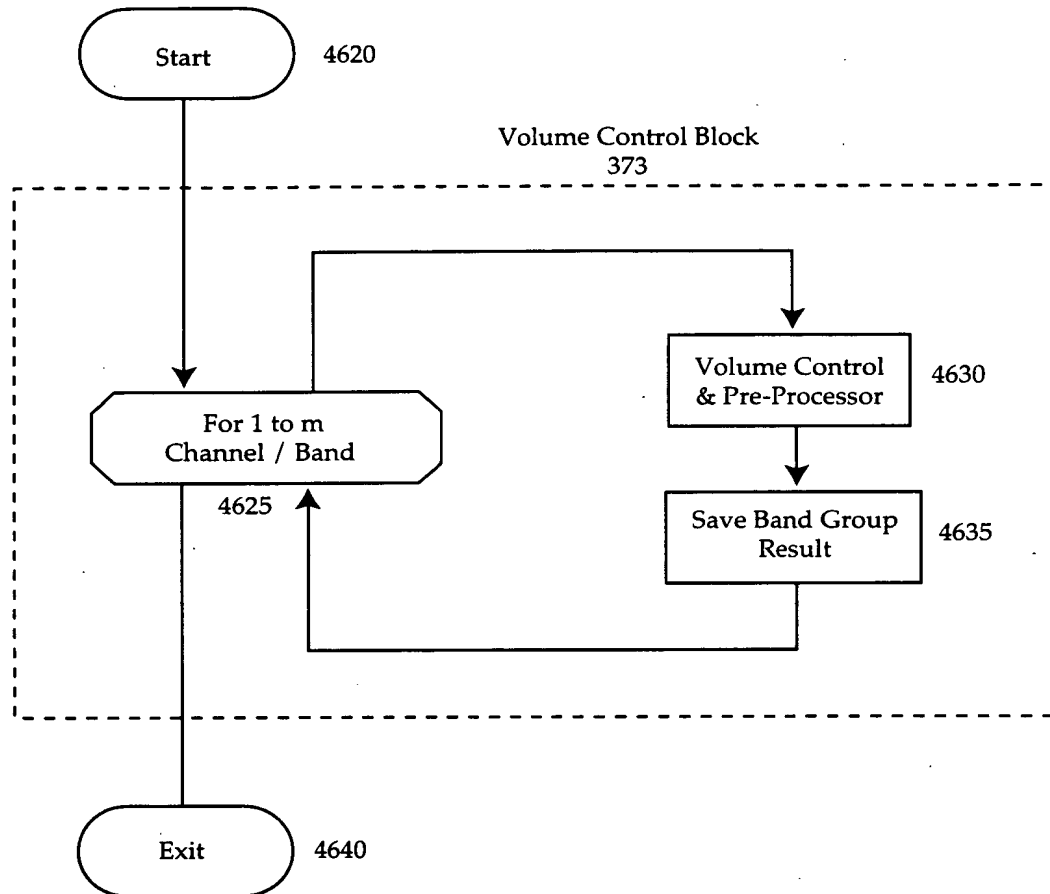


FIG. 46

006211-51232/60

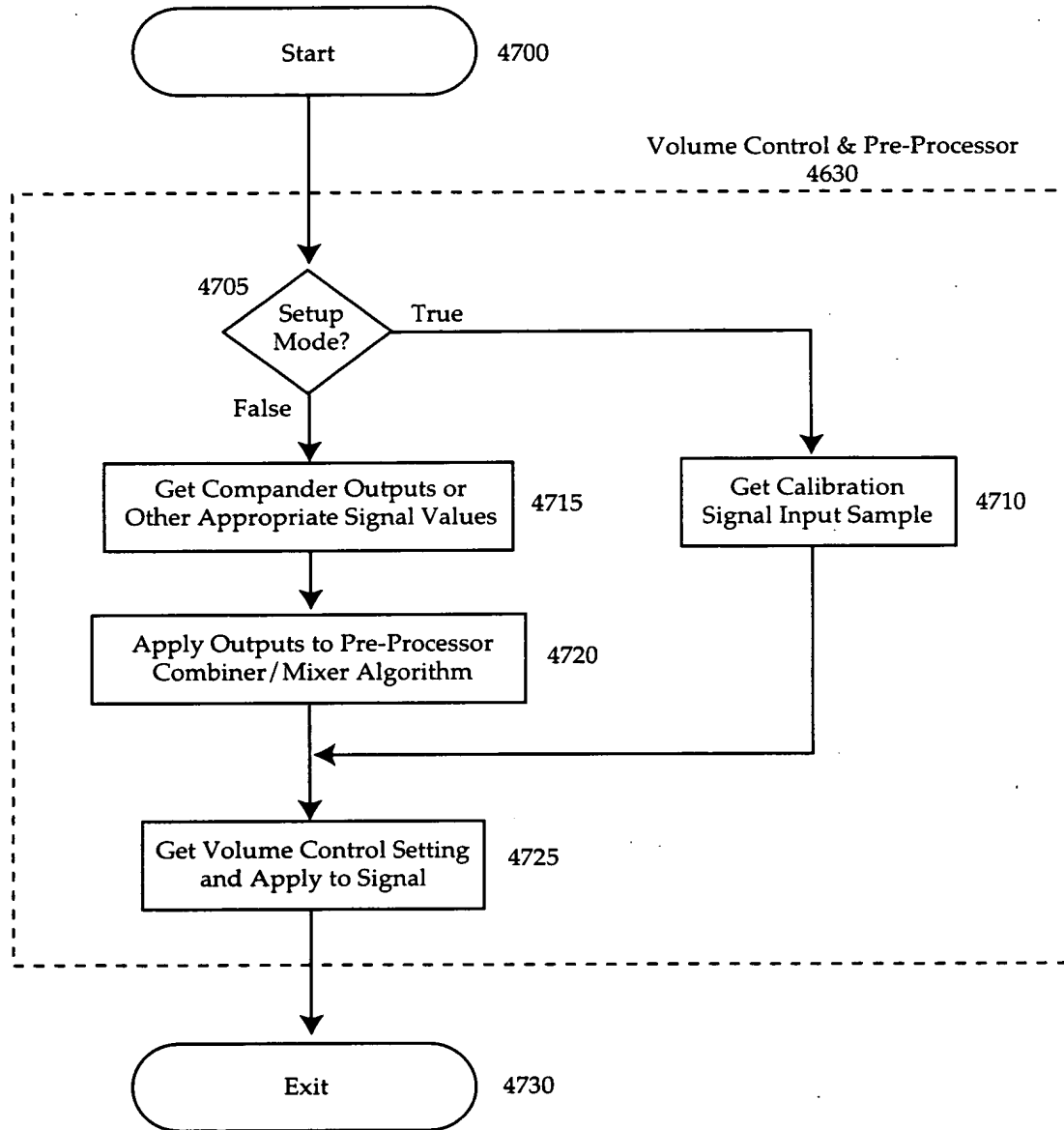


FIG. 47

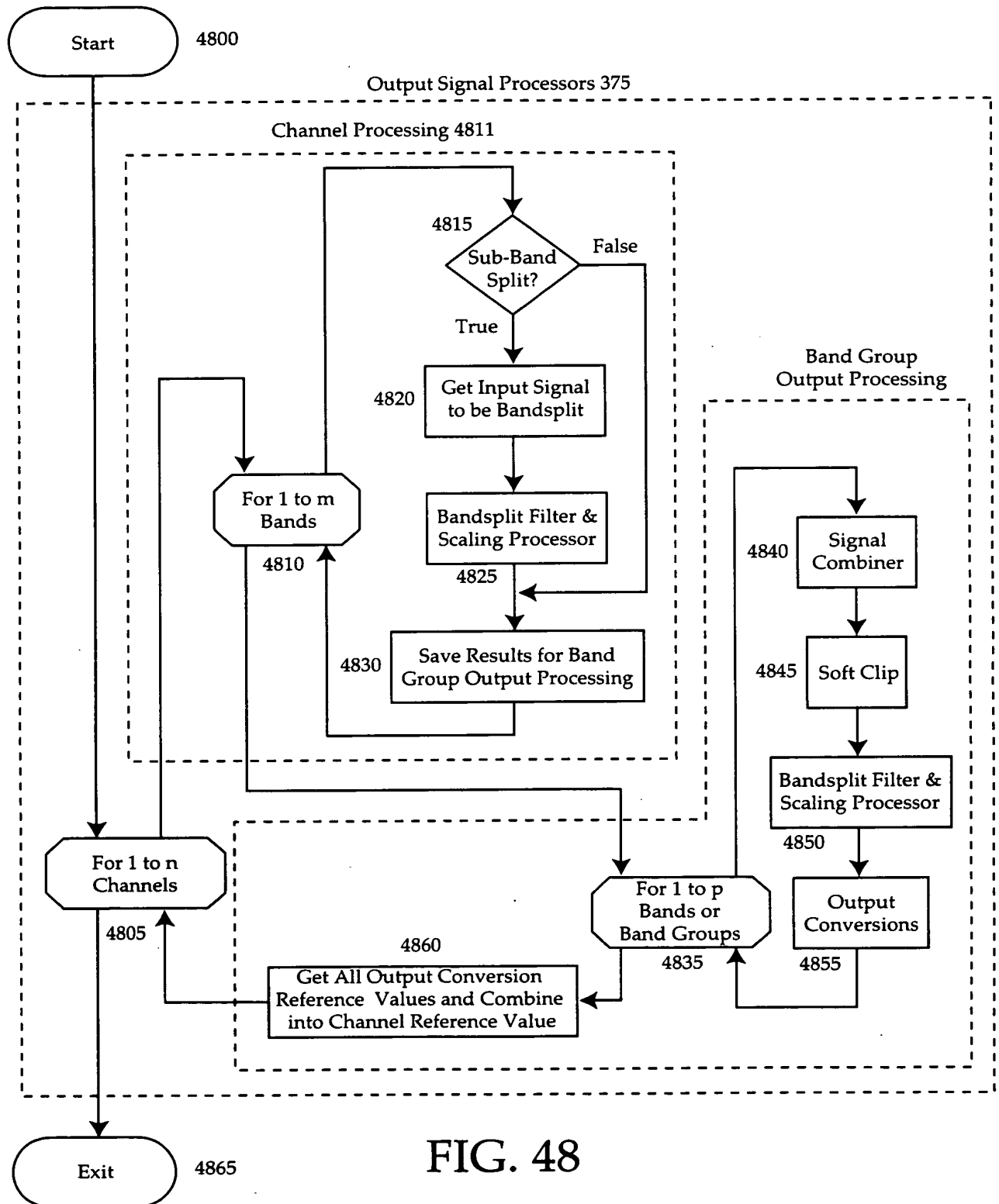


FIG. 48

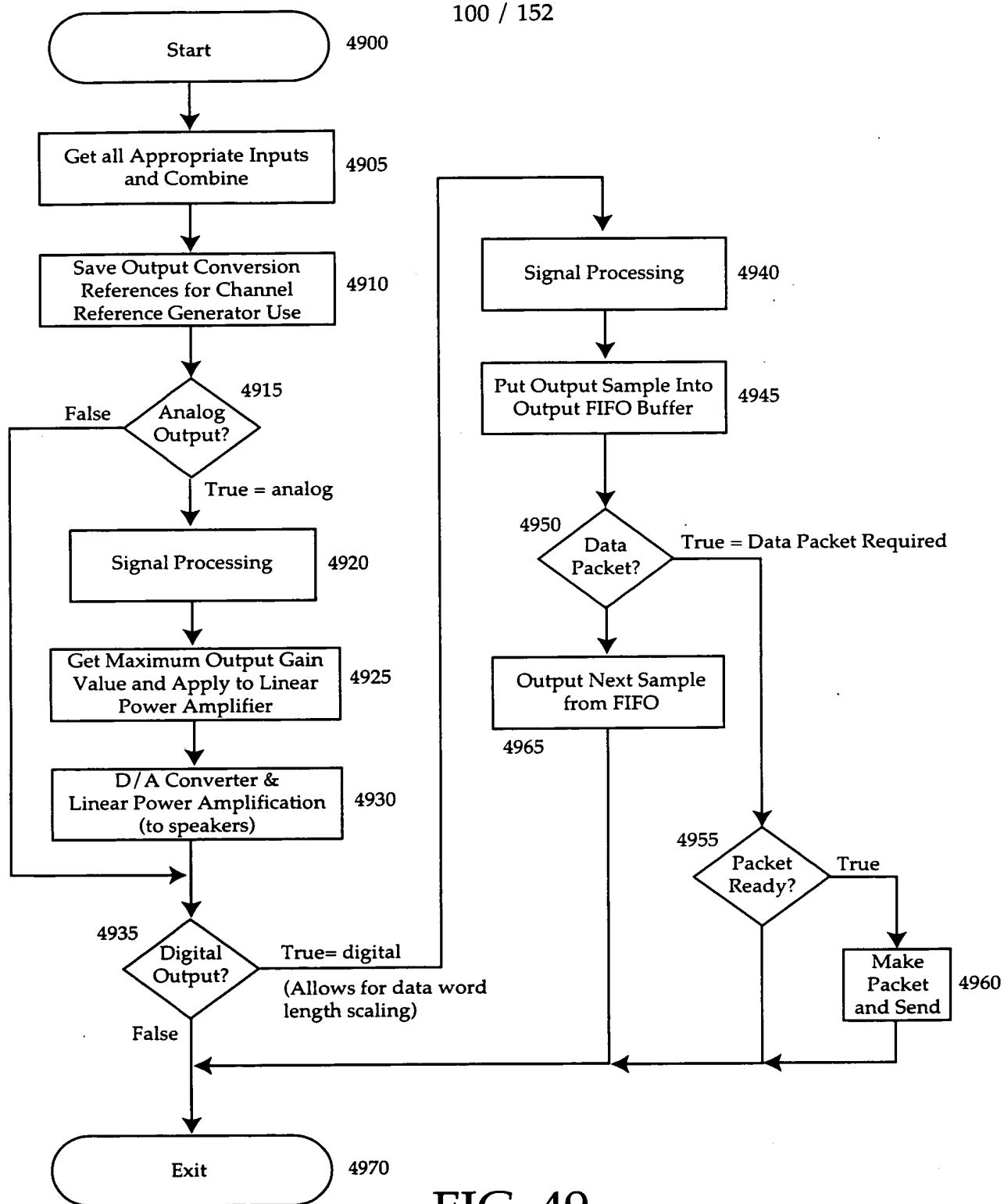


FIG. 49

Calibrator / Annunciator 420

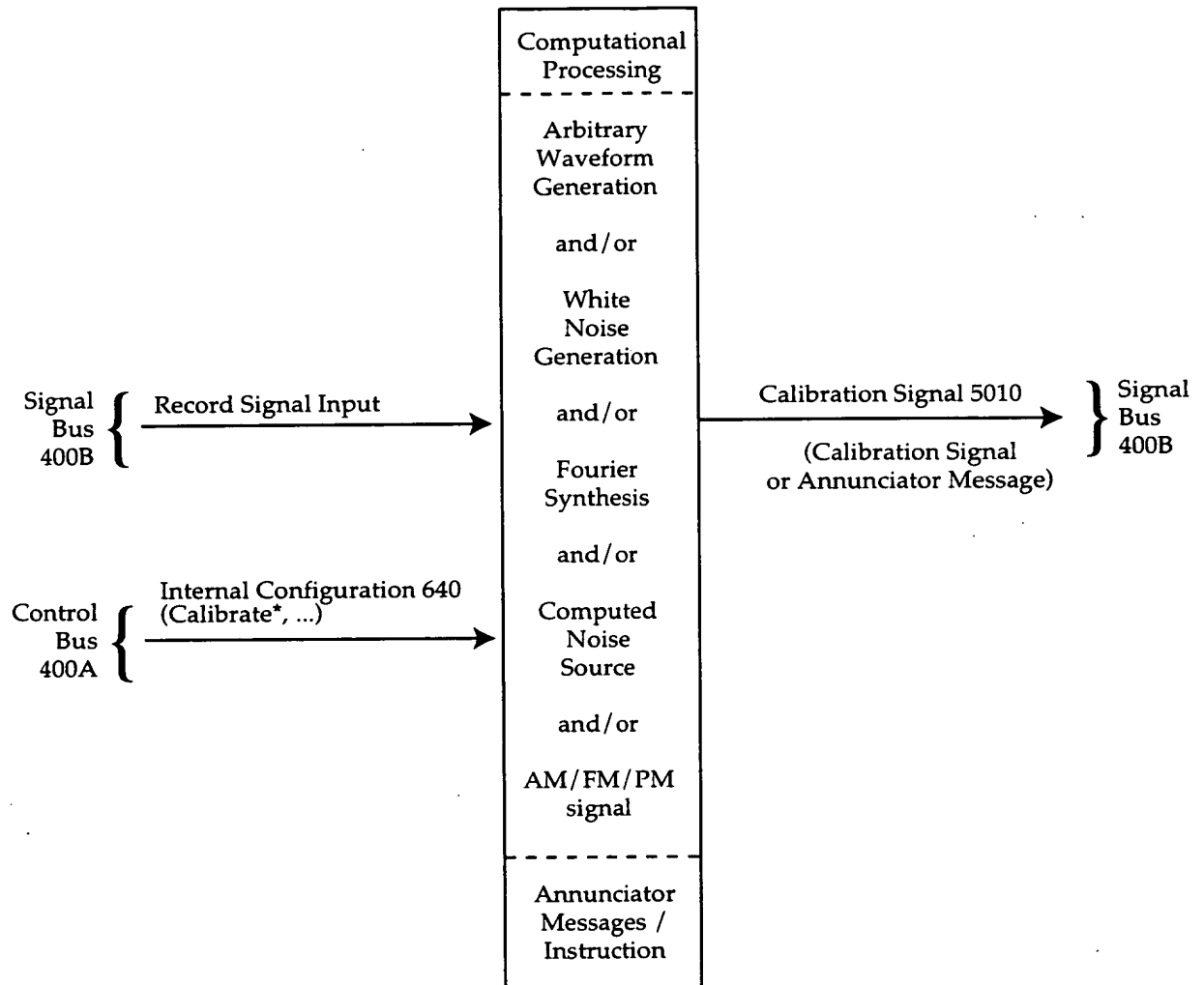
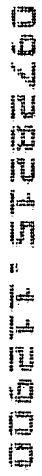


FIG. 50



1

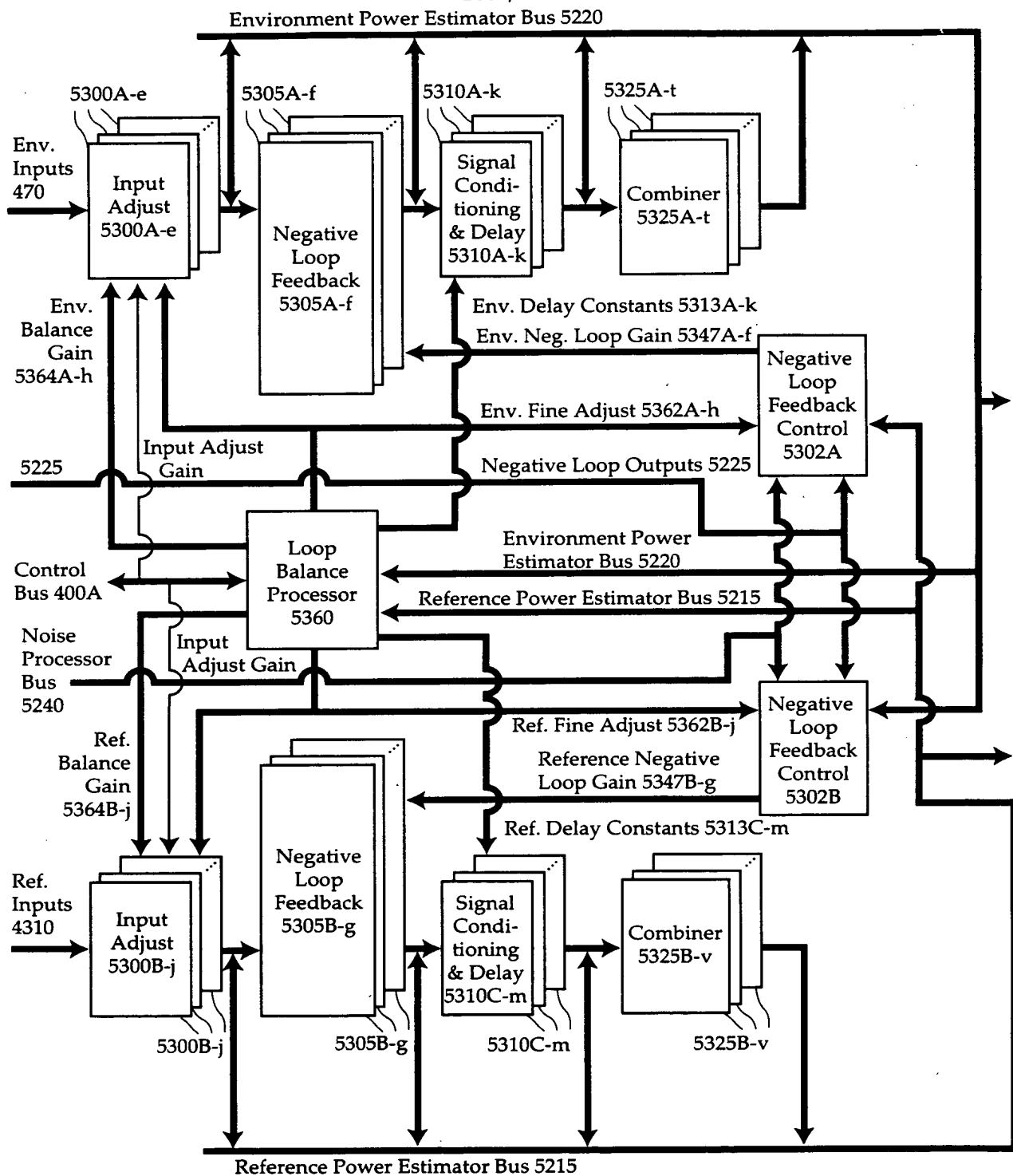


FIG. 52A

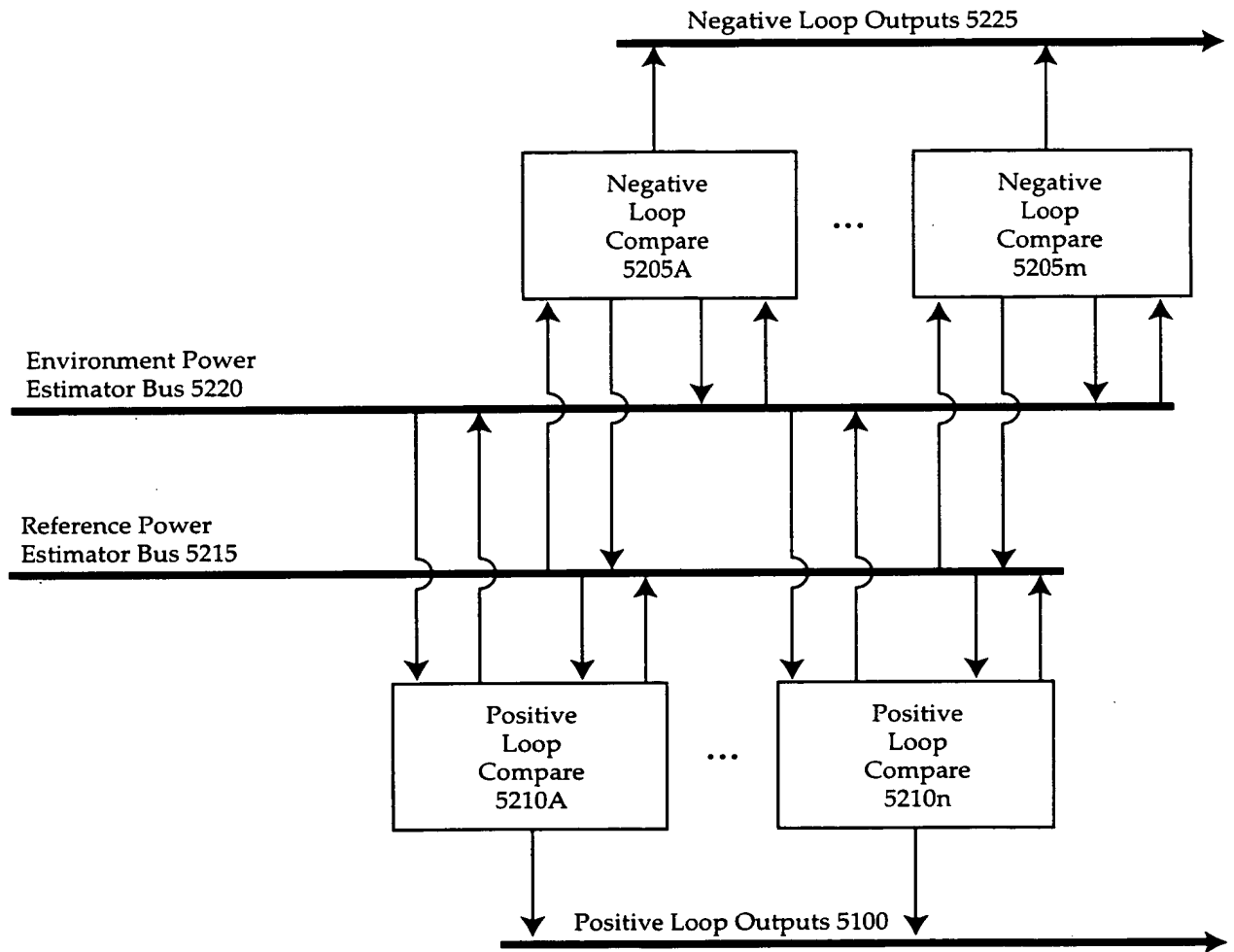


FIG. 52B

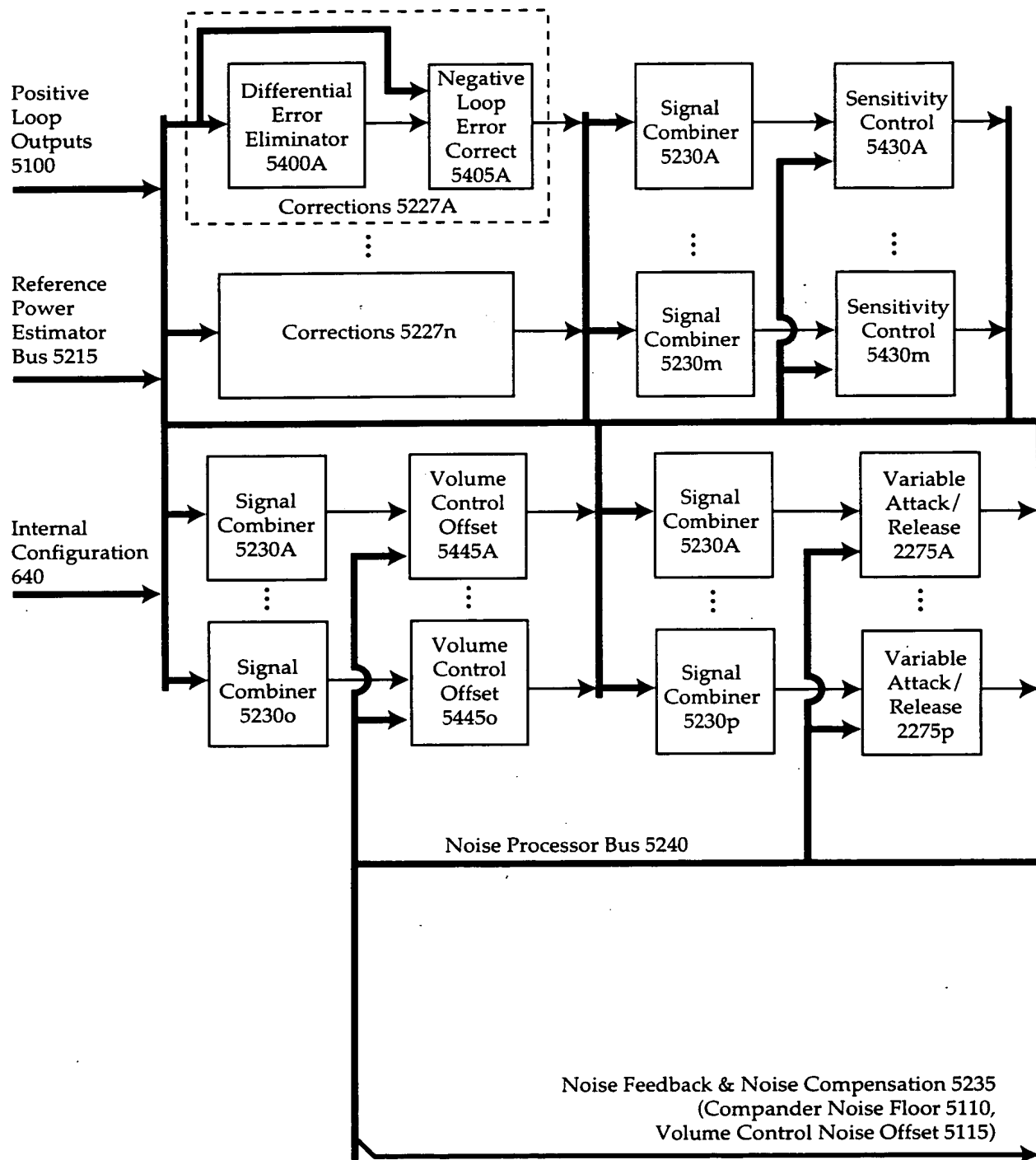
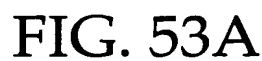


FIG. 52C



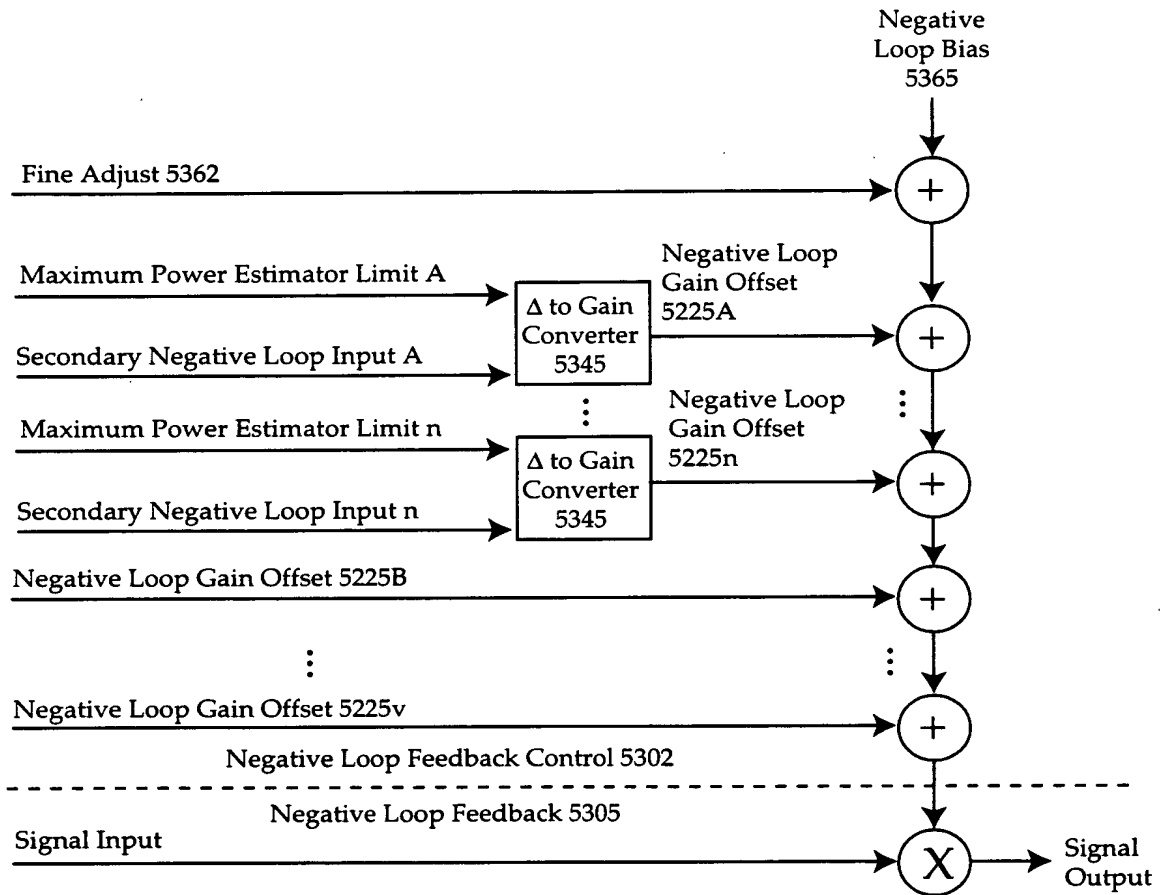


FIG. 53B

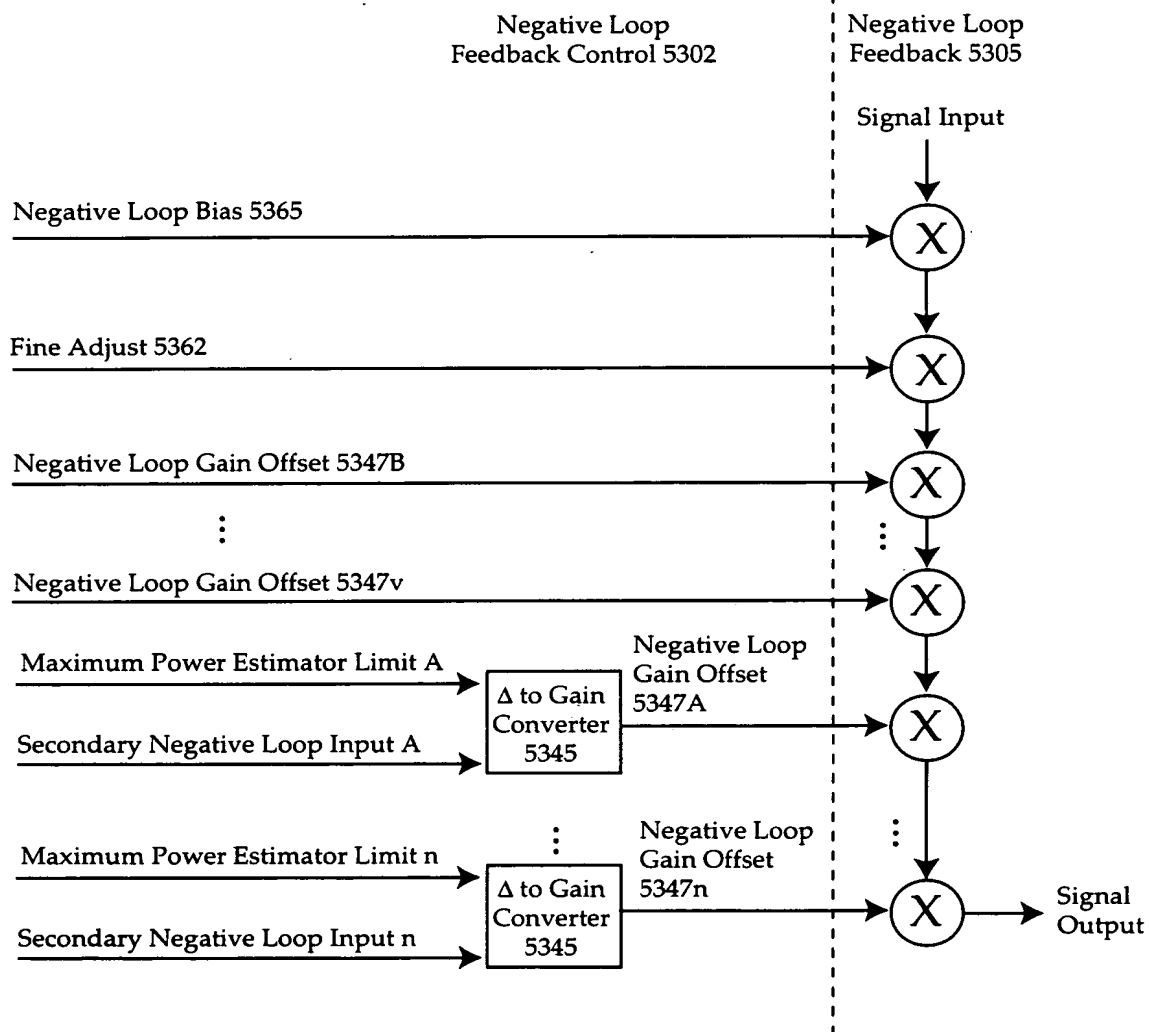


FIG. 53C

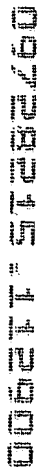


FIG. 53D

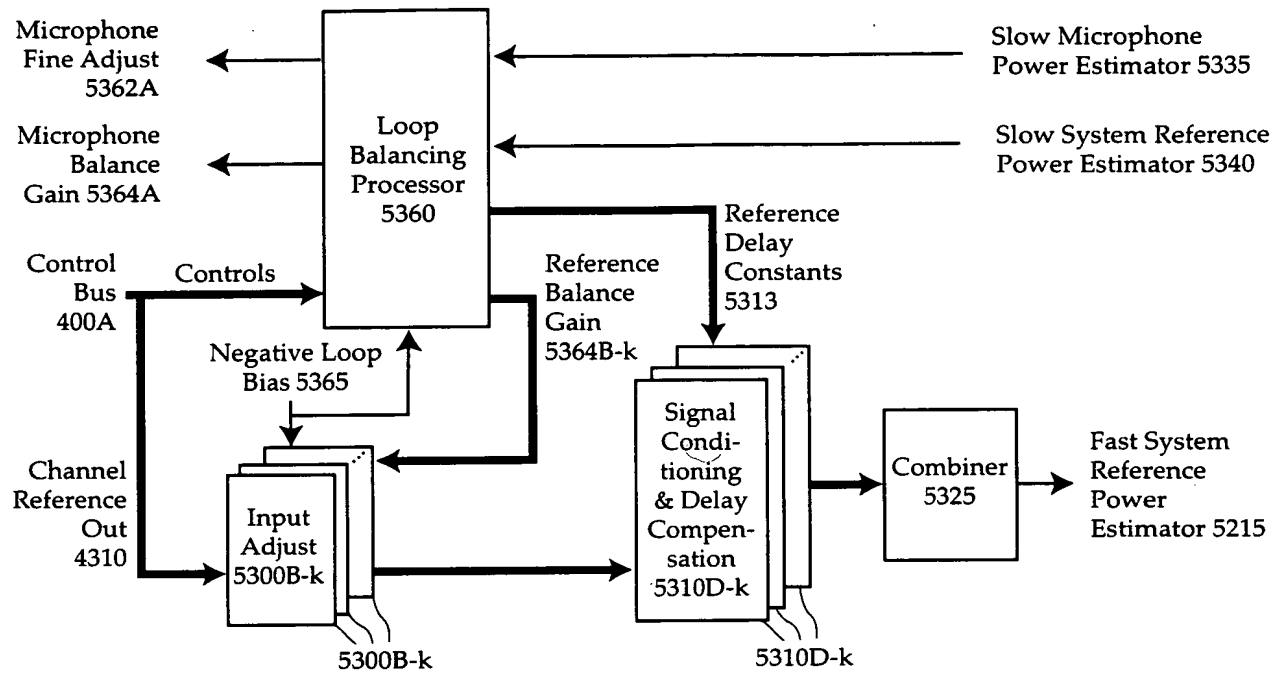


FIG. 53E

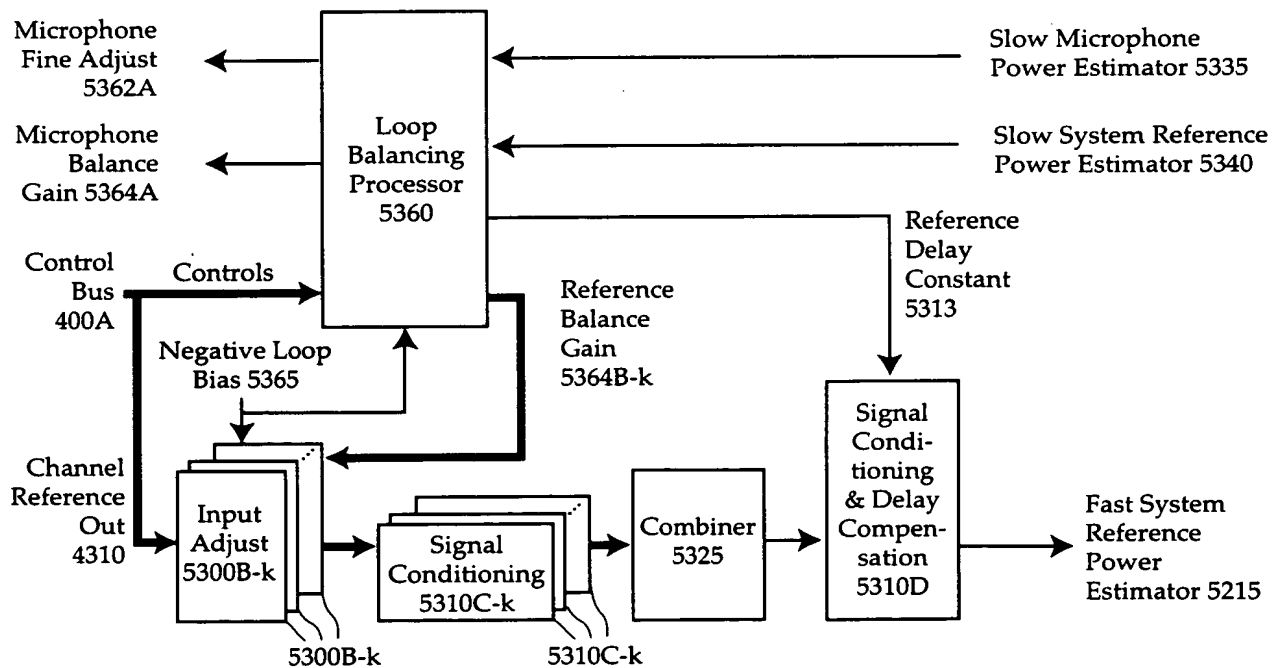


FIG. 53F

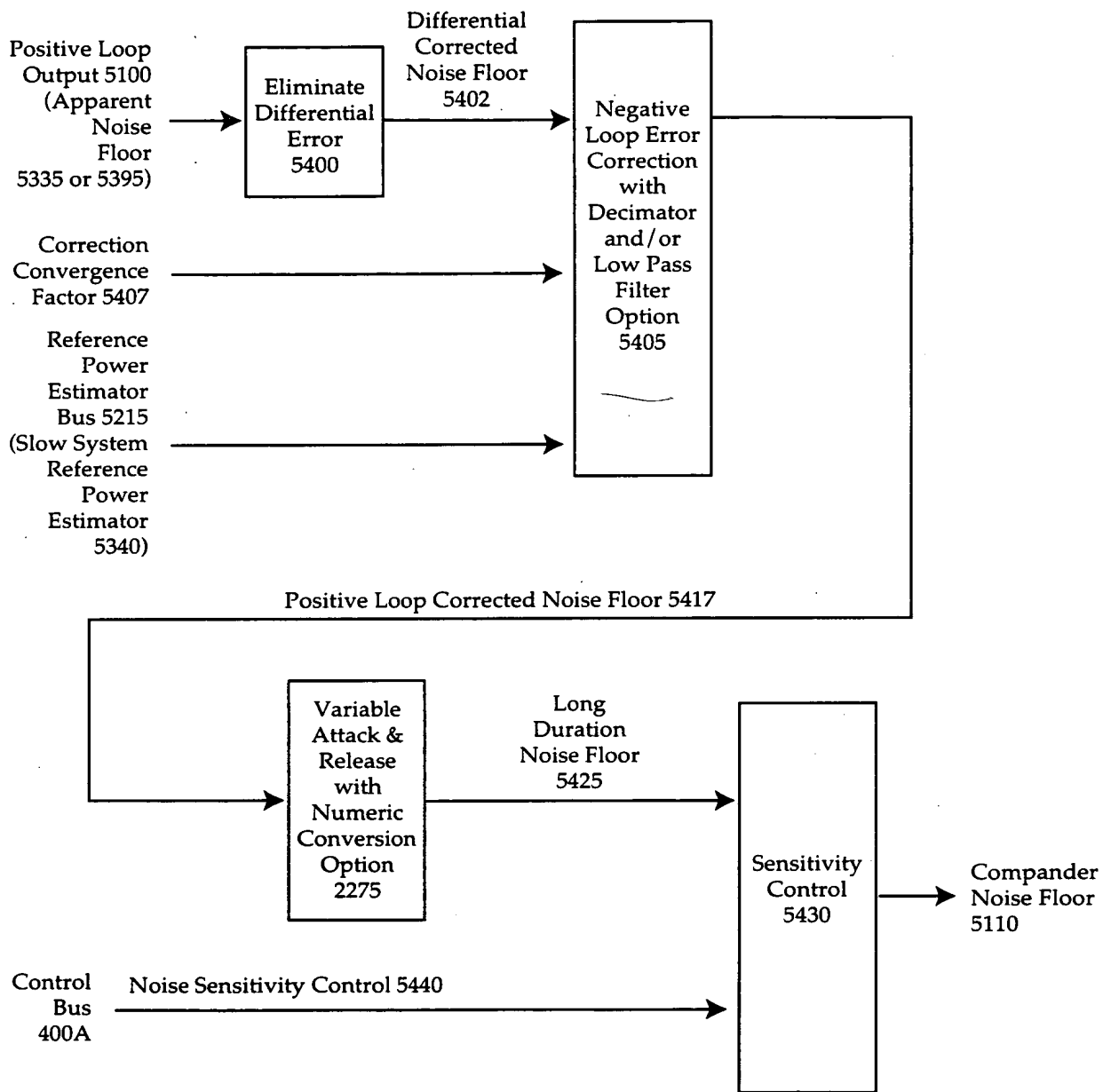


FIG. 54A

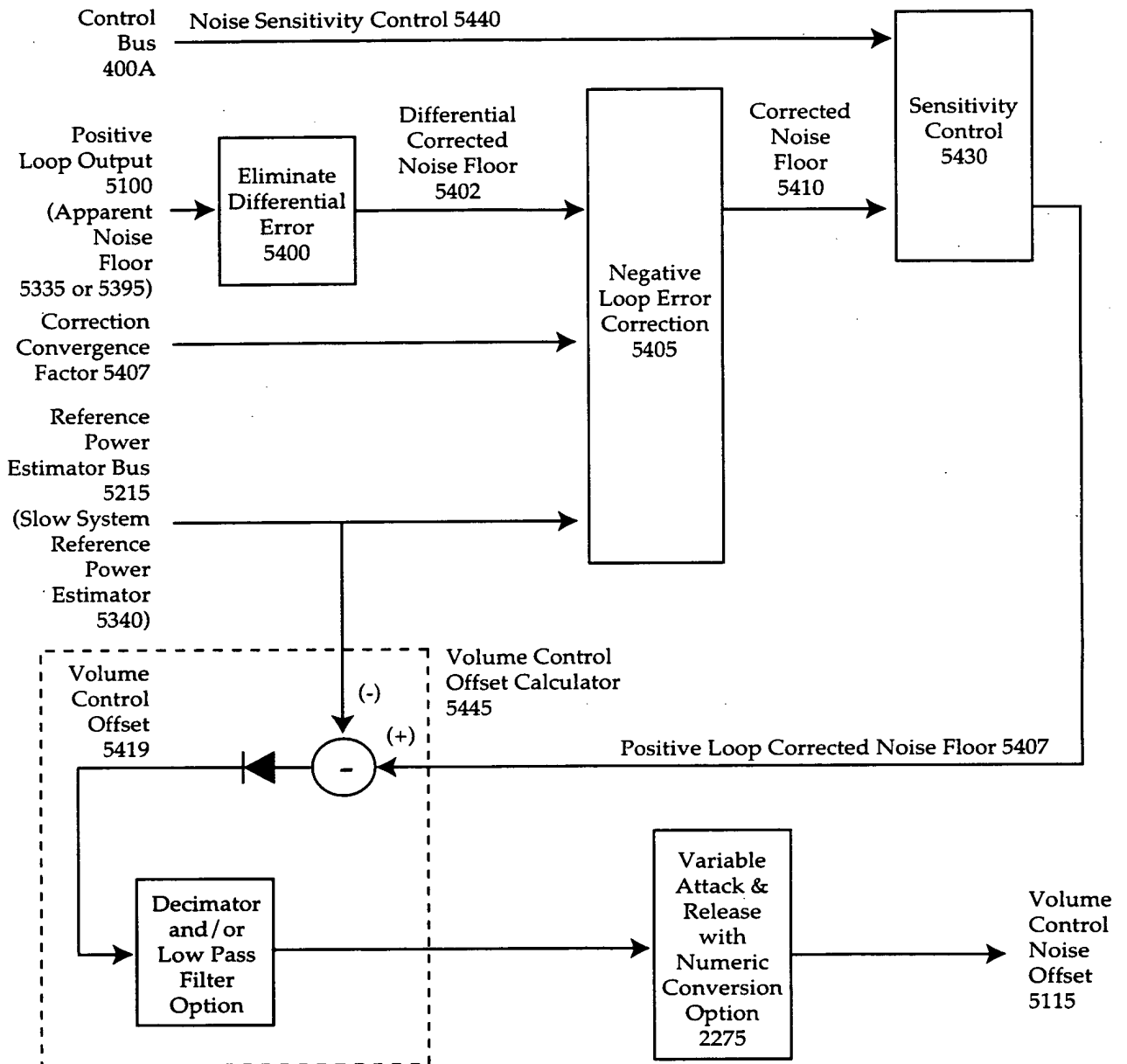
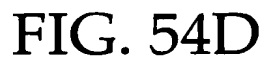


FIG. 54B



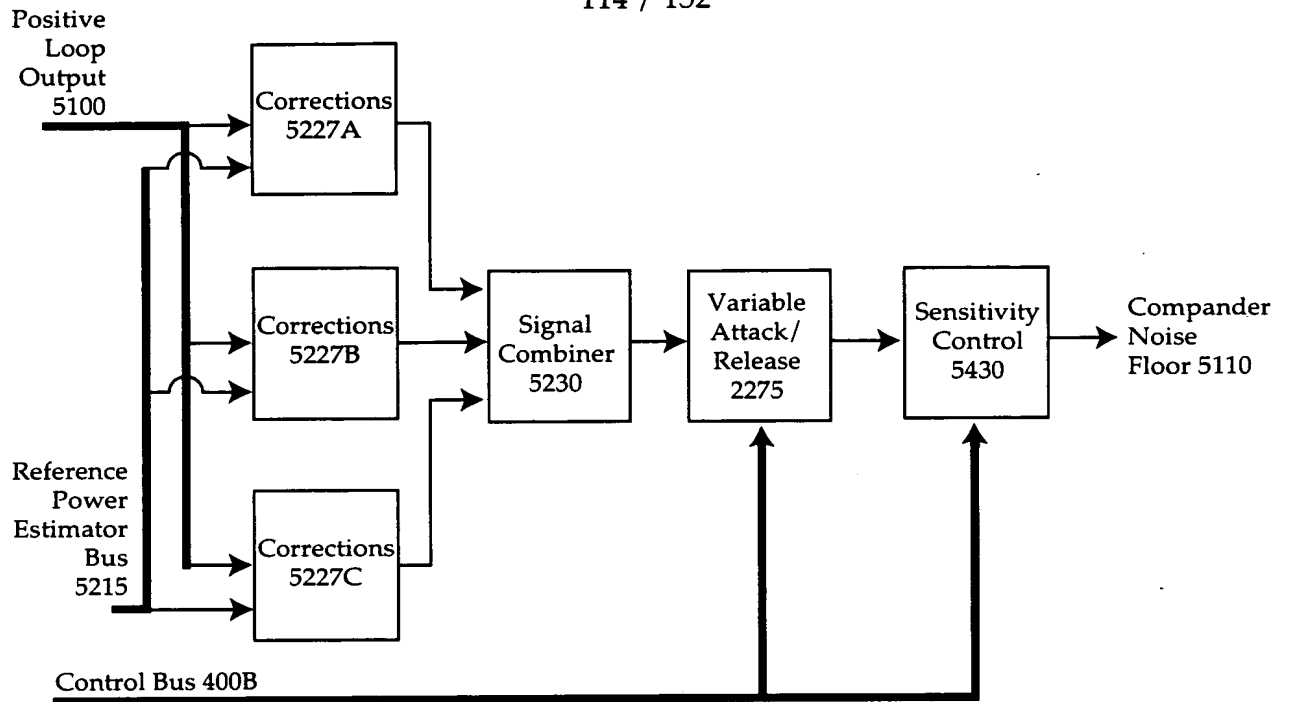


FIG. 54E

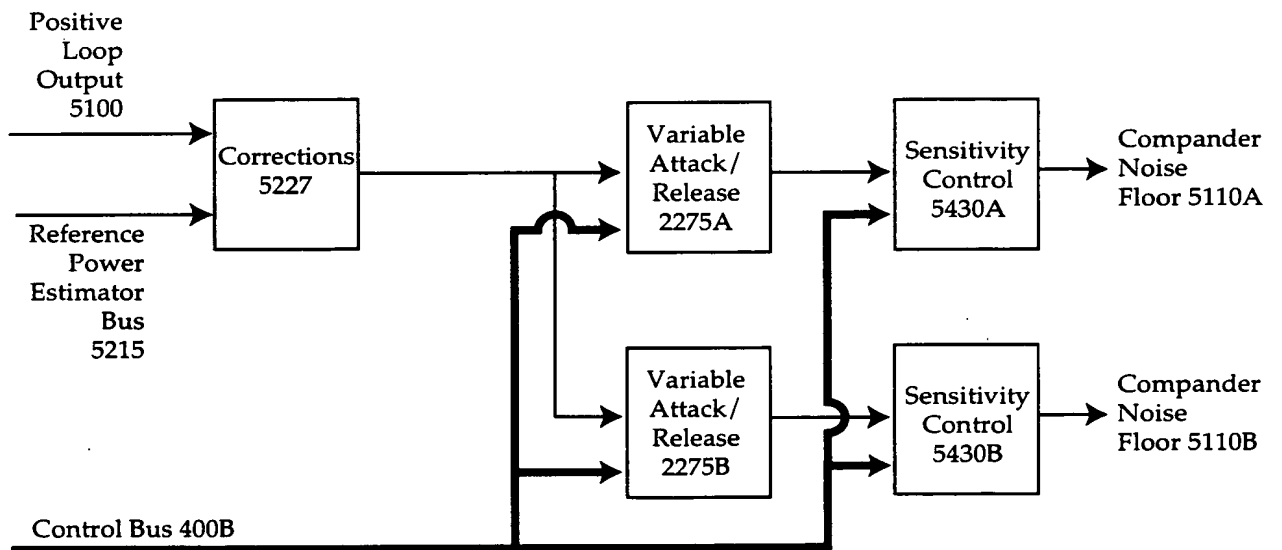


FIG. 54F

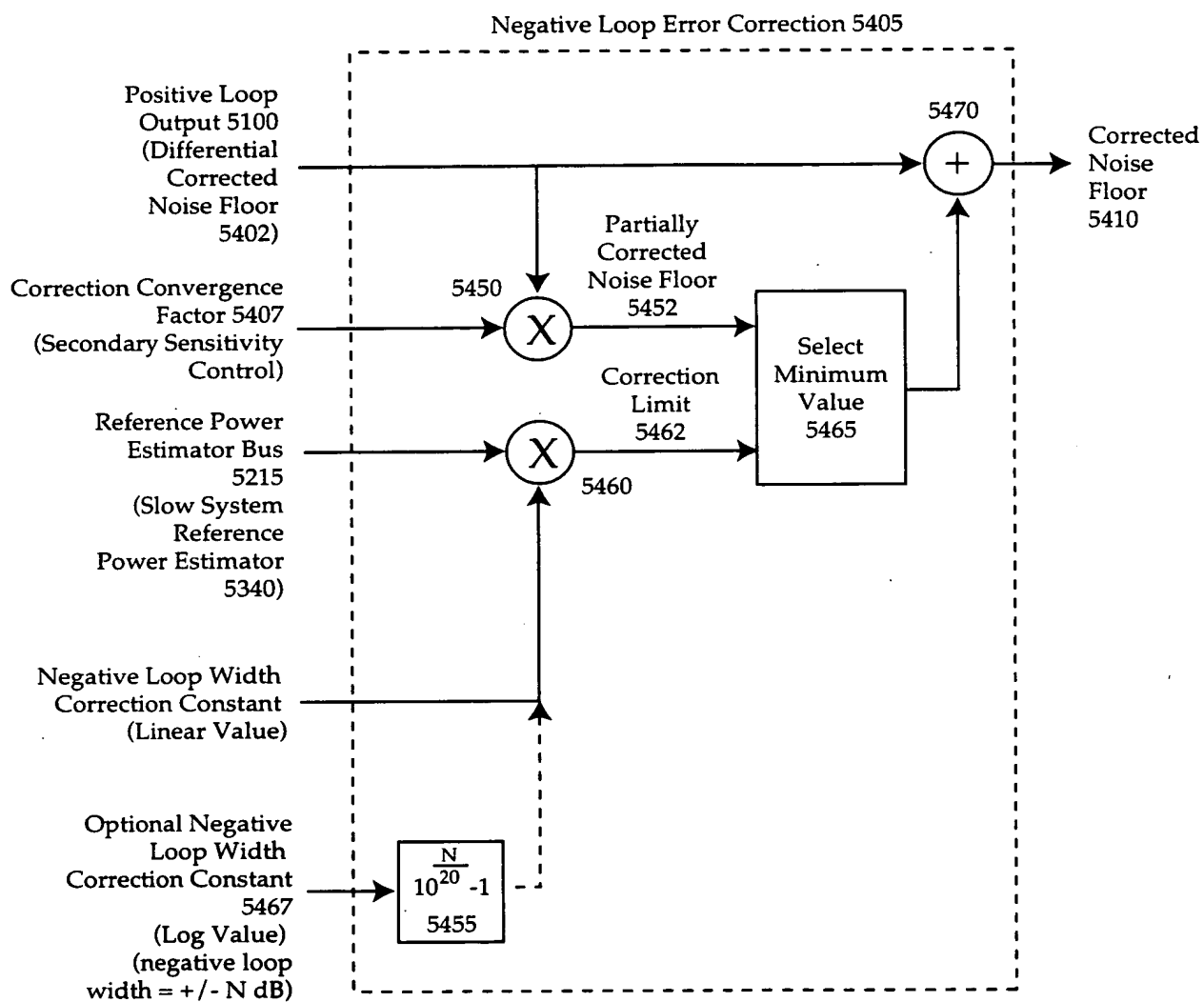


FIG. 54G

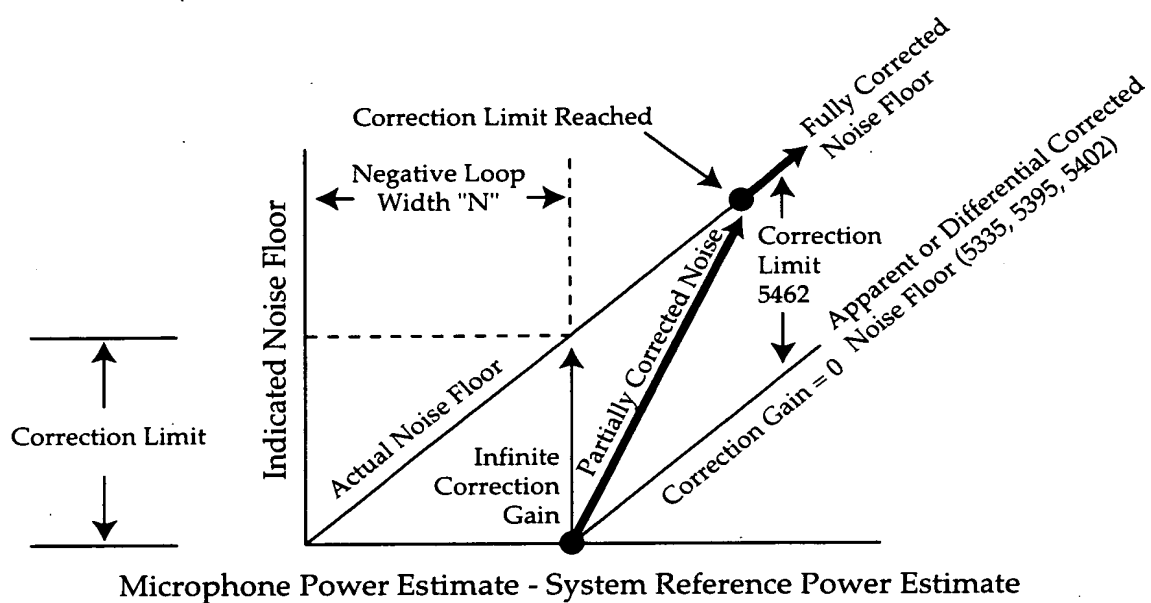


FIG. 54H

006275-112900

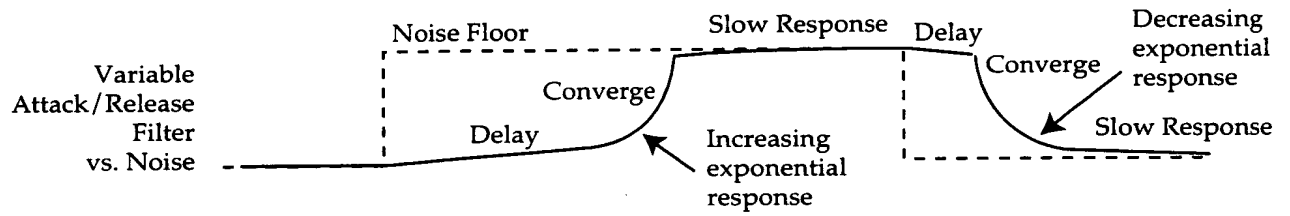
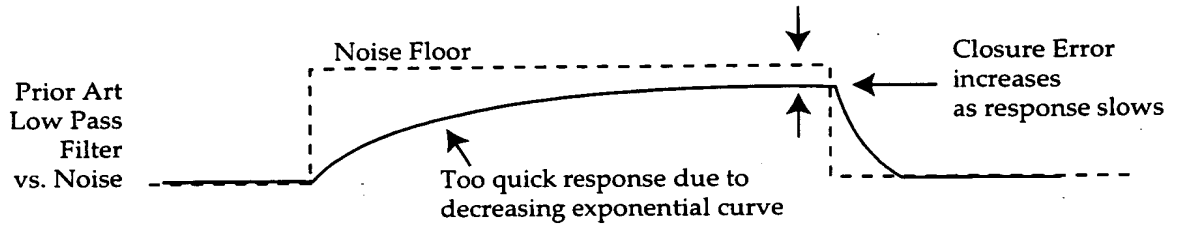


FIG. 55A

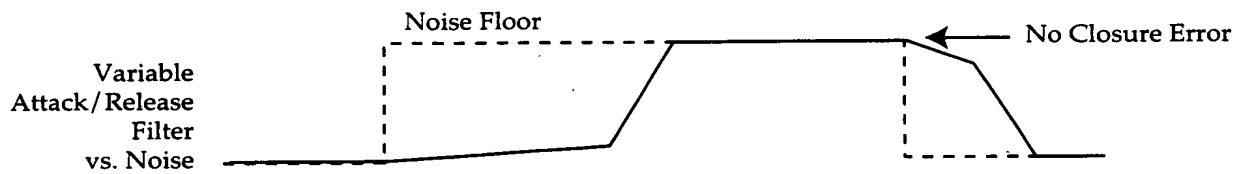
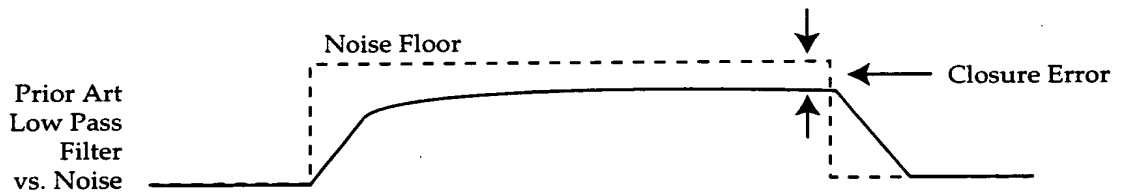


FIG. 55B

00621-5482/60

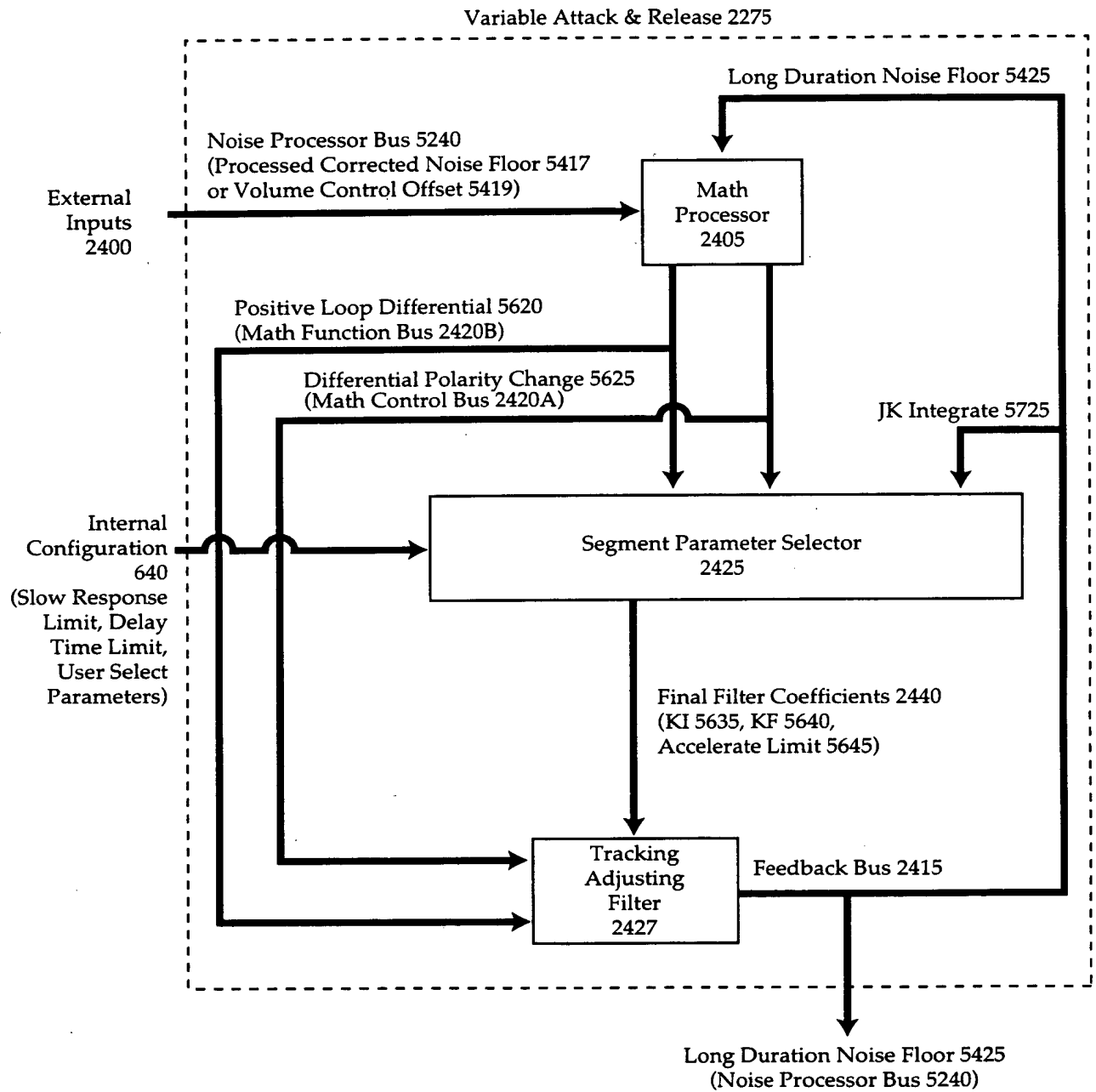


FIG. 55C

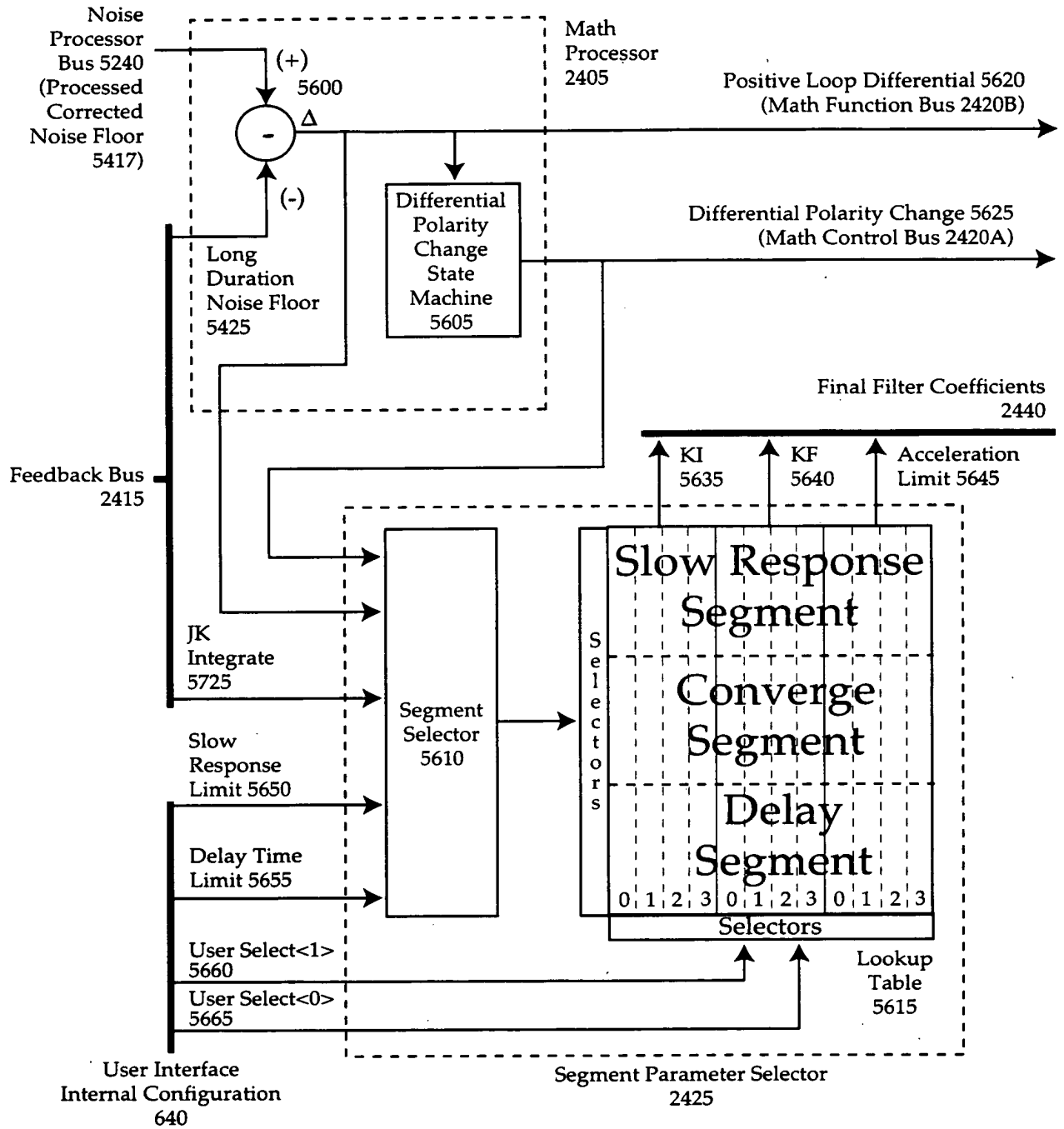


FIG. 56

The diagram illustrates the Acceleration Governor control system. It features several key components and signal paths:

- Inputs:** Acceleration Limit 5645, KI 5635, KF 5640, and Positive Loop Differential 5620 (Math Function Bus 2420B).
- Processing:**
 - The Acceleration Limit 5645 is multiplied (X) by a signal from the Feedback Bus 2415 at node 5750.
 - KI 5635 and KF 5640 are multiplied (X) by the Positive Loop Differential 5620 at node 5705.
 - The output of 5705 is added (+) to a signal from the Feedback Bus 2415 at node 5710.
 - The output of 5710 is multiplied (X) by a signal from the Feedback Bus 2415 at node 5700.
 - The output of 5700 is added (+) to a signal from the Feedback Bus 2415 at node 5715.
 - The output of 5715 is fed into the Minimum Input Difference Selector 5717.
 - The output of 5717 is added (+) to a signal from the Feedback Bus 2415 at node 5720.
 - The output of 5720 is fed into the JK Integrate 5725 block.
 - The output of 5725 is added (+) to a signal from the Feedback Bus 2415 at node 5735.
 - The output of 5735 is fed into the Positive Loop Average 5740 block.
- Control Elements:**
 - JK Hold 5745:** Receives a Load signal and provides a positive bias value to the Feedback Bus 2415.
 - Clear:** A signal that resets the JK Integrate 5725 block.
 - Long Duration Noise Floor 5425 (Noise Processor Bus 5240):** Provides a signal to the Feedback Bus 2415.
- Output:** The Feedback Bus 2415, which receives signals from JK Integrate 5725, JK Hold 5745, and the Long Duration Noise Floor 5425, and provides a signal to the Feedback Bus 2415.

When:

$K_F = 1 - KI$	Low pass filter (slow response)
$K_F = 1$	Integrator (delay response)
$K_F > 1$	Accelerating Integrator (converge)

FIG. 57A

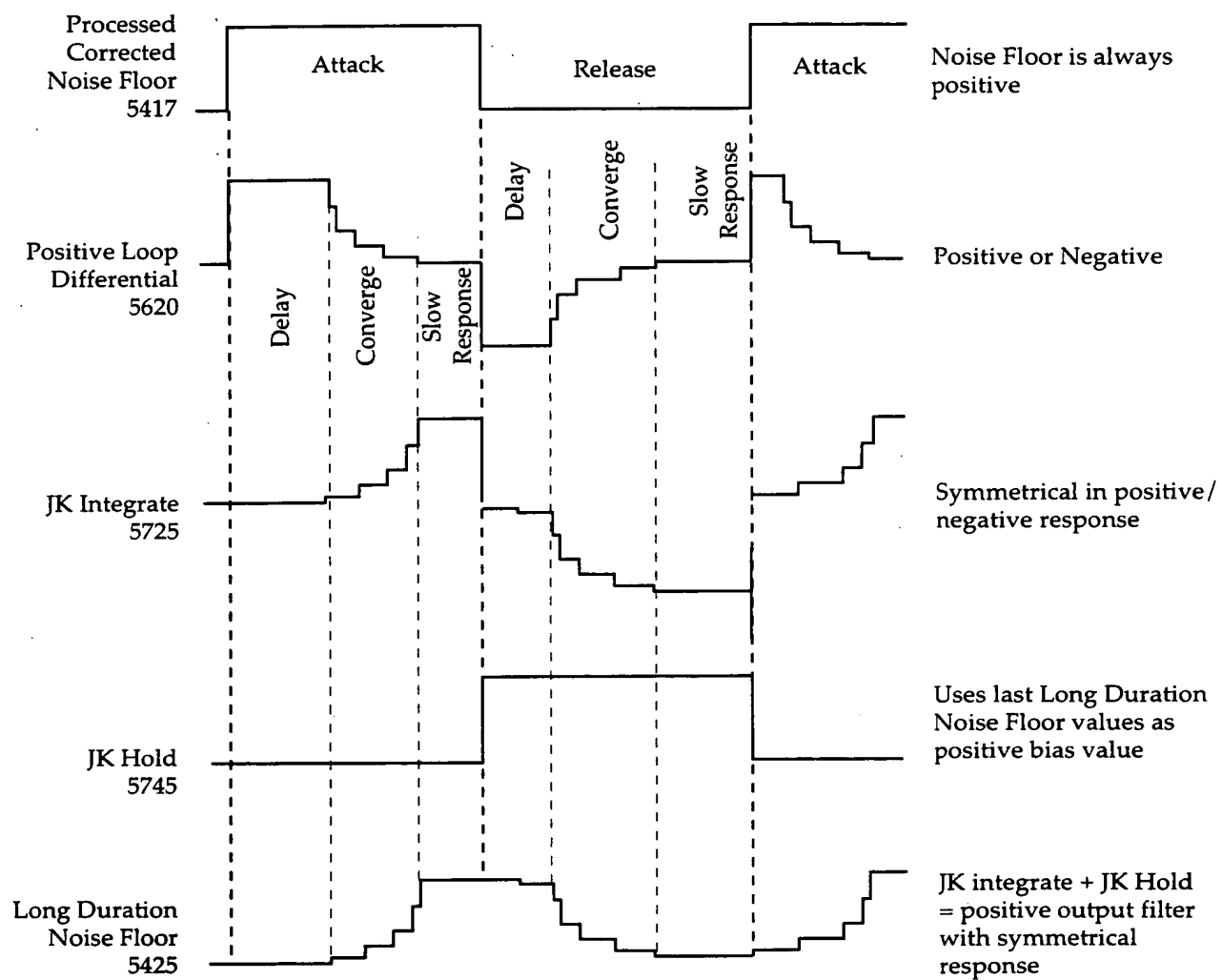


FIG. 57B

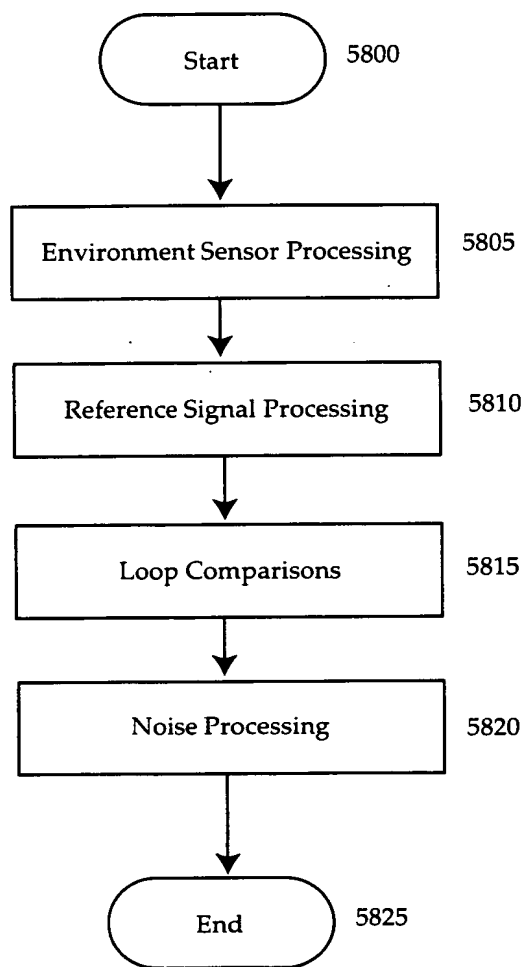


FIG. 58

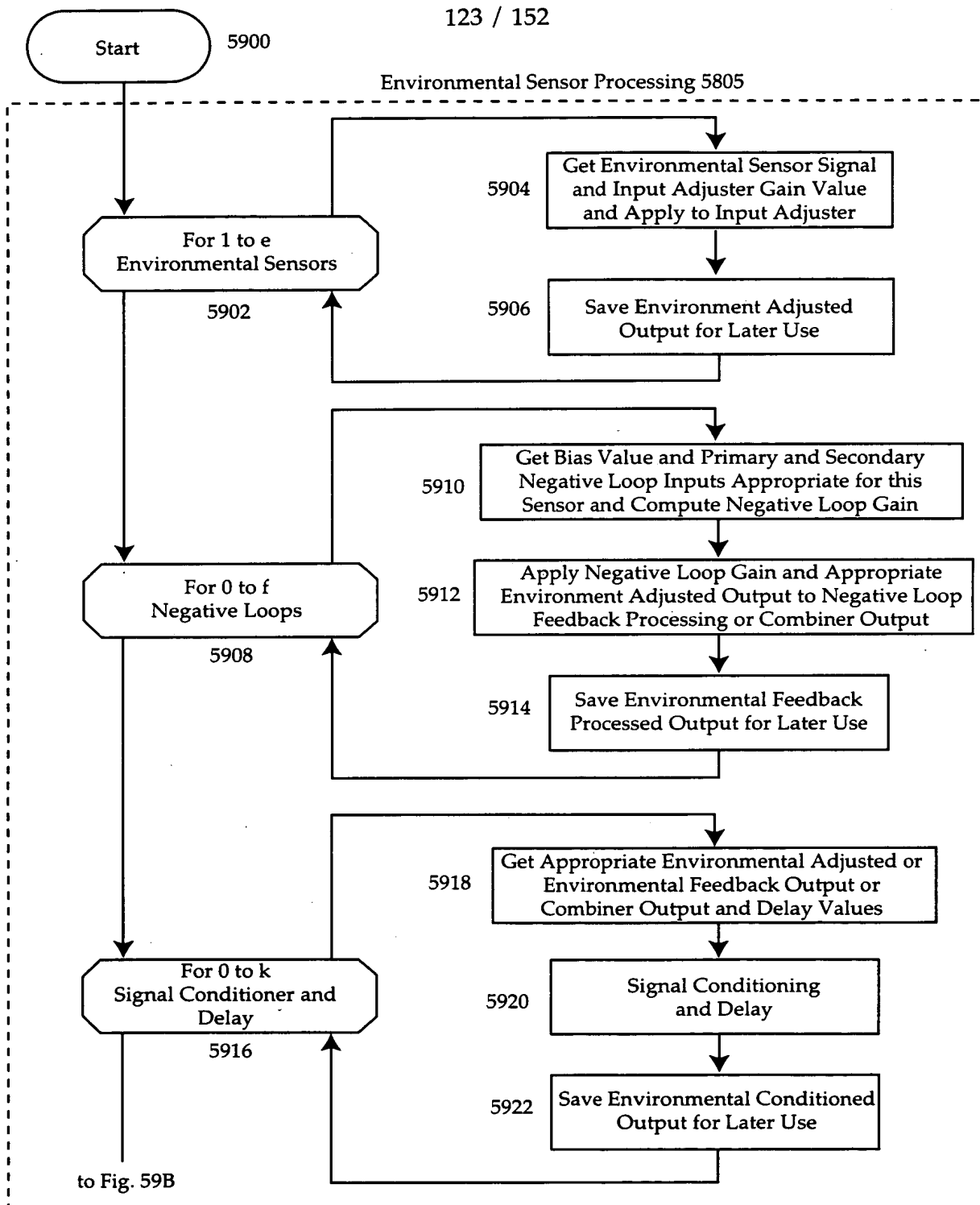


FIG. 59A

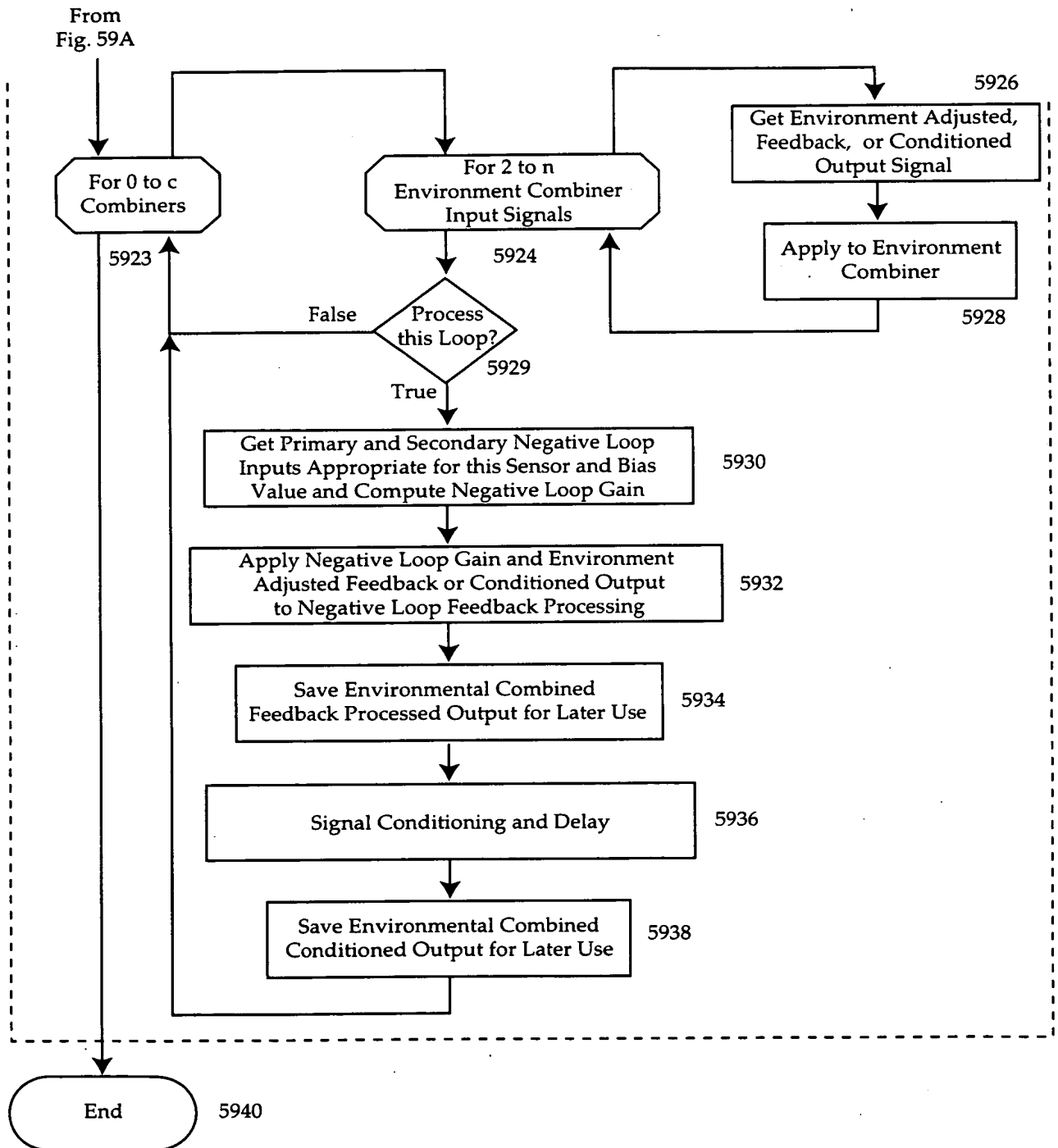
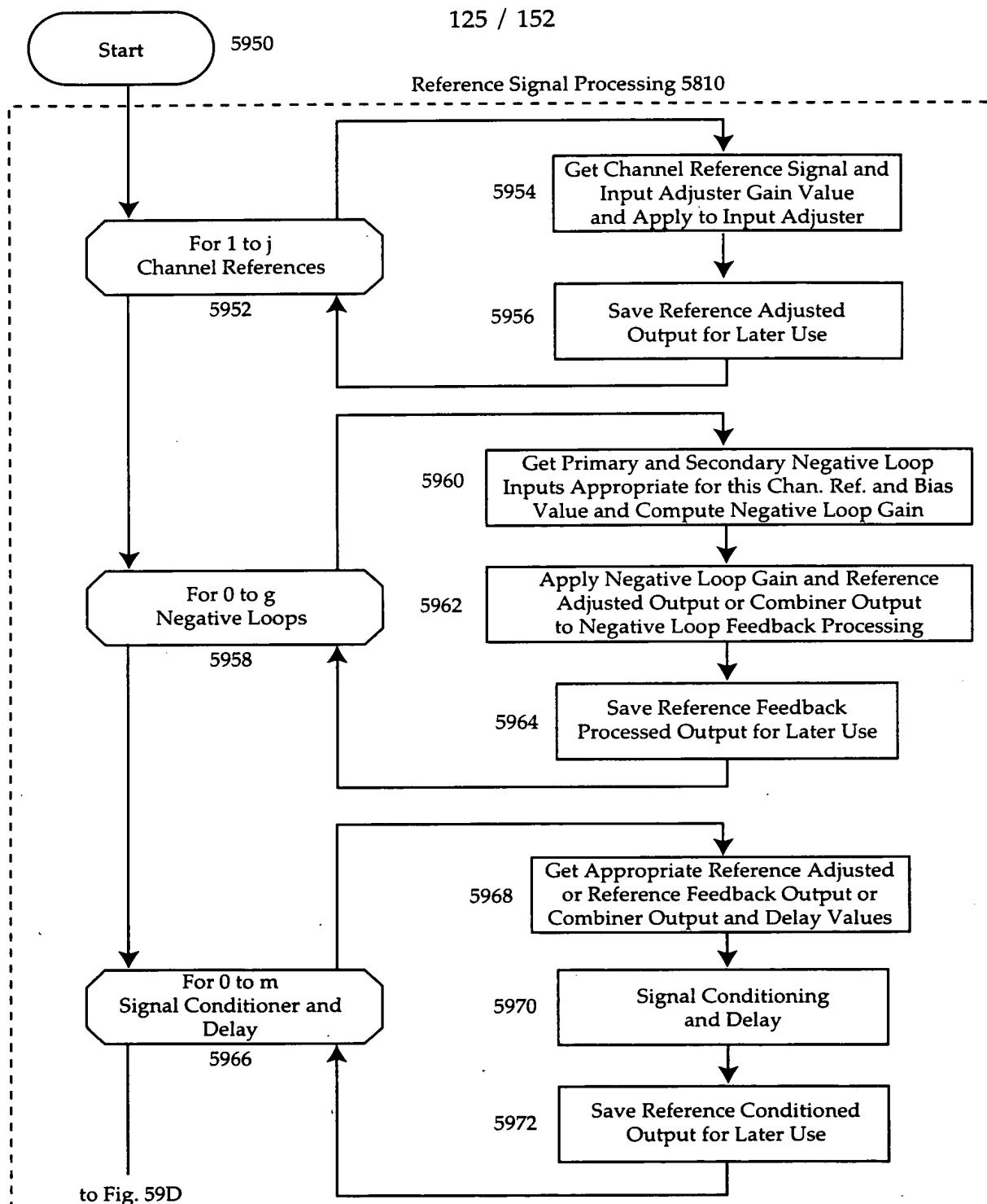


FIG. 59B

—



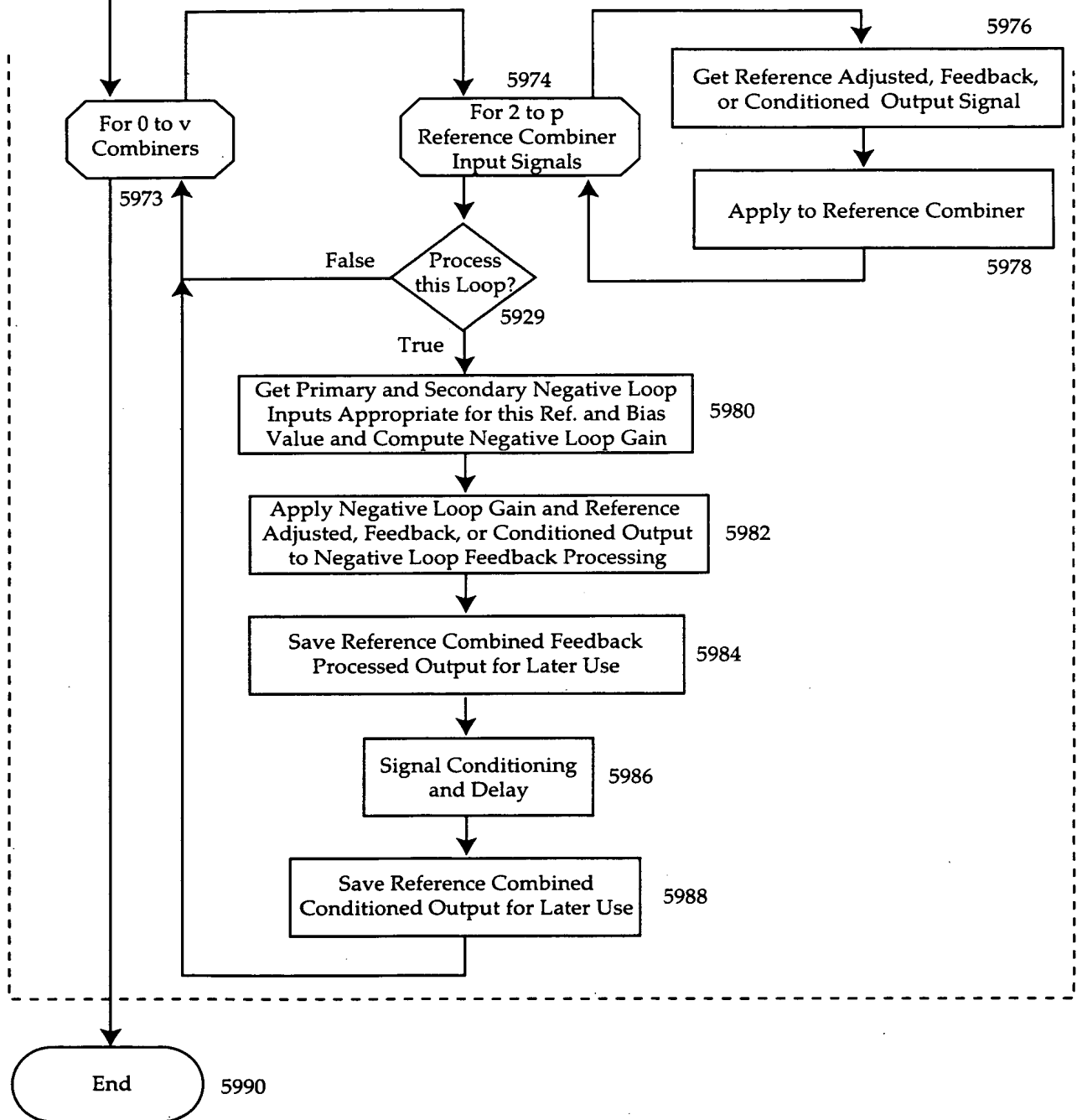
From
Fig. 59A

FIG. 59D

00627-5222/60

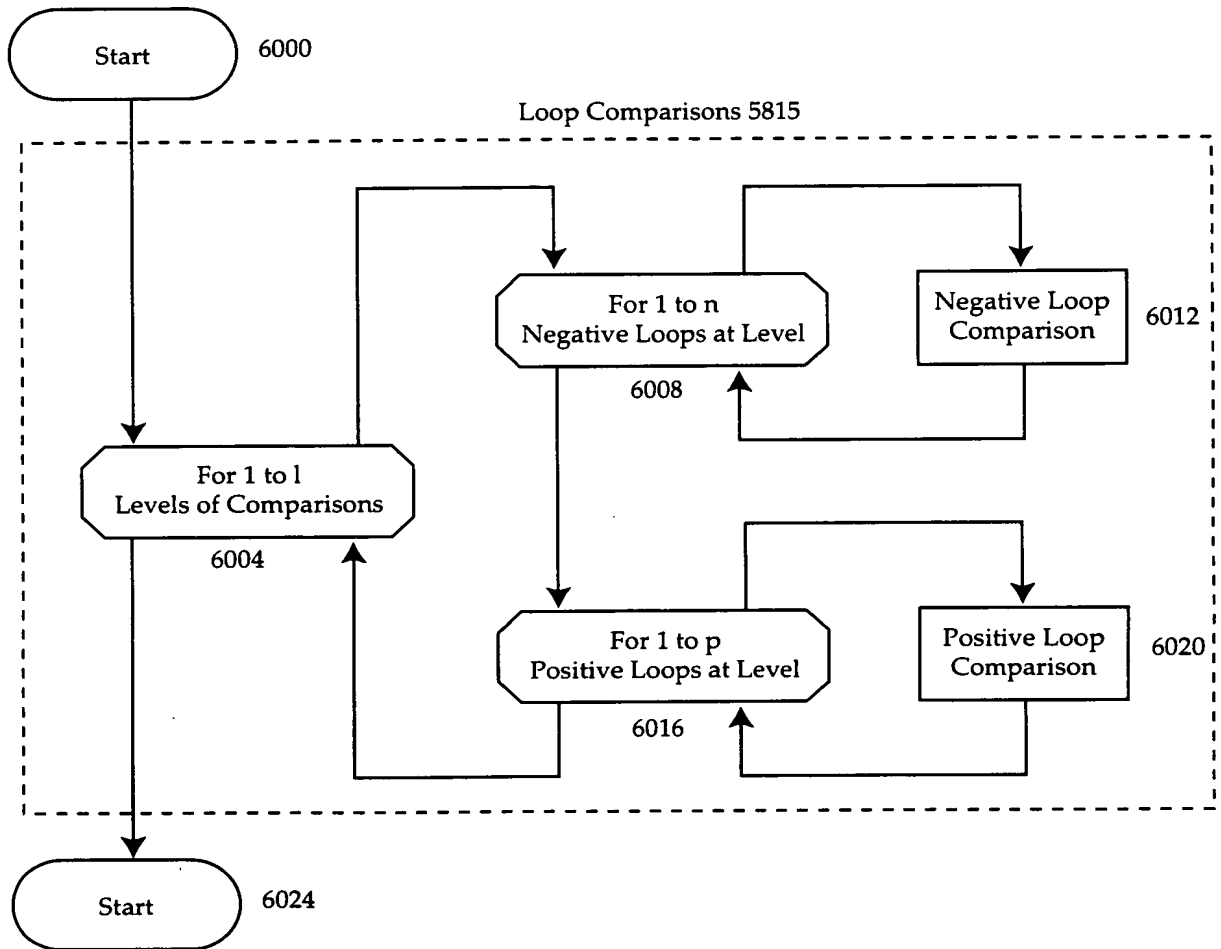


FIG. 60A

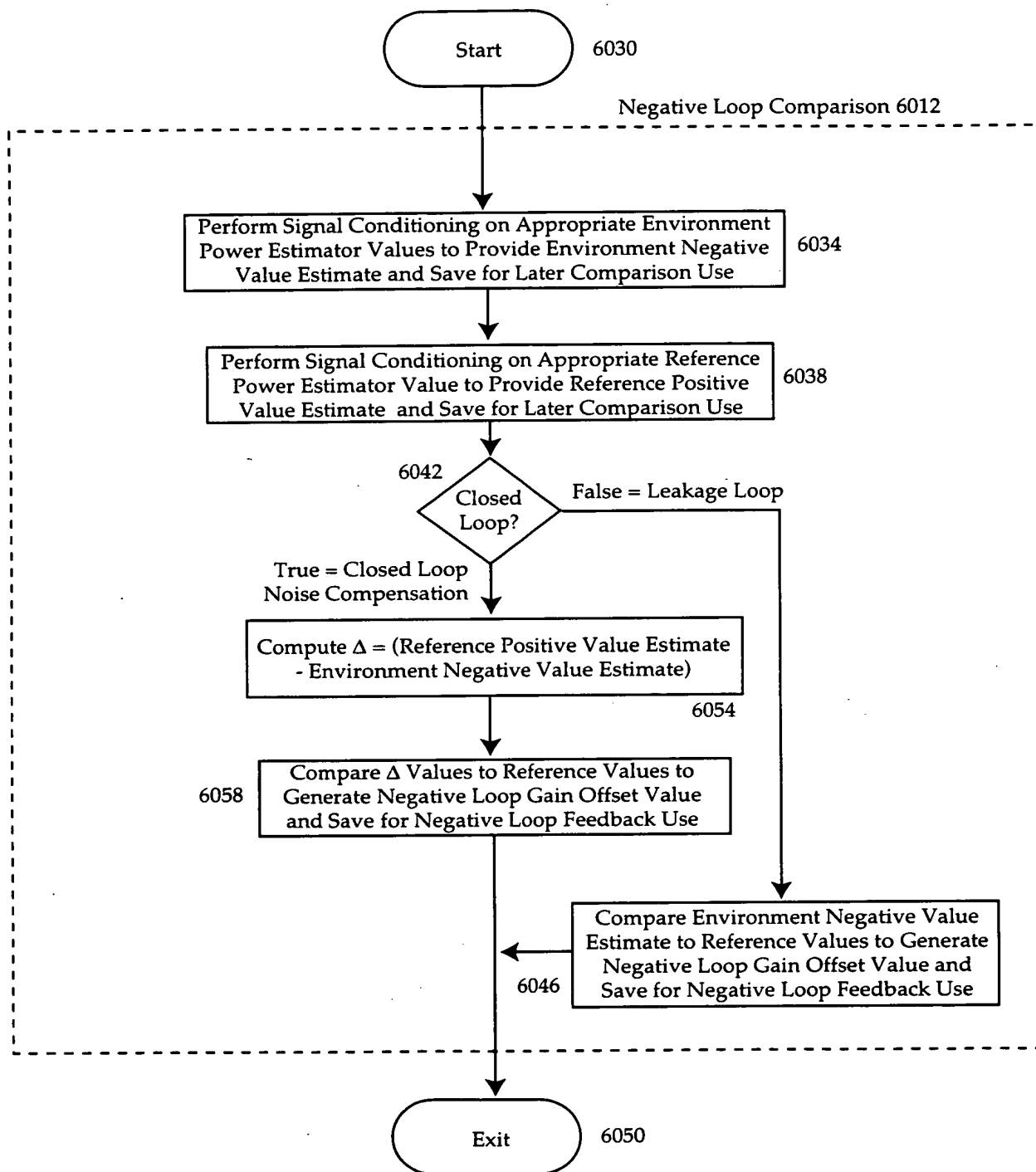


FIG. 60B

Perform Signal Conditioning on Appropriate Environment
Power Estimator Value to Provide Environment Positive
Value Estimate and Save for Later Comparison Use

6064

Perform Signal Conditioning on Appropriate Reference Power Estimator Value to Provide Reference Negative Value Estimate and Save for Later Comparison Use

6068

6072

Closed Loop?

False

True = Closed Loop
Noise Compensation

6080

Compute Δ = Environment Positive Value Estimate
- Reference Negative Value Estimate

6084

 $\Delta > 0?$

True

False

$\Delta = 0$
(No Noise)

6088

Exit

6076

FIG. 60C

[illegible]

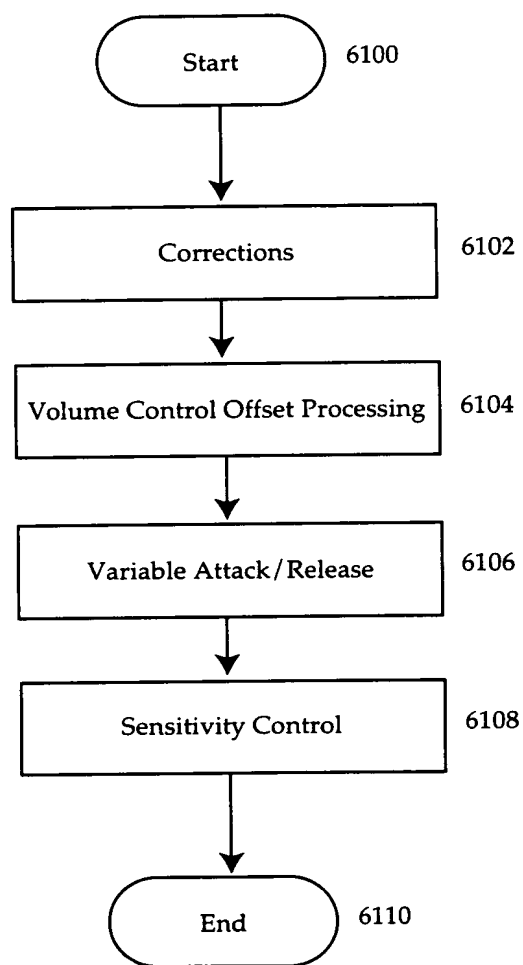


FIG. 61A

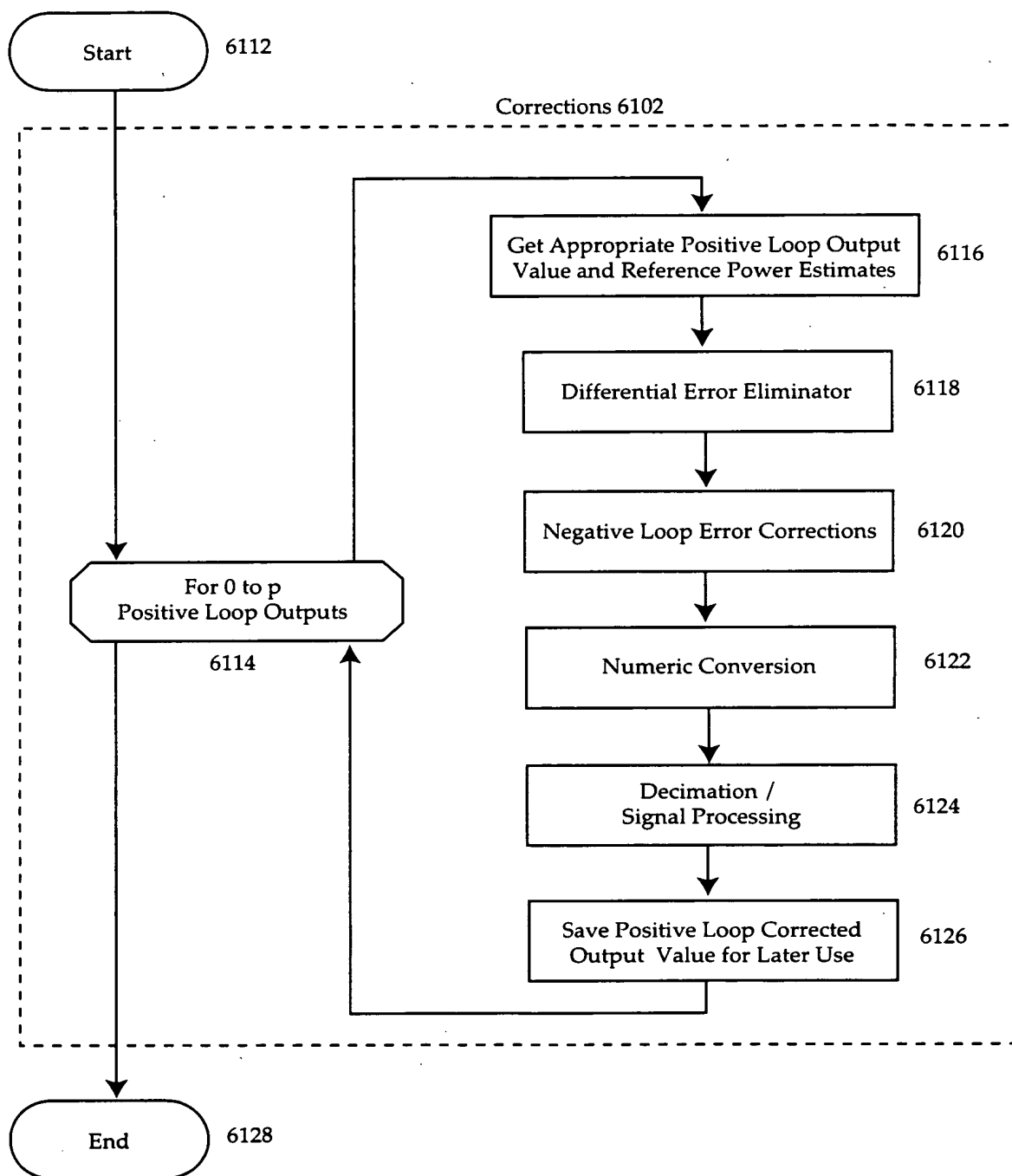


FIG. 61B

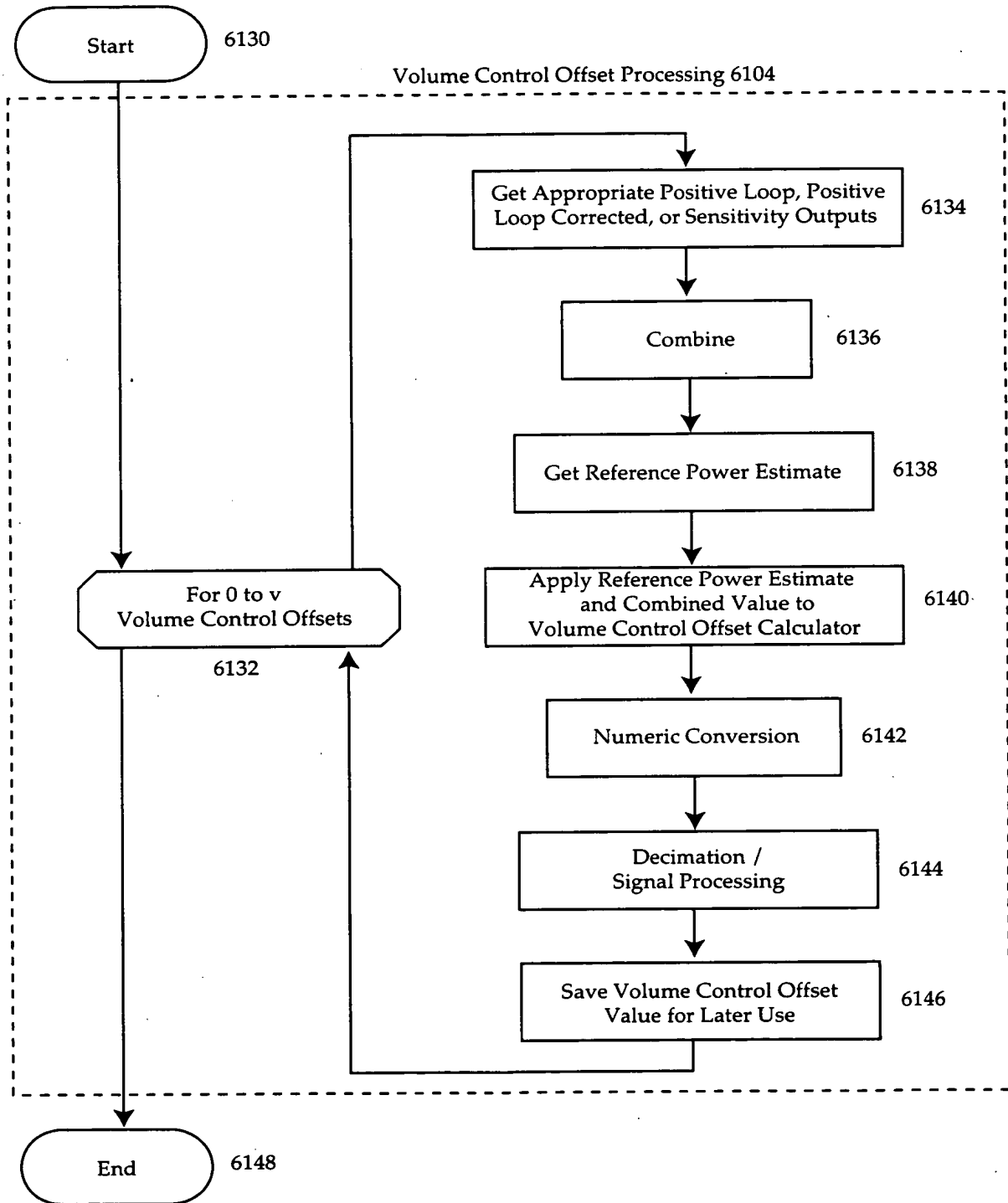


FIG. 61C

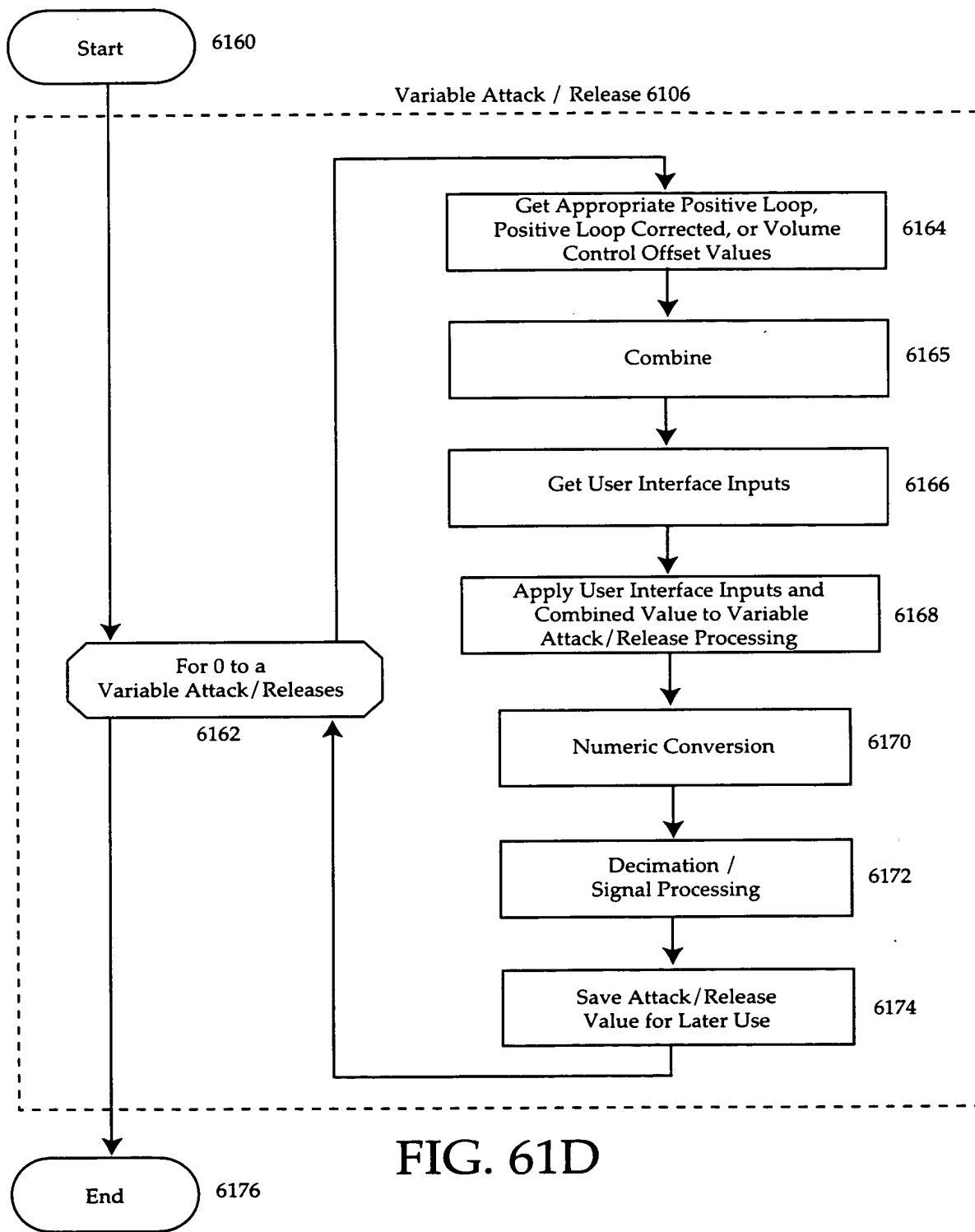


FIG. 61D

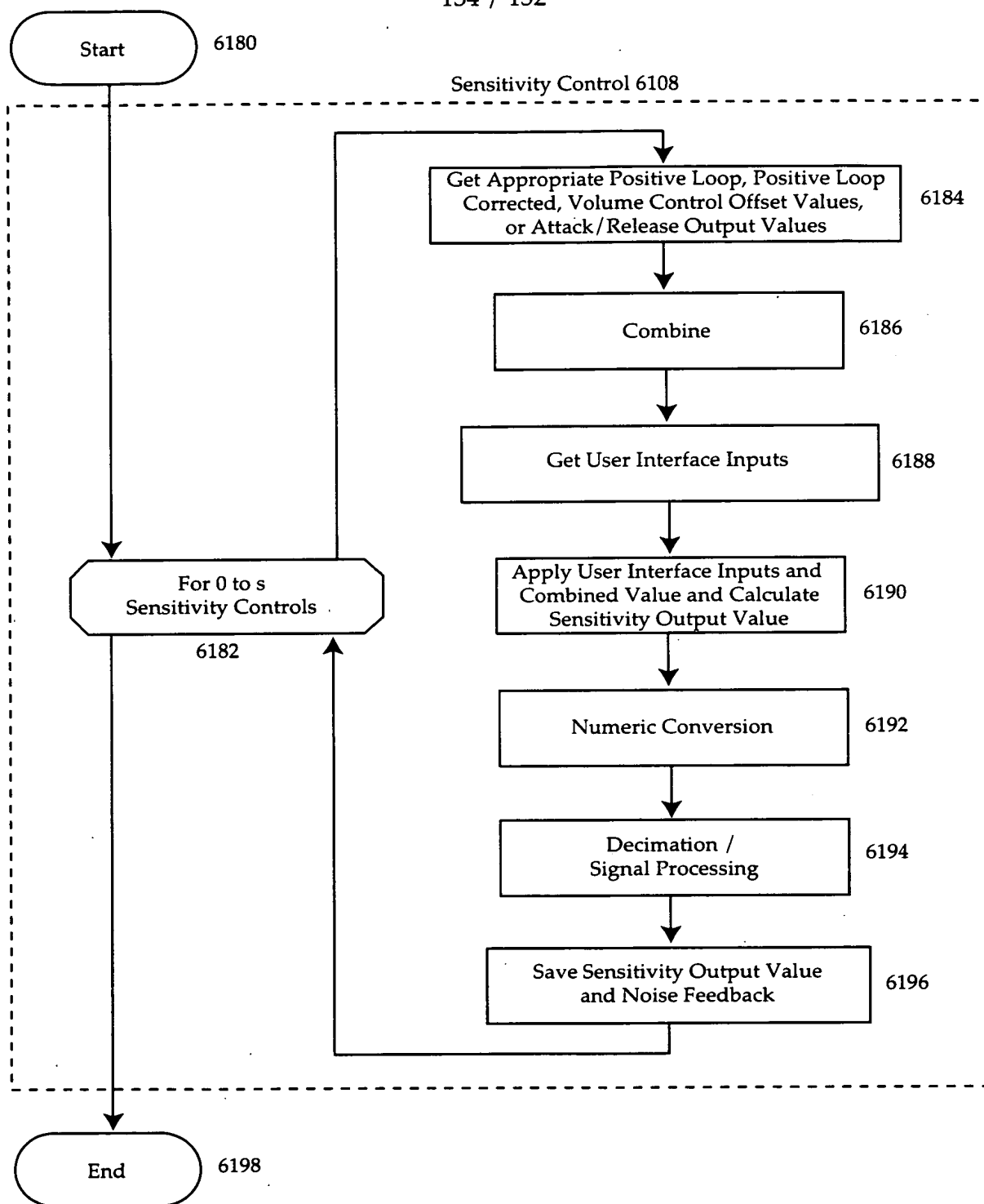


FIG. 61E

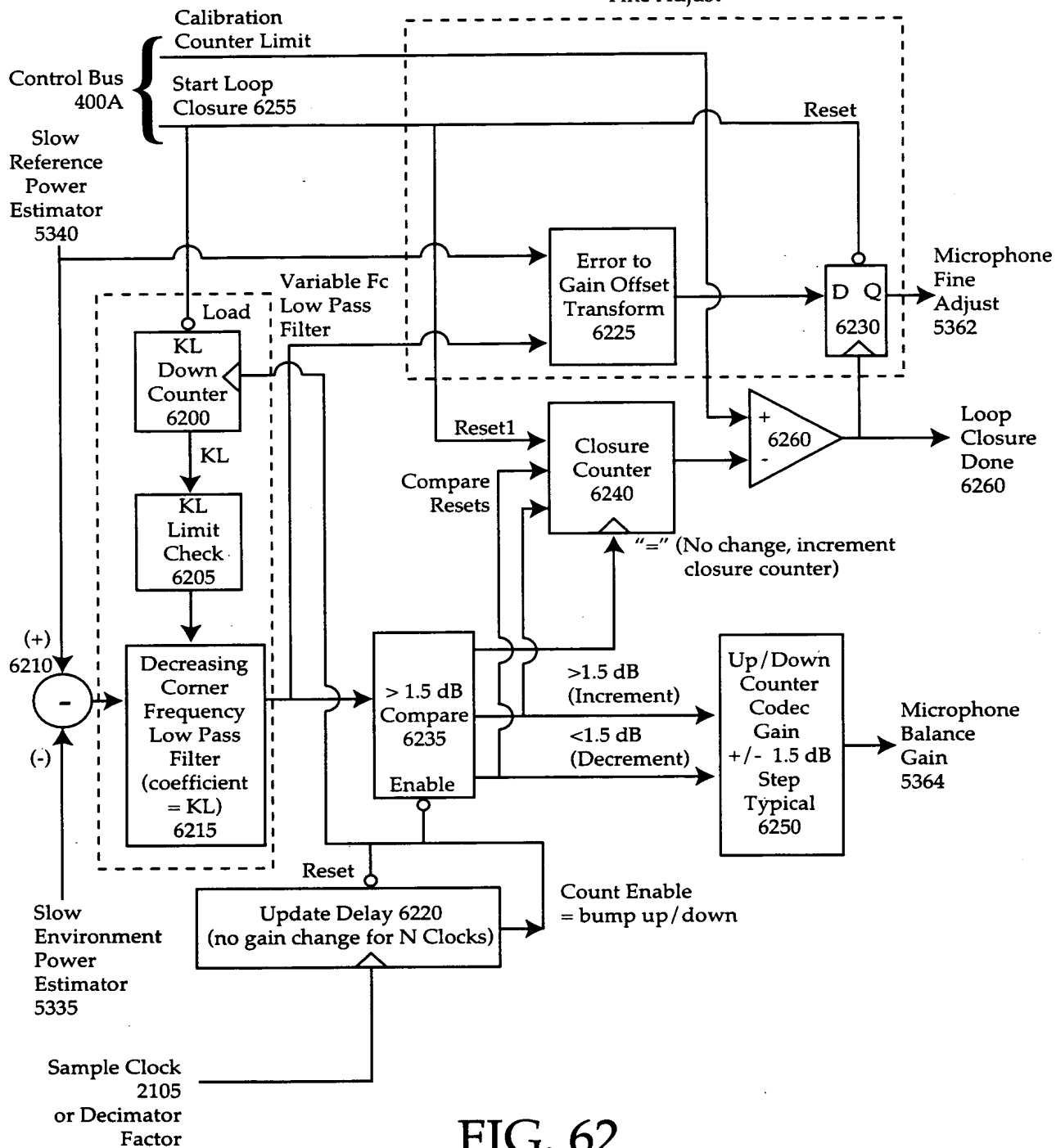


FIG. 62

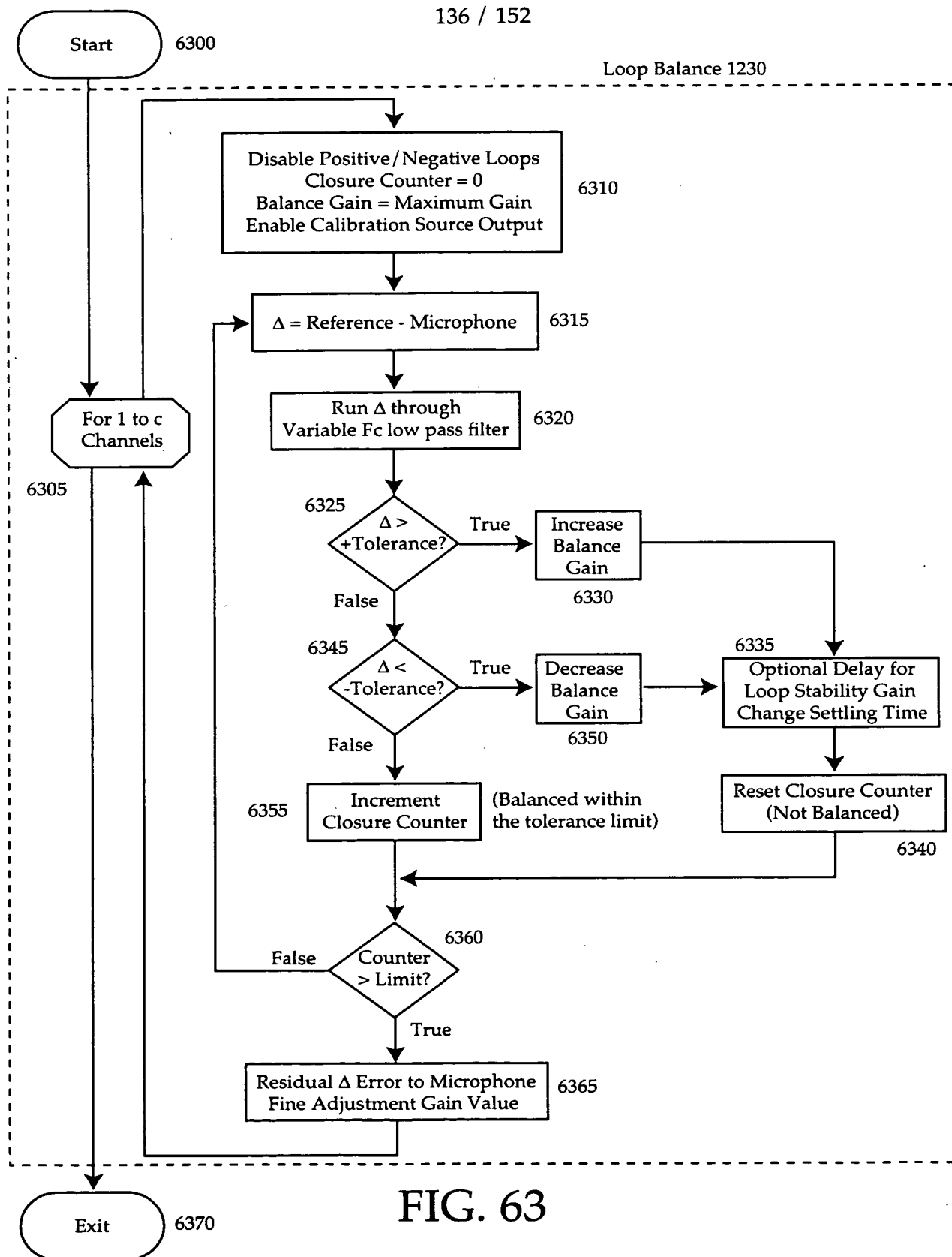


FIG. 63

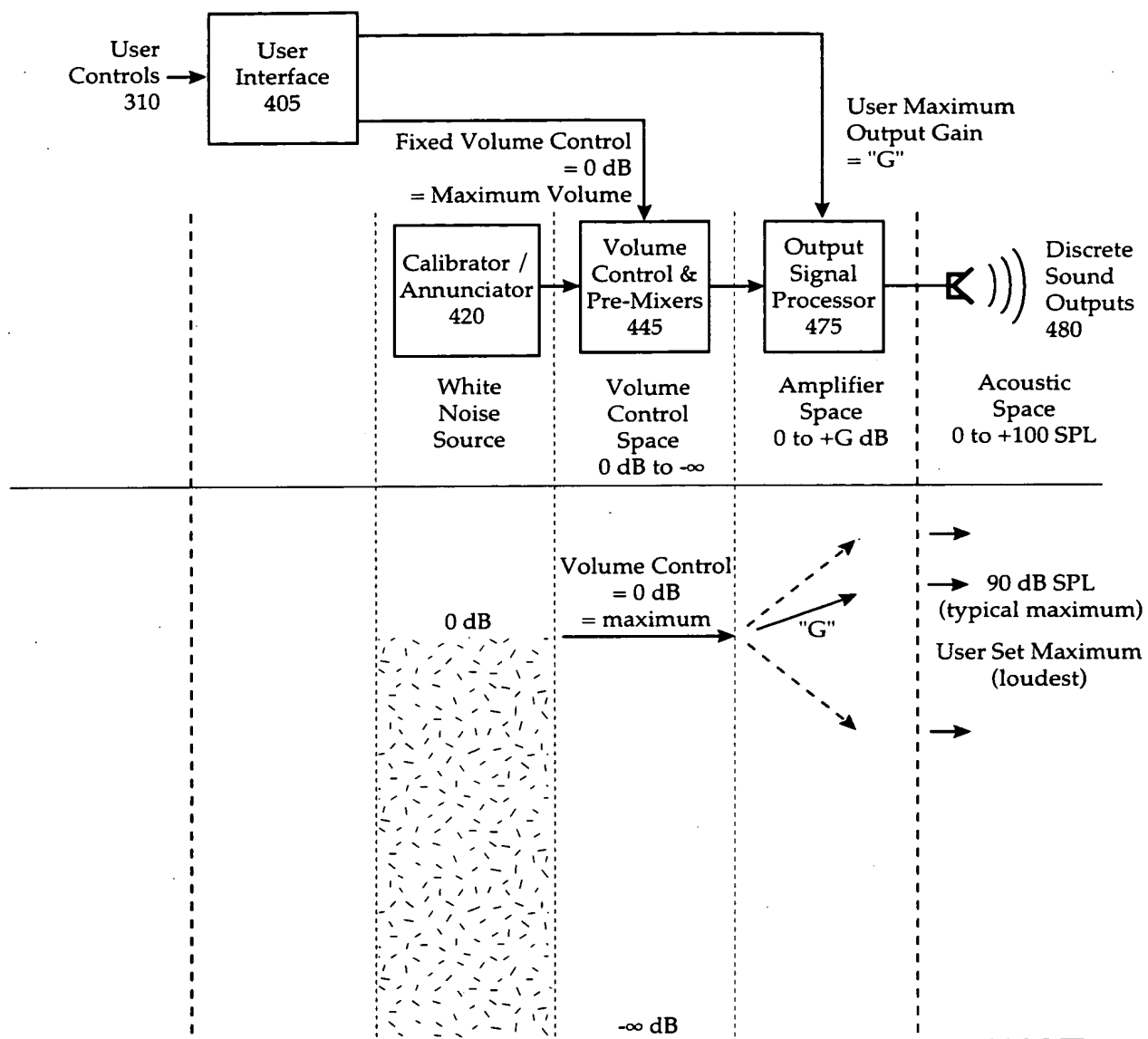


FIG. 64

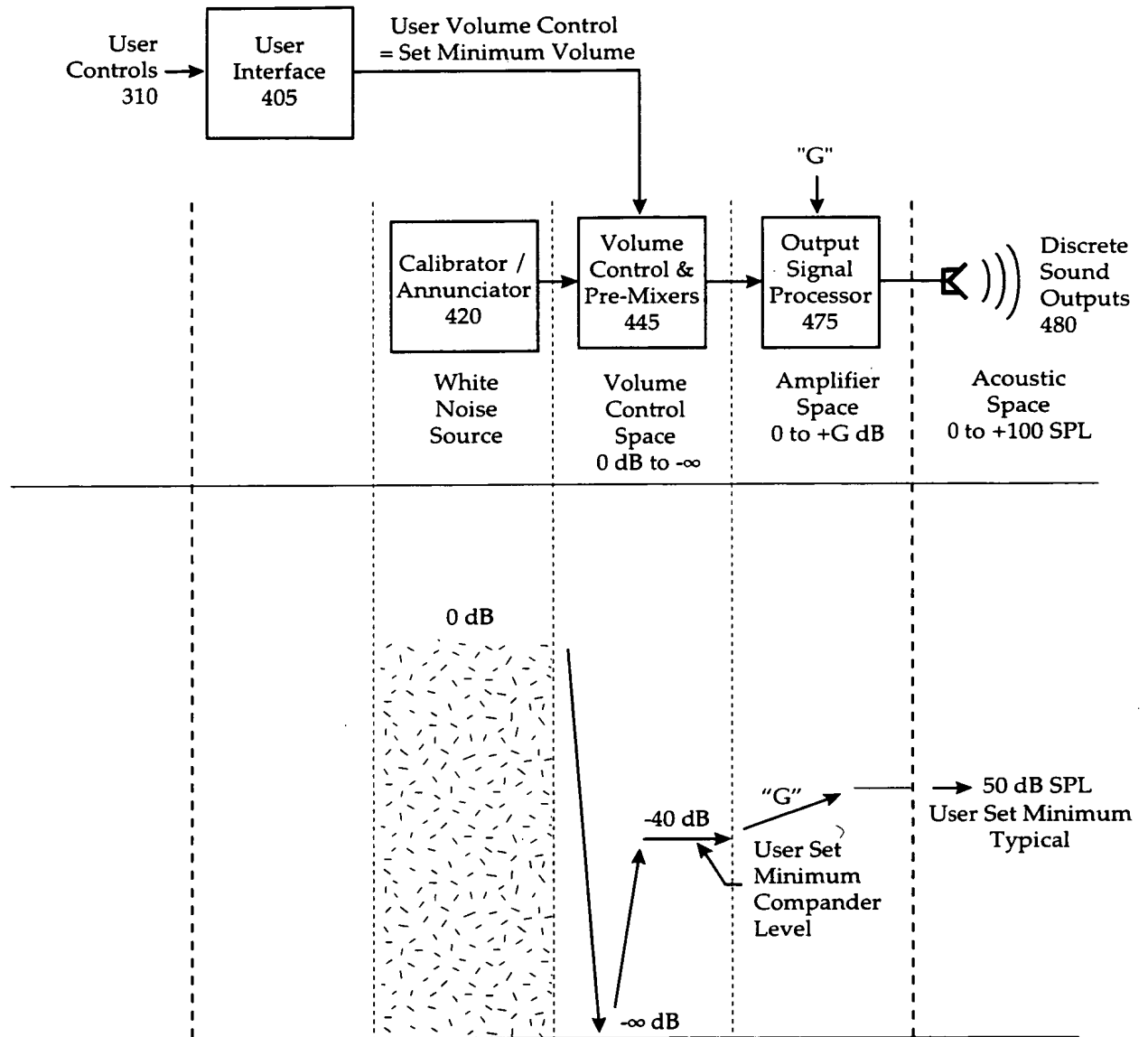


FIG. 65

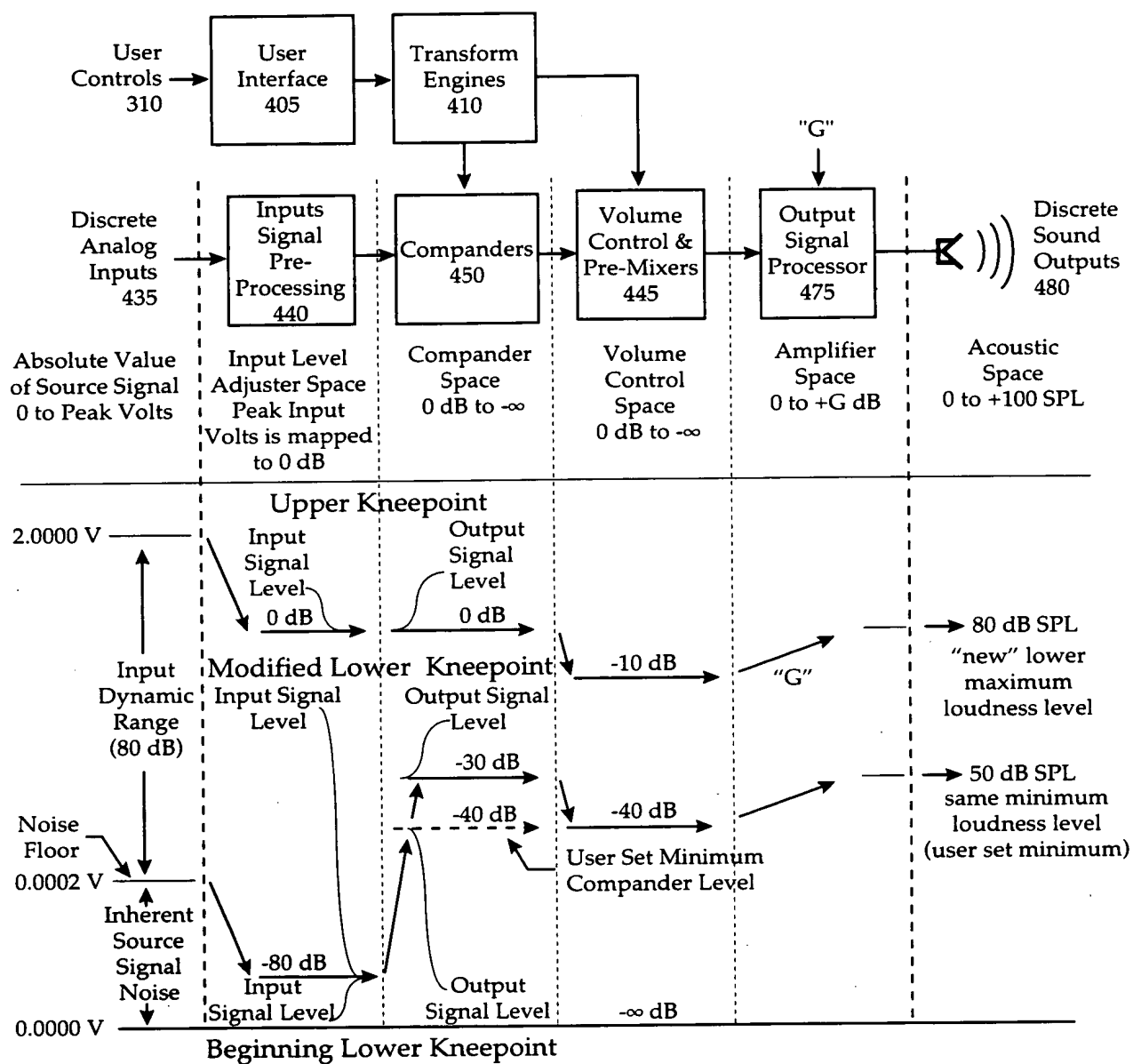


FIG. 66

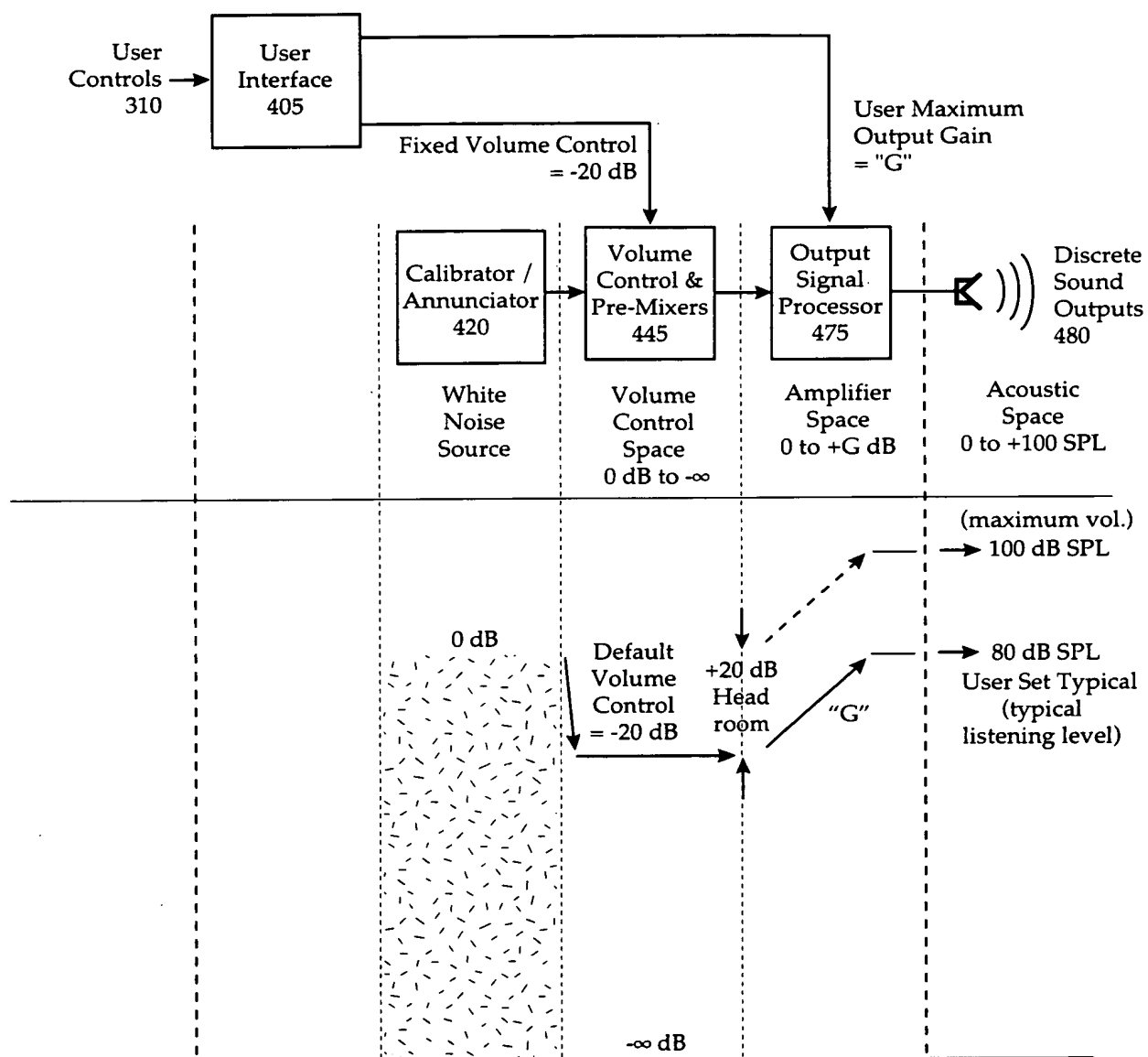


FIG. 67

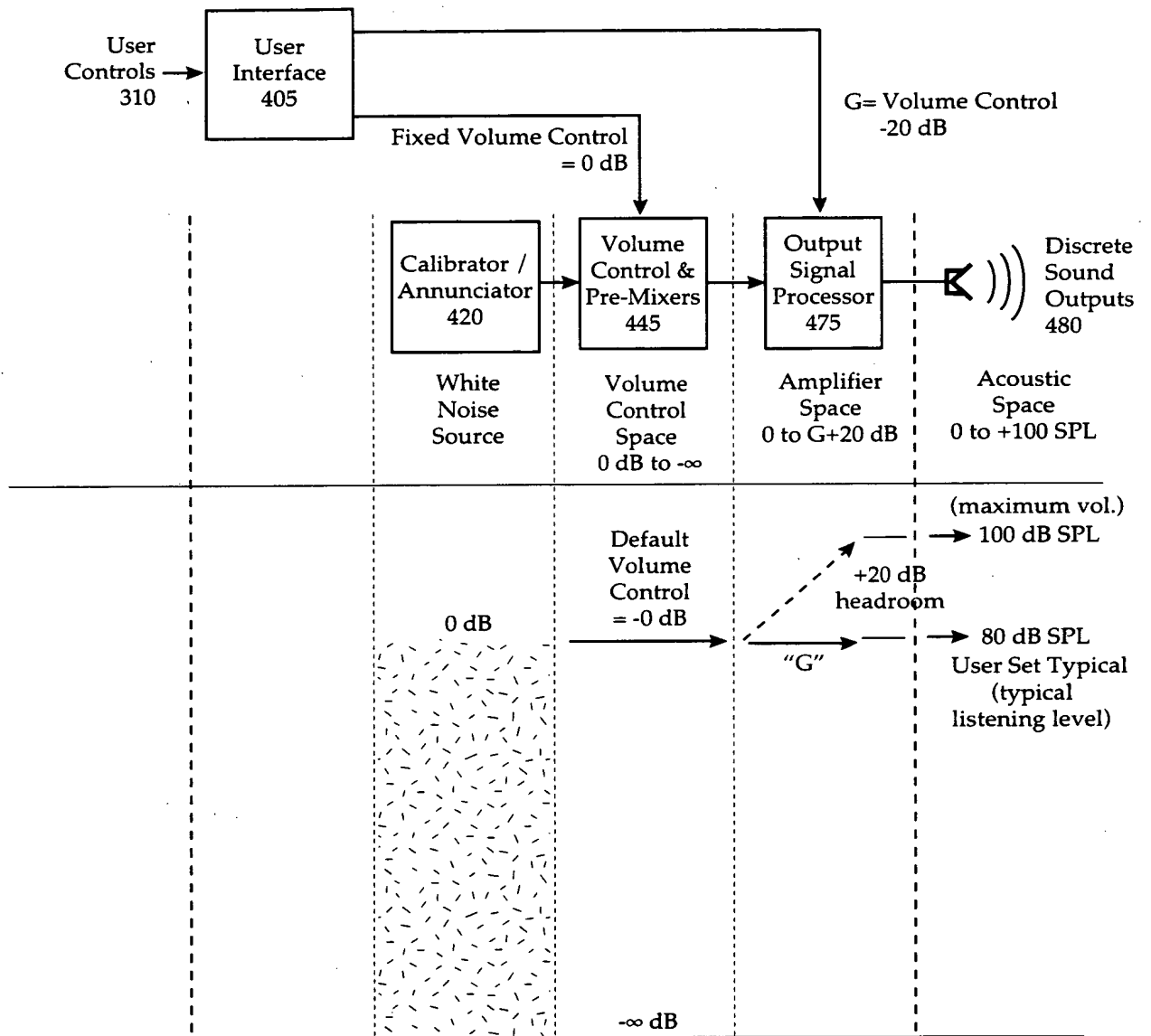


FIG. 68

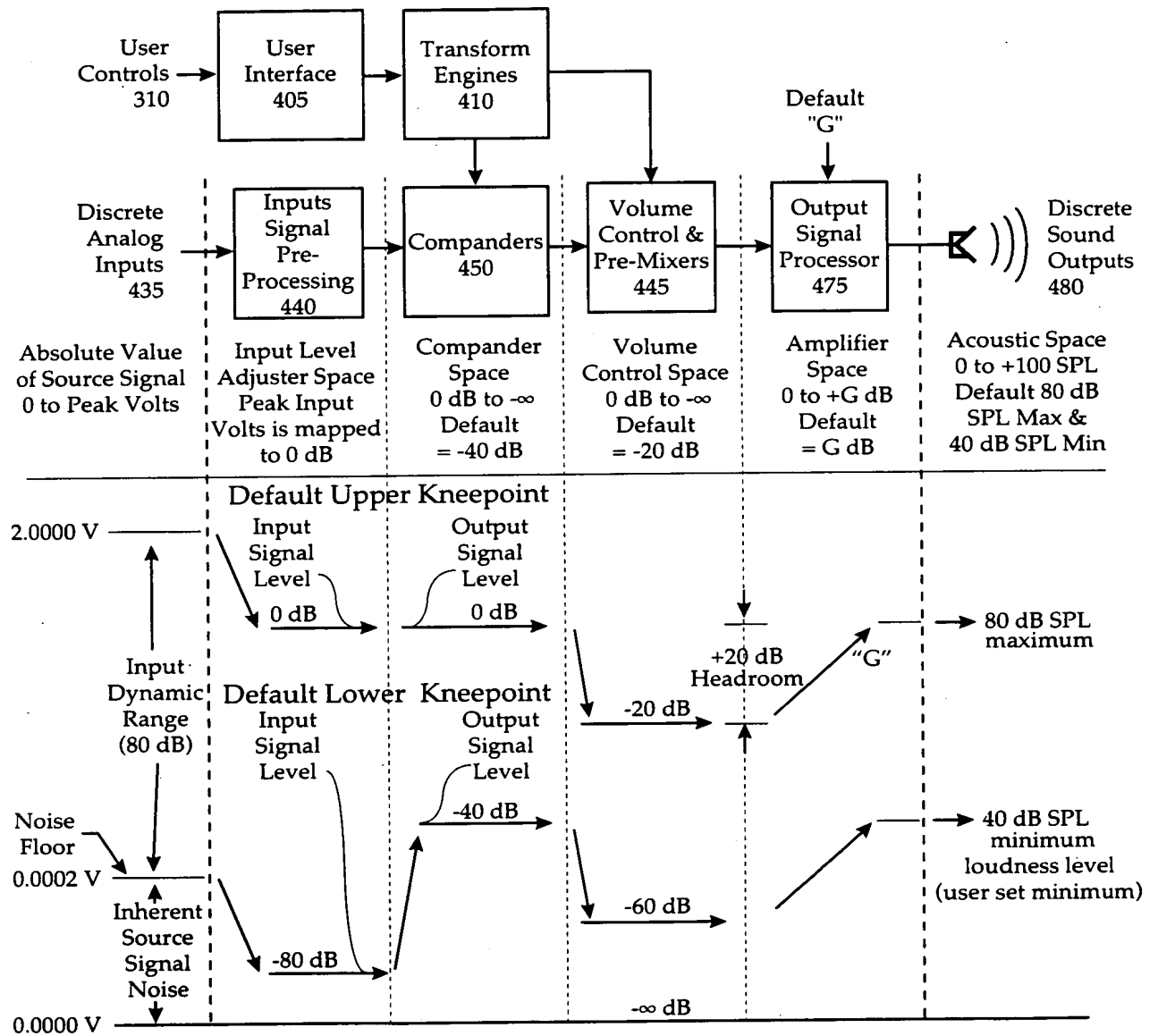


FIG. 69

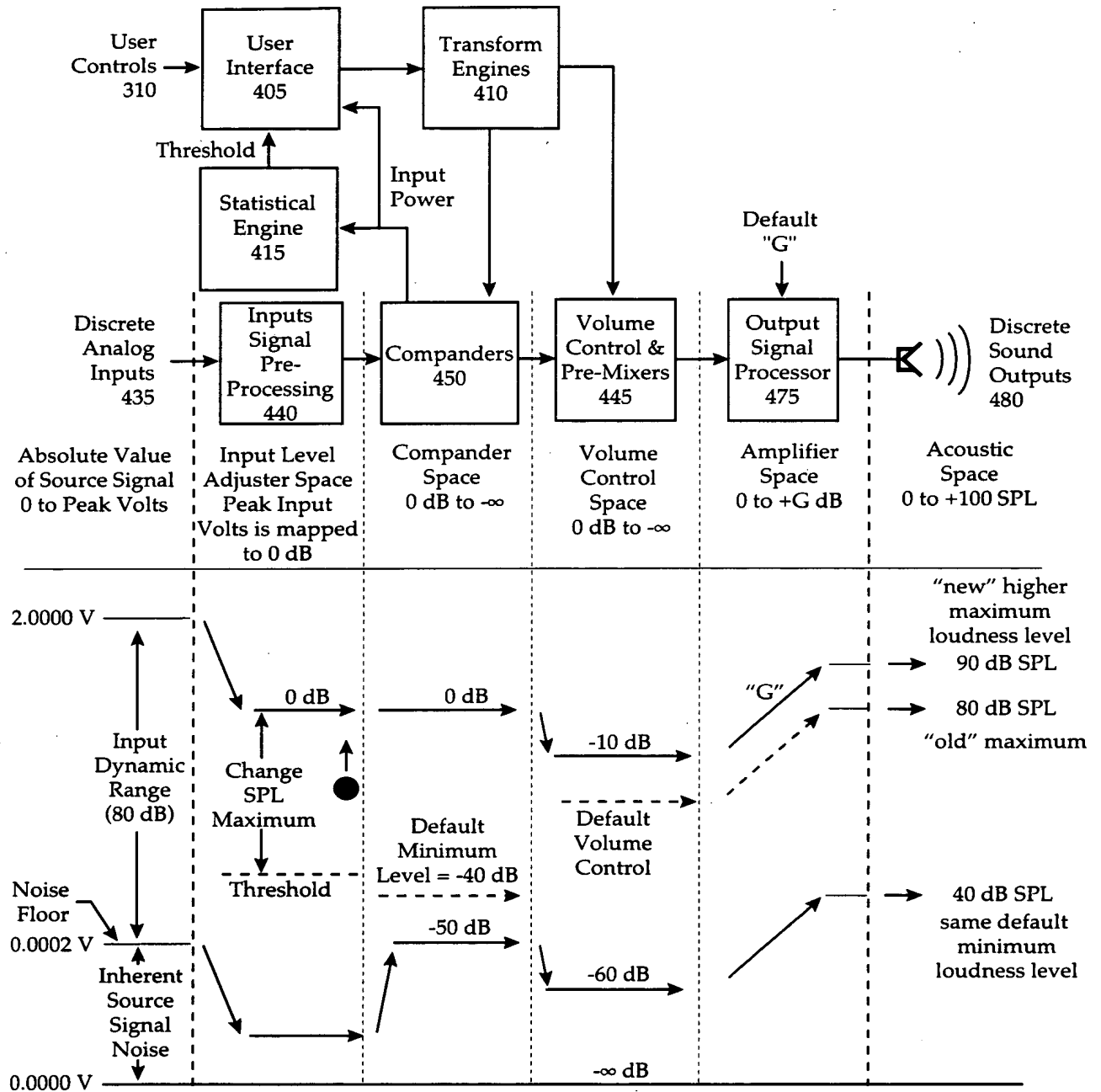


FIG. 70

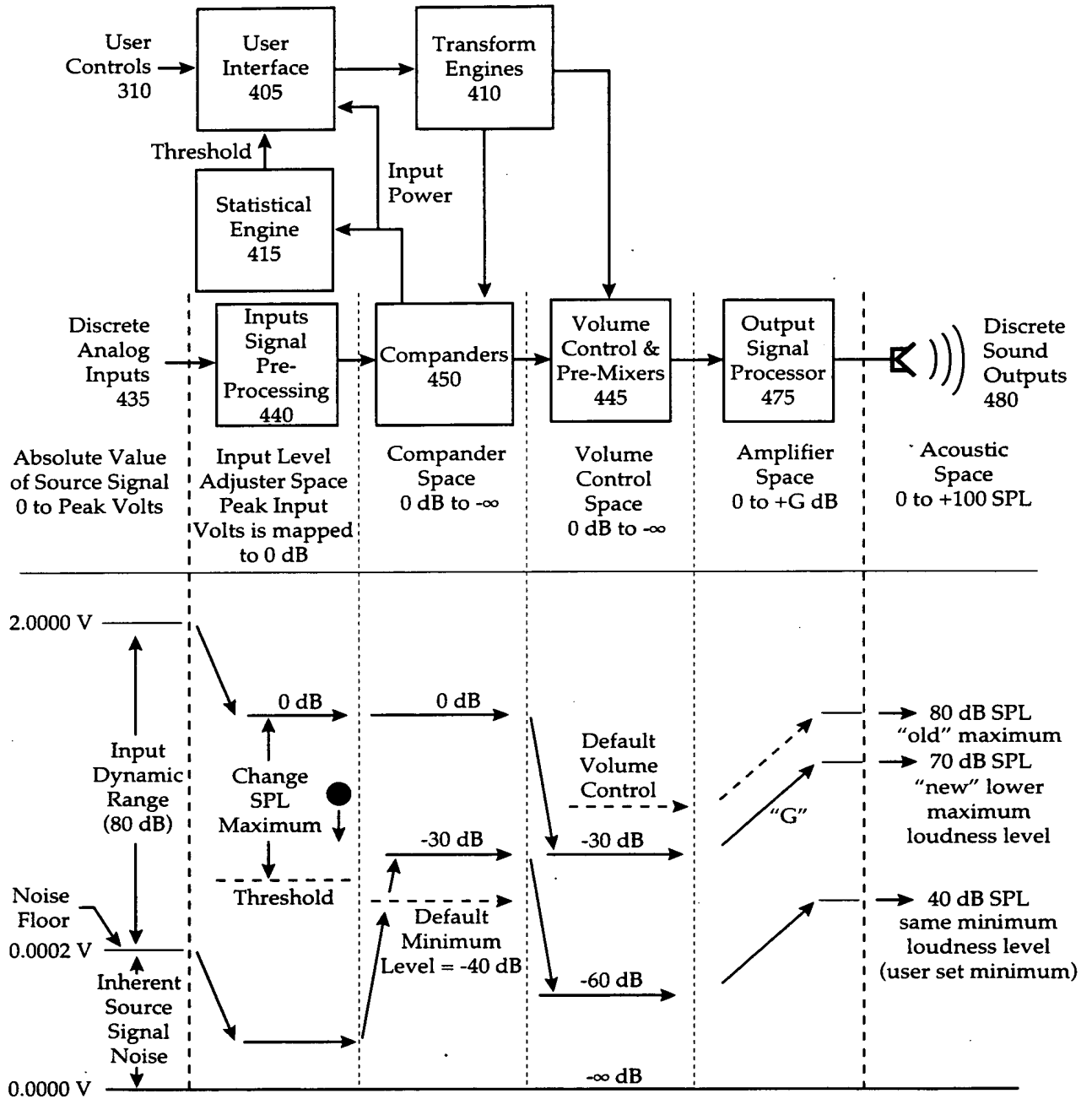


FIG. 71

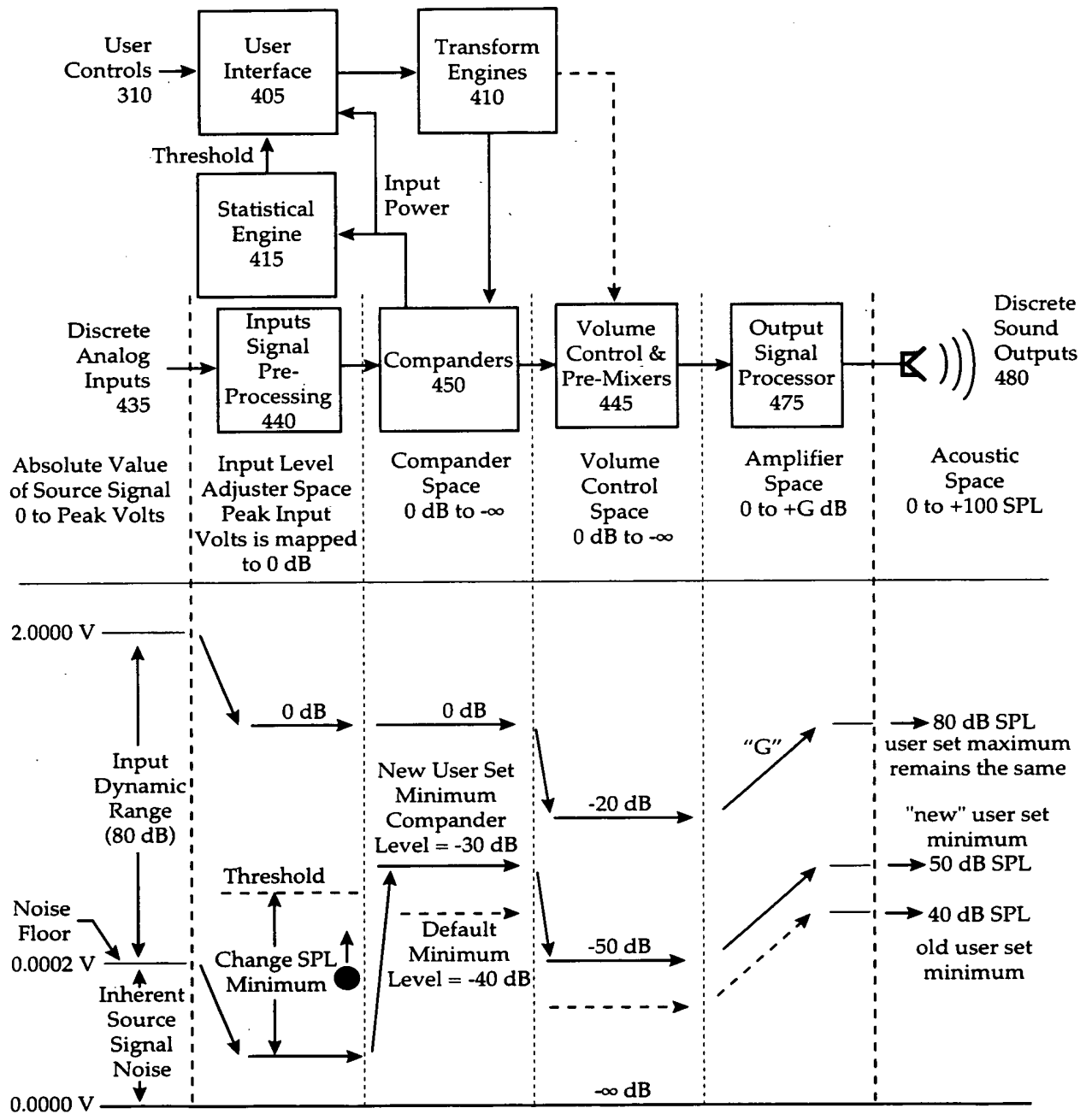


FIG. 72

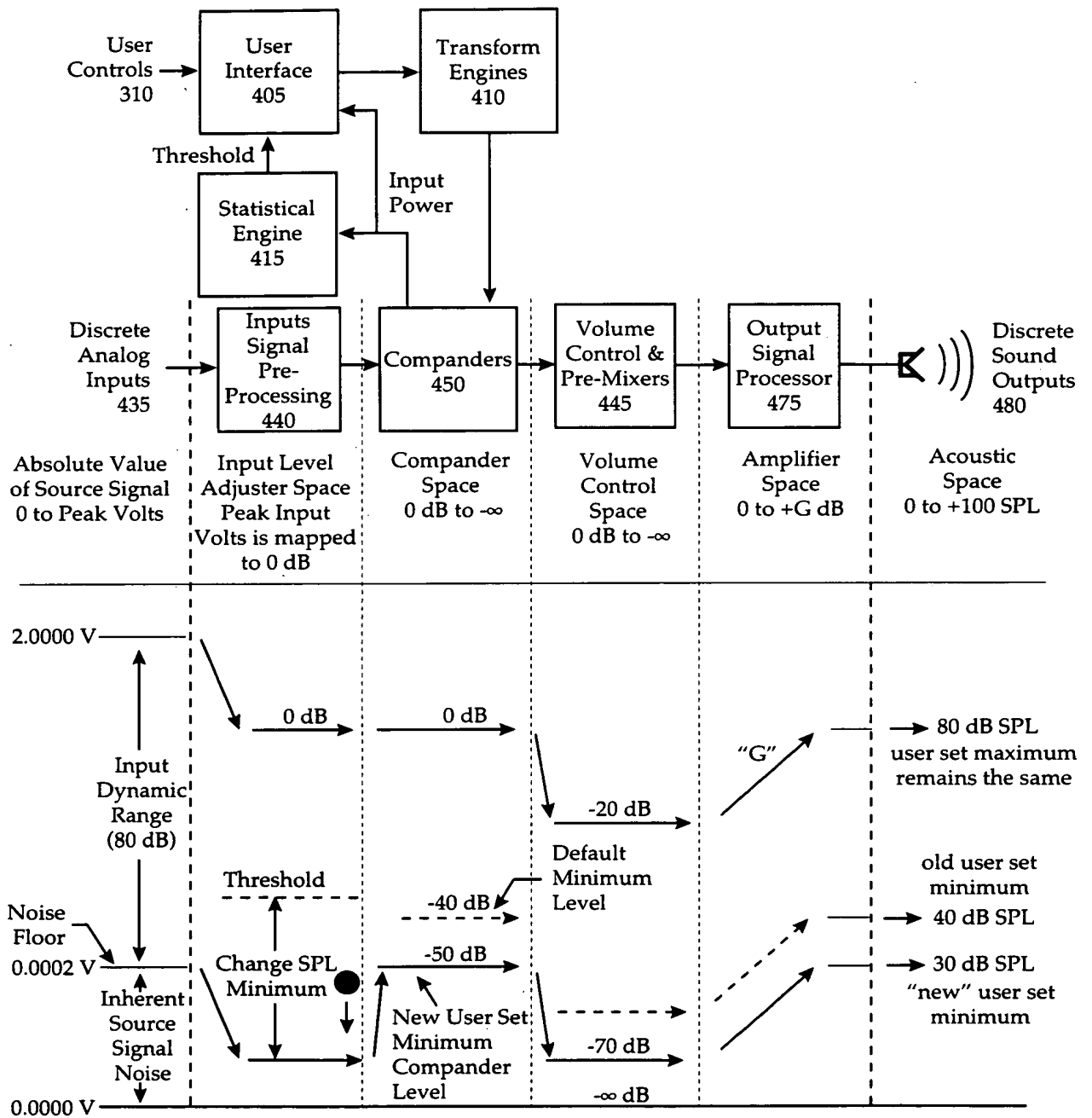


FIG. 73



FIG. 74

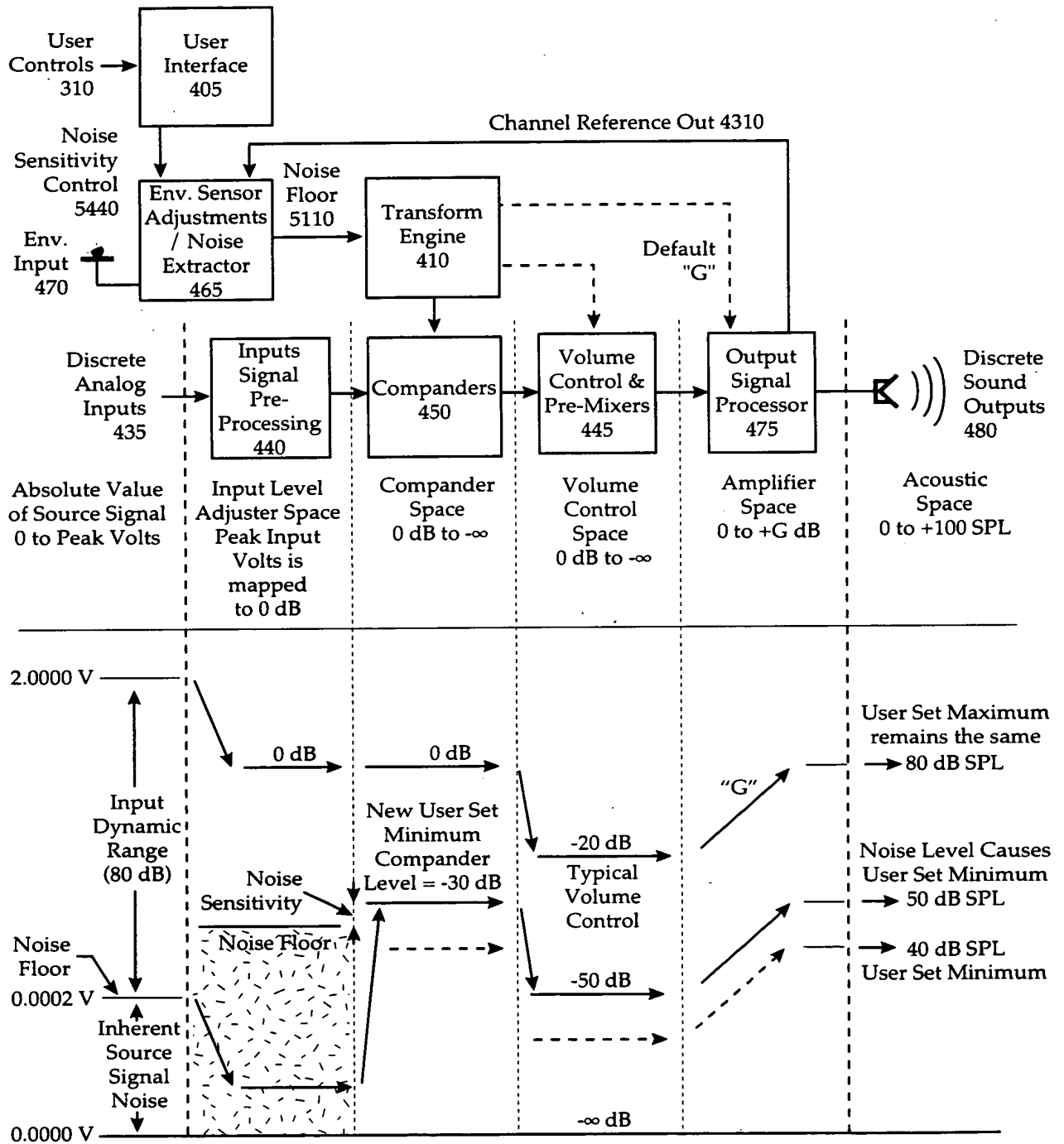


FIG. 75

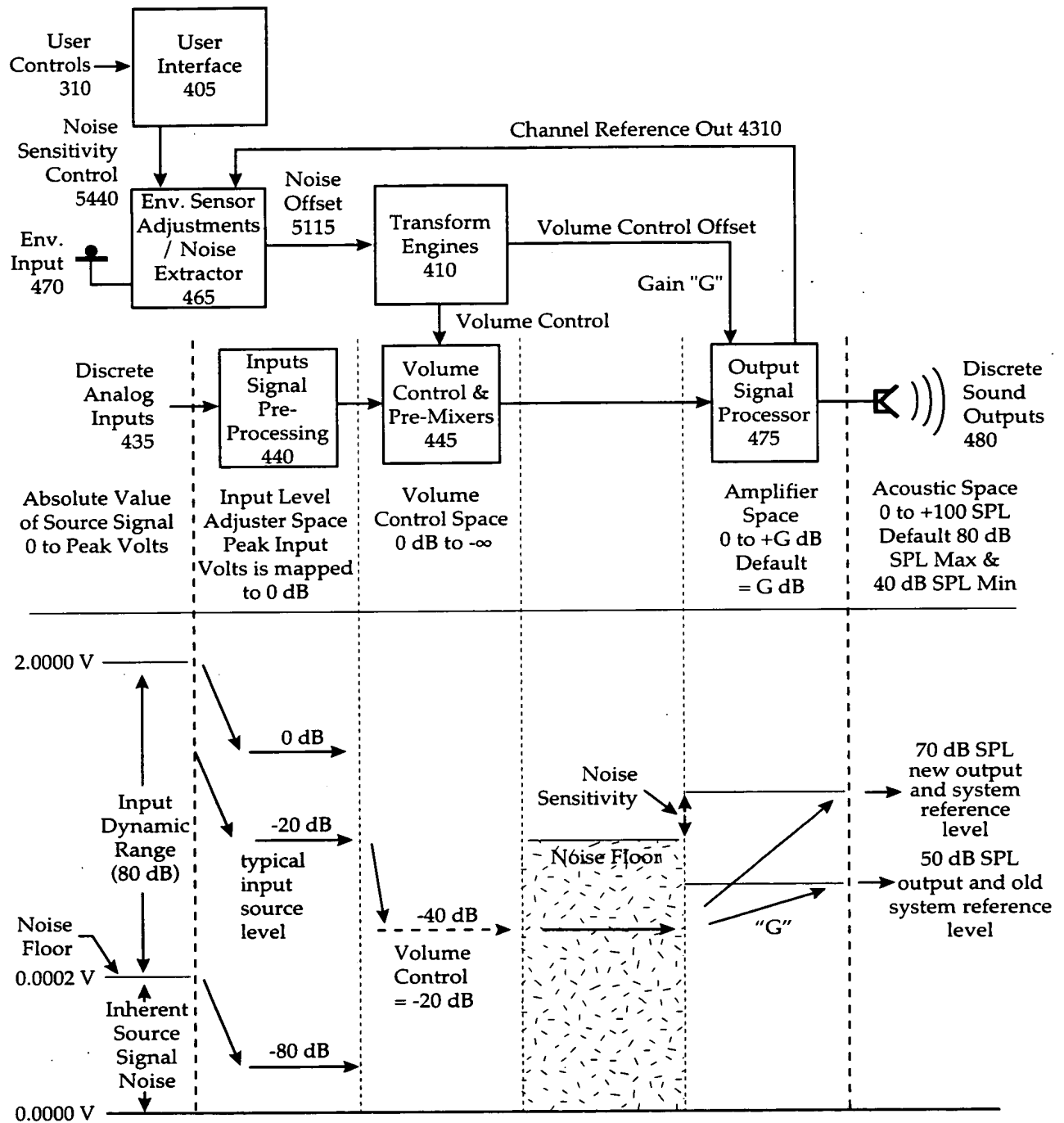


FIG. 76

Setup Command Decoder
and Setup Command Execute
660E and 660F

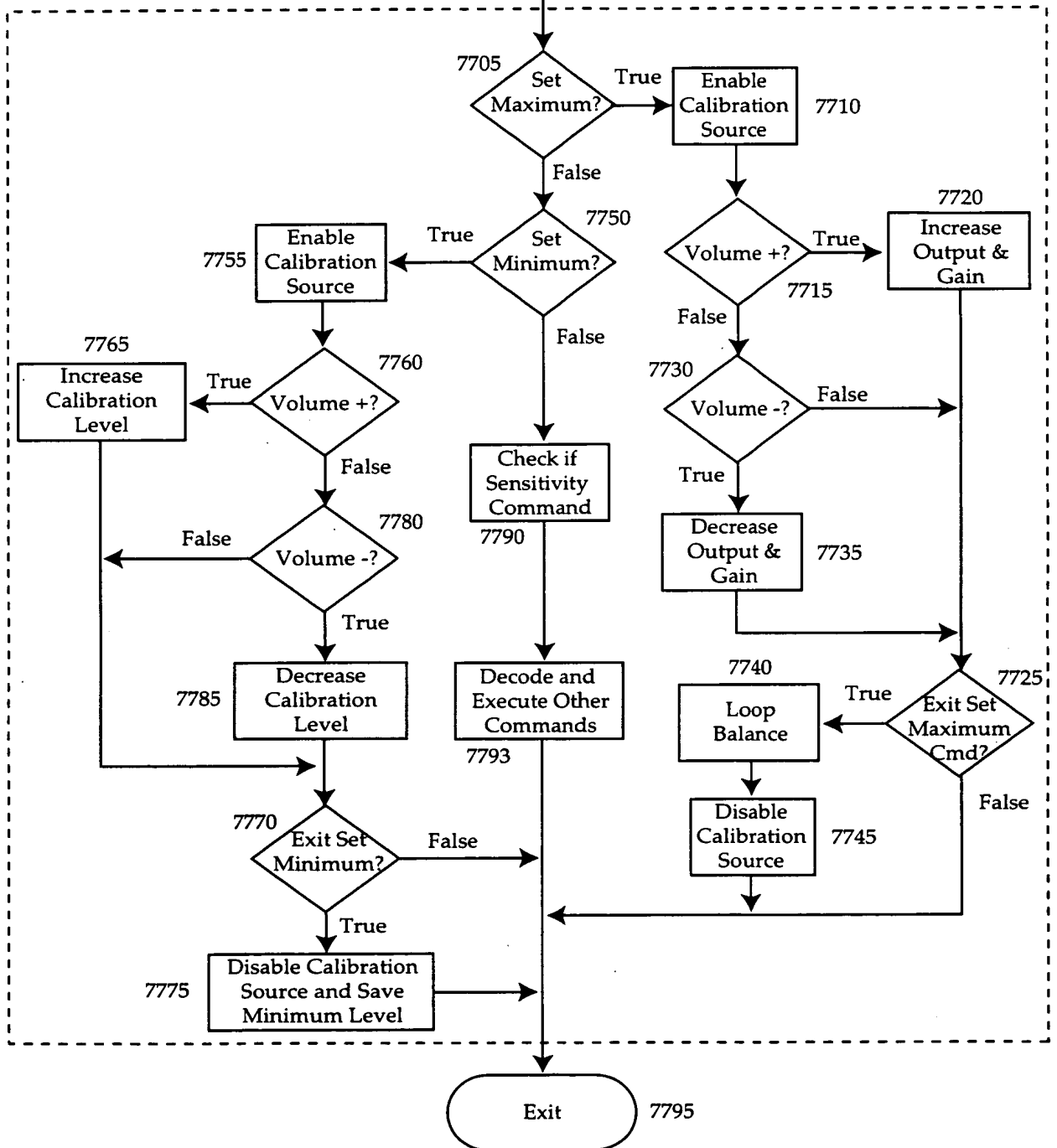


FIG. 77

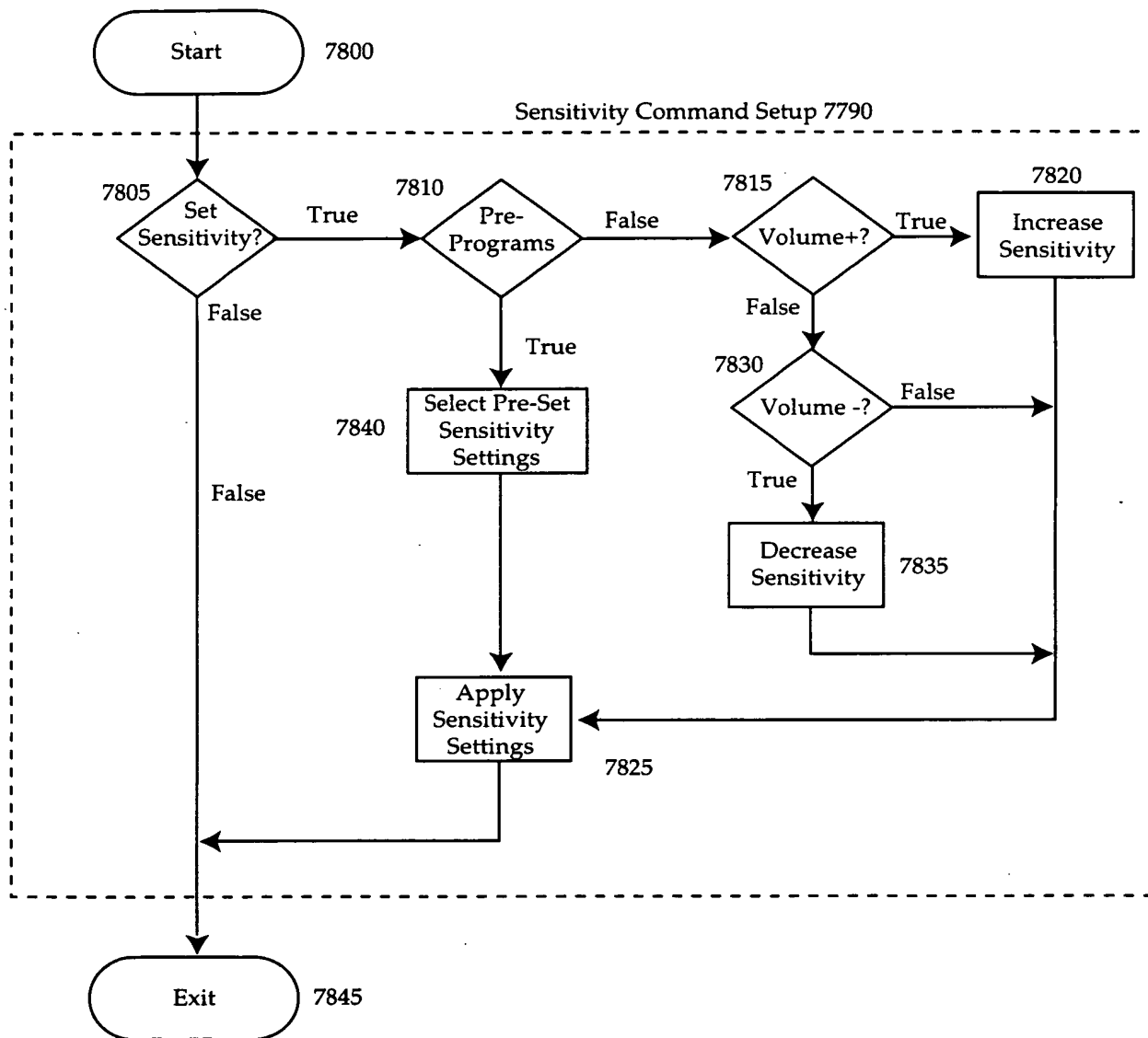


FIG. 78

Operation Command Decoder
and Operation Code Execute
660C and 660D

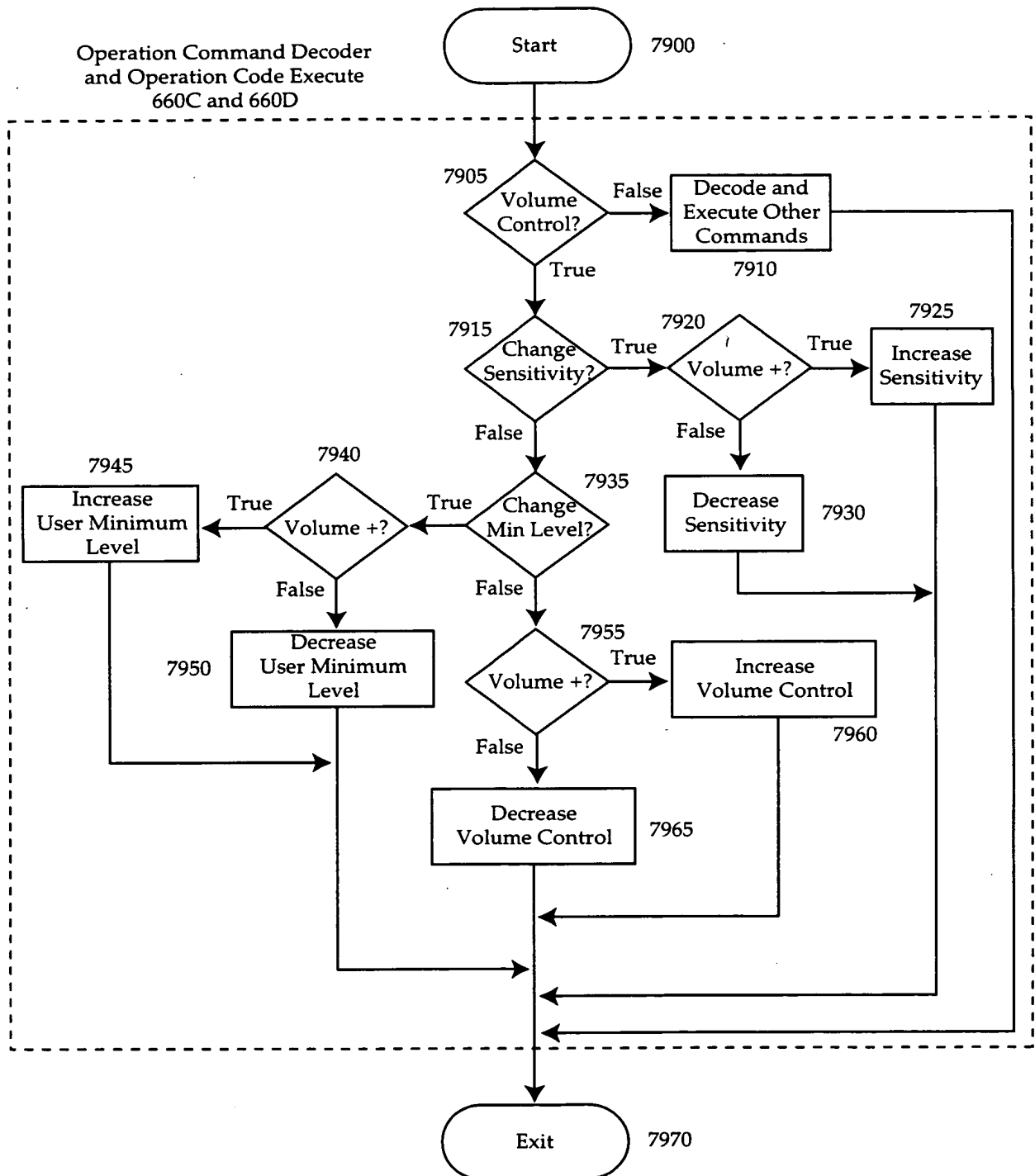


FIG. 79

006215T 5T82260